

Notes & Errata

4K RAM EXPANSION FOR DREAM 6800

(December 1980, 2/CC/57): Table 1, depicted below, was inadvertently omitted from the article.

\bar{E}	A10	A11	RAM 1	RAM 2	RAM 3	RAM 4
L	L	L	0000-03FF	_____	_____	_____
L	H	L	_____	0400-07FF	_____	_____
L	L	H	_____	_____	0800-0BFF	_____
L	H	H	_____	_____	_____	0C00-0FFF
H	X	X	NOT SELECTED			

ACOUSTICALLY-COUPLED MODEM

(September 1980, File No. 2/CC/53): The capacitor setting the free running fre-

quency of the PLL, IC7, which is connected to pin 9 should be .033uF as mentioned in the text rather than .0033uF shown on the circuit diagram, PC overlay and parts list.

SELECTALOTT (December 1980, 3/EG/19): The orientation of IC1, as shown on the overlay diagram on page 55 is wrong. IC1 should be shown the other way around, ie pins 16 and 8 swapped. The circuit diagram is correct.

LOW VOLTAGE CHASER (November 1980, Circuit & Design Ideas): The three-terminal regulator should be LM320T-12 not LM30T-12.