

BIO → FEEDBACK

Dear SERVO:

I received my January 06 issue of *SERVO* and jumped right into Gerard Fonte's "Programmable Logic – Part 1" article. The article was very good and it's just the type of article I want to see more of.

However I have some questions – on Page 30, Figure 10, does the truth table and the reduced truth table match the schematic? In the truth table, should the "Input 1 Comparison" column read "inverted" for the inputs of 6 and 7 instead of "follows"? Also in the reduced truth table should the "OUTPUT" column read "inverted input 1" for the input

of 3 instead of "follows input 1"? Also, in order to have the circuit depicted in Figure 9 equivalent to the circuit in figure 10, should D3 of Figure 10 be tied to "Not Input 1" instead of "Input 1"?

David Ellis
Arlington, TX

Writer response:

Mr. Ellis is correct on all counts. I screwed up. For whatever reason, I managed to equate "0,1" with "1,0" in the main truth table and then propagated that error into the schematic and reduced truth table. A corrected Figure 10

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is provided here. I hope that this clarifies matters and apologize for any confusion that was caused.

Gerard Fonte