

Errata

- I like the more technically oriented articles that are published in *Modern Electronics*. The material by Forrest Mims is always very interesting to me, and the more technical the better. While I was leafing through the October 1988 issue, I noted that there seems to be something wrong with the circuit shown for the "Phone Line 'Busy' Indicator."

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Transistor Q1 is a pnp device (not npn as shown); the 2N3906 number is correct as are the emitter, base and collector con-

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nections into the circuit. Also, place near the input terminals that connect the Telephone line to R1 and R2 with “+” and “-” signs, respectively.—Ed.

• While devouring the October 1988 issue of *Modern Electronics*, I noticed an apparent error in Figures 4 and 5 of the “Troubleshooting With a dc Voltmeter” article, as well as in the accompanying description on page 20. I fail to see how a reverse bias on the base-emitter junction of a transistor can result in proper circuit operation. Shouldn't V_{ee} be -16 and -30 volts in Figures 4 and 5, respectively, to provide forward bias since the bases of the transistors are grounded? If the foregoing is true, the text in the first paragraph under Emitter-Bias Circuit should read “. . . V_E is 0.67 volt *below* ground.”

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You're correct about the V_{ee} potentials being negative voltages.—Ed.