

## LETTERS

SEND YOUR COMMENTS TO THE EDITORS OF ELECTRONICS NOW MAGAZINE

## **Logic Analyzer Corrections**

I have received many questions and comments from Electronics Now readers regarding my article, "Build a High-Performance Logic Analyzer" in the March 1998 issue. The schematic on page 34 has several errors that should be corrected. On IC1, there are two pins labeled 2. The pin that is connected to C27 should be labeled 13. There are also two pins labeled 26. The pin that feeds signal \*RAMWR should be labeled 25. Two pins are labeled 16. The pin that feeds signal \*RAMRD should be labeled 19. On IC2, pins 5 and 8 should be connected to pins 2, 6, and 12, which all go to ground.

The representation of J1 has also caused some confusion. The pinout of J1 does not correspond to the layout shown

on the schematic. The actual DB25 connector has pins 1 to 13 in a row on one side with pins 14 to 25 in a row on the other side.

There have been several questions about IC1, the Lattice ispLSI1016E.

Write To: Letters, Electronics Now Magazine, 500 Bi-County Blvd., Farmingdale, NY 11735

Due to the volume of mail we receive, not all letters can be answered personally. All letters are subject to editing for clarity and length.

This device can be programmed in the system without the need for a special programmer. The isp prefix stands for In System Programmable. The Logic Analyzer board has provisions for programming the ispLSI1016E on the board. You would need to install an 8-pin SIP header in the location next to IC1, and you need to remove IC8 before programming. A simple adapter cable allows programming of ispLSI devices from the parallel ports of Windows PCs.

The design and programming software is available free from the Lattice Web site at www.latticesemi.com. You can also get the data sheet for the ispLSI1016E and the schematic for the adapter cable. More information about in-system programmable PLDs can be

found on the Alta Engineering Web site at www.gutbang.com/alta (download PLDXPLOR.ZIP) as well as links to the Lattice Web site and many other PLD

vendors that offer in-system program-

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mable devices.