nections into the circuit. Also, place near the input terminals that connect the Telephone line to R1 and R2 with " + " and "• - "signs, respectively.-Ed.

- While devouring the October 1988 issue of Modern Electronics, I noticed an apparent error in Figures 4 and 5 of the "Troubleshooting With a dc Voltmeter" article, as well as in the accompanying description on page 20. I fail to see how a reverse bias on the base-emitter junction of a transistor can result in proper circuit operation. Shouldn't $\mathrm{V}_{\text {ee }}$ be -16 and -30 volts in Figures 4 and 5, respectively, to provide forward bias since the bases of the transistors are grounded? If the foregoing is true, the text in the first paragraph under Emitter-Bias Circuit should read ". . . $\mathrm{V}_{\mathrm{E}}$ is 0.67 volt below ground."

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You're correct about the $V_{e e}$ potentials being negative voltages.-Ed.

