

'TENWAY SUPERCHASER'
 MK. I & MK. II -
 LMC1000-056

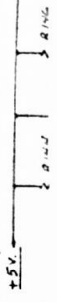
SCHEMATIC - INPUT CIRCUIT
 LAMB LABORATORIES 731009 WES.

- NOTES:-
1. ALL RESISTORS ARE 1/4 WATT, ± 5% UNLESS SPECIFIED. K = 1000, M = 1,000,000
 2. CAPACITOR VALUES ARE IN Mfd. ± 20% 100VDC.
 3. E1 = 741TC, E2 = 3302P
 4. ALL DIODES ARE 1N4448.

mean
 look at board from front
 C18 is marked
 next dot: it will cut

change C32 to
 .1 unless specified
 capacitor
 100V DC

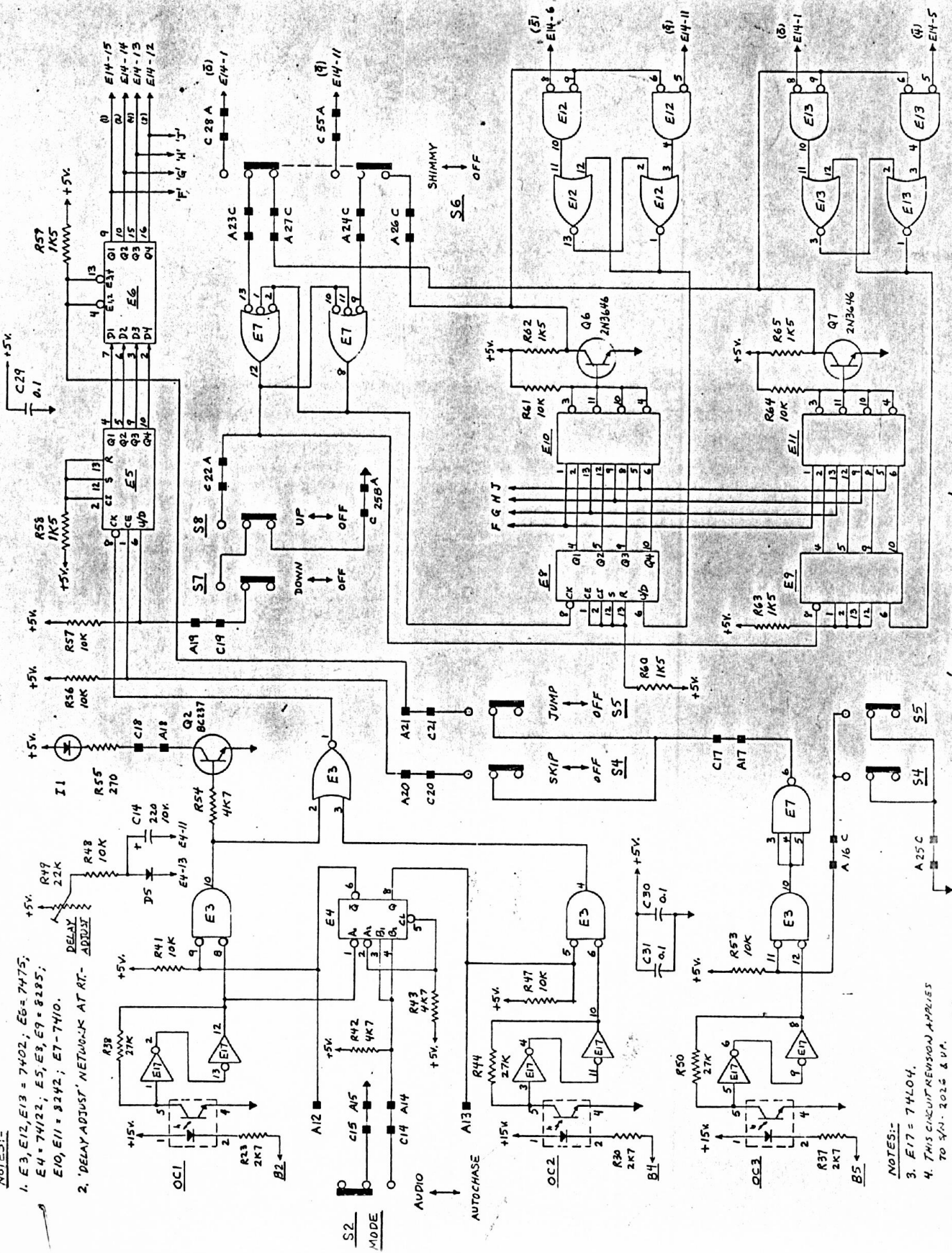
0.032 microsecond capacitor



+5V

NOTES:-

1. E3, E12, E13 = 7402; E6 = 7475; E4 = 74122; E5, E9, E9 = 8285; E10, E11 = 8242; E7 = 7410.
2. 'DELAY ADJUST' NETWORK AT RT- ADJUST



NOTES:-

3. E17 = 74104.
4. THIS CIRCUIT REVISION APPLIES TO 5A-2025 & VA.
5. ELIMINATED: R39, 49, 45, 46, 51, 52, 03, 4, 5.

'TENWAY SUPERCHASER'

L MC1000-055 MK I & MK II.

SCHEMATIC - CONTROL CIRCUITS

SHT. 1 of 3

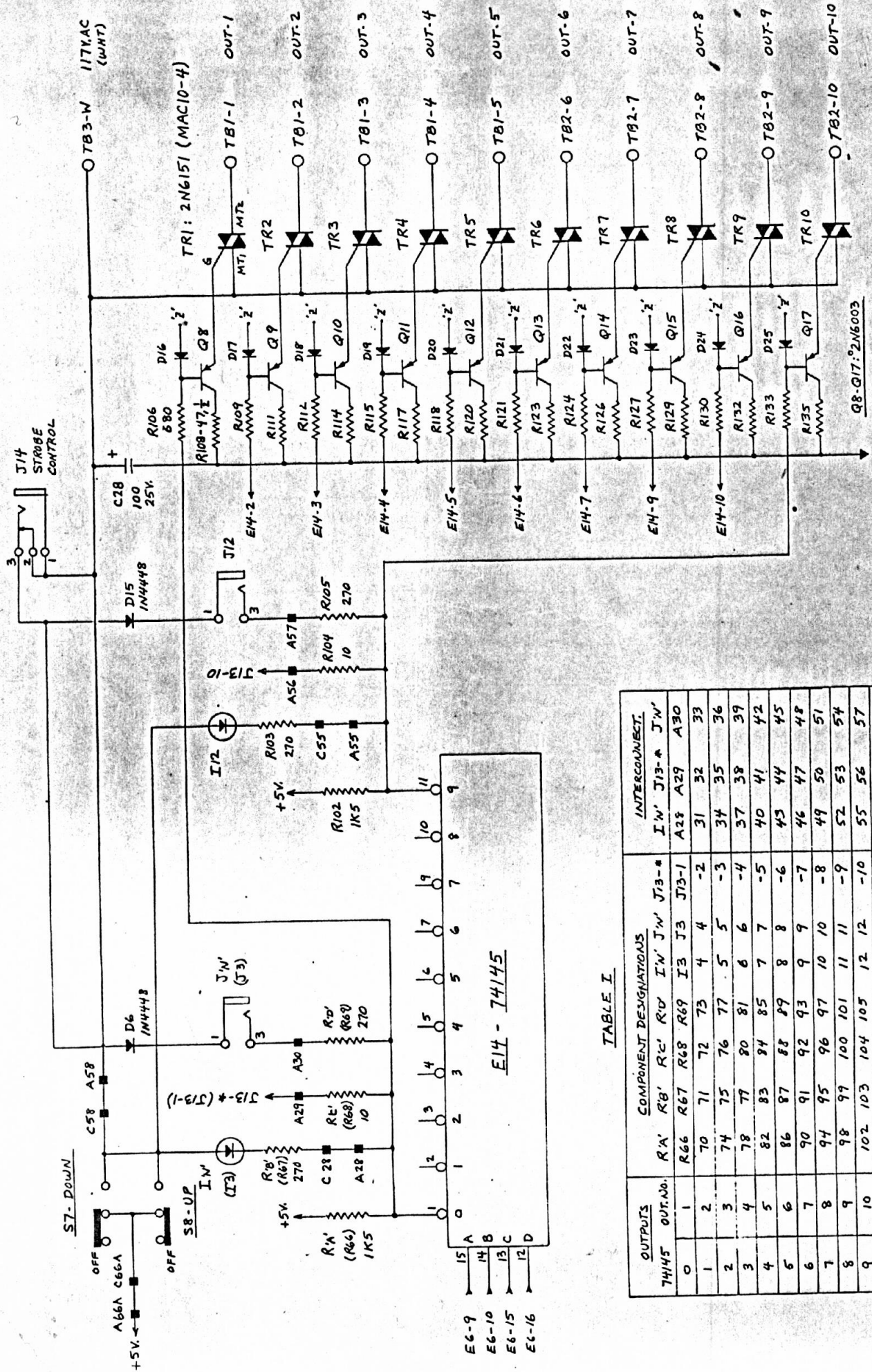


TABLE I

74145 OUT. No.	COMPONENT DESIGNATIONS										INTERCONNECT.		
	RA'	RB'	RC'	RD'	RE'	IF	JW'	JX'	JY'	JZ'	I'W'	J13-A	J'W'
0	R66	R67	R68	R69	I3	J3	J3-1	J3-1	J3-1	J3-1	A28	A29	A30
1	70	71	72	73	4	4	-2	-2	-2	31	32	33	
2	74	75	76	77	5	5	-3	-3	-3	34	35	36	
3	78	79	80	81	6	6	-4	-4	-4	37	38	39	
4	82	83	84	85	7	7	-5	-5	-5	40	41	42	
5	86	87	88	89	8	8	-6	-6	-6	43	44	45	
6	90	91	92	93	9	9	-7	-7	-7	46	47	48	
7	94	95	96	97	10	10	-8	-8	-8	49	50	51	
8	98	99	100	101	11	11	-9	-9	-9	52	53	54	
9	102	103	104	105	12	12	-10	-10	-10	55	56	57	

NOTES:-

1. THIS SCHEMATIC REVISION BEGINS WITH S/N 20,001.

TENWAY SUPERCHASER I

LMC 1000-055 MK.II

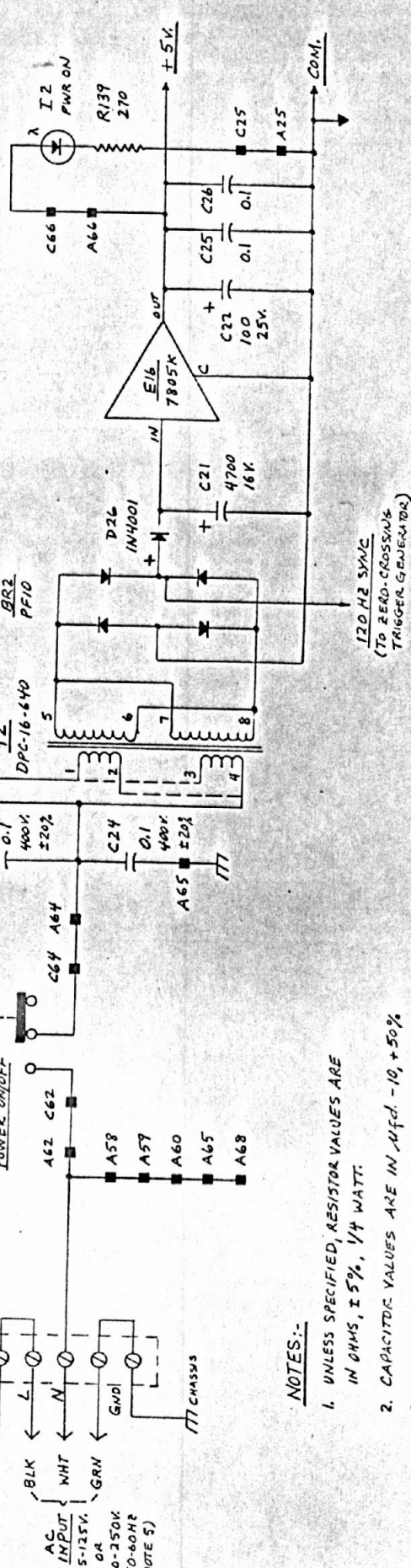
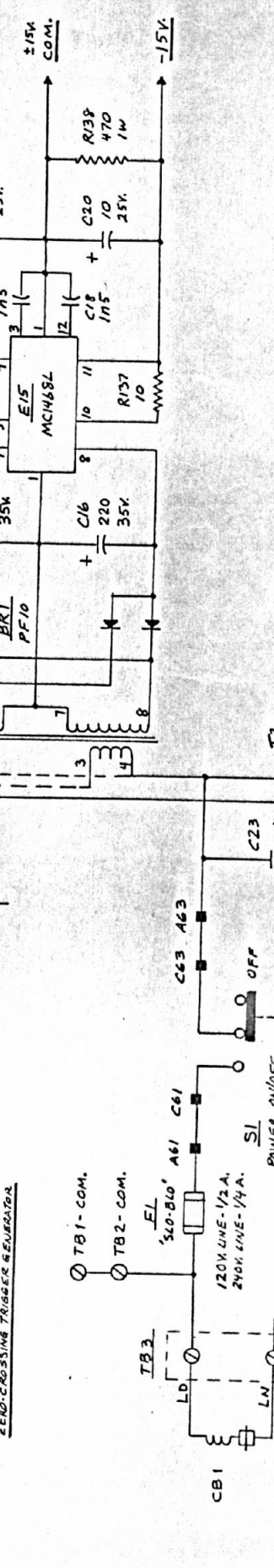
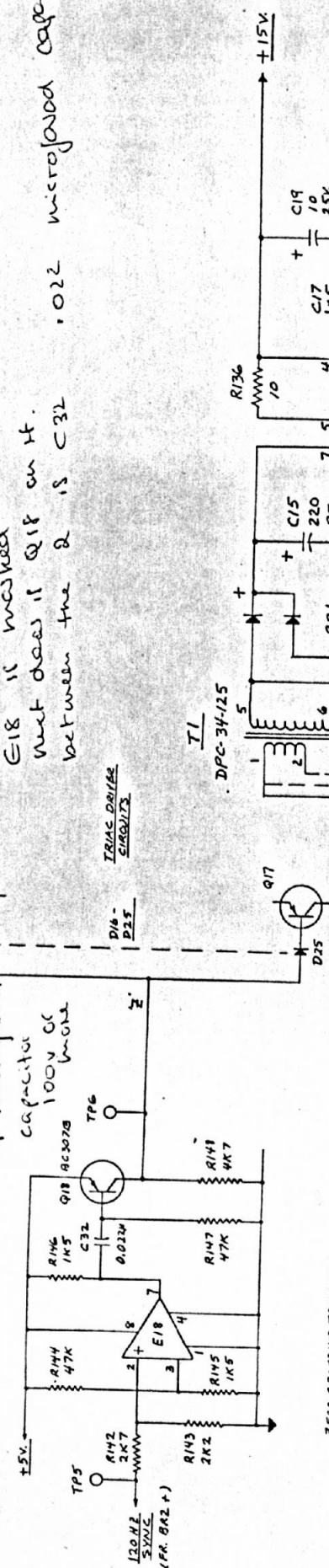
SCHEMATIC - CONTROL CIRCUITS

LAMB LABORATORIES 731011 405.000

SHT. 2 OF 3.

REV. B

change C32 to .1 microfarad capacitor
 .1 microfarad capacitor
 look at board from front
 E18 is masked
 just does it Q18 on it.
 between the 2 is C32 .022 microfarad capacitor



- NOTES:-**
- UNLESS SPECIFIED, RESISTOR VALUES ARE IN OHMS, 5%, 1/4 WATT.
 - CAPACITOR VALUES ARE IN uFd. -10, +50%
 - TERMINAL BLOCK CONNECTION (ON MAIN P.C. BD.)
 - P.C. BOARD INTERCONNECTION:
 - MAIN P.C. BOARD (LMC1000-055)
 - FRONT PANEL P.C. BD. (LMC1000-057)
 - THIS SCHEMATIC REVISION BEGINS WITH S/N 20,001.
 - TRANSFORMERS T1 & T2 HAVE SPLIT PRIMARIES, S40W WIRING IN PARALLEL FOR 117VAC INPUT; FOR 210-250 VAC INPUT PRIMARIES TO BE CONNECTED IN SERIES, I.E. TERMINALS 2 TO TERMINAL 3 ON EACH. F1 IS 400V 1/4 AMP. 250V M.D.C.