

Octal display for microprocessors

The function of this circuit is to display an eight-bit data word in conventional octal form. This is useful in microprocessor prototyping, where it is usual to use the octal representation. Digit and data selection is effected by a three state counter built around synchronously

operated JK flip-flops. The two or three bits appropriate to each display decoder by wired AND open-collector gates, and inverters. A segment current of about 3mA was found adequate for most lighting conditions, with a multiplexing oscillator frequency of approximately 2kHz.

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