REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	FIRST PRODUCTION	3-20-87	

D0 D1 D2 D3 D4 D5 D6 D7

TICK INT

500 KHz

INT

SYS RES

U306 LS74

U305 4024

U307 LS00

U310 HCU04

U309 LS138

U308 HC00

U317 LS373

U312 Z80 CPU

U315 6116LP

U313 2764

U314 6116LP

U311 LS245

U318 HC32

U319 HC04

U3 LS273

U303 LS74

U304 LS74

U302 LS32

U301 LS08

U309 LS138

U308 HC00

U310 HCU04

U307 LS00

U318 HC32

U319 HC04

U302 LS32

U303 LS74

U304 LS74

U306 LS74

U305 4024

U317 LS373

U311 LS245

U3 LS273

U312 Z80 CPU

U315 6116LP

U313 2764

U314 6116LP

5.6 MEG R307

R308 2K

HCU04

12MHz

C309 47pf

C307 47pf

C318 .5

R310 150K

R305 27K

R304 100K

R303 1K

R300 75K

R301 56

D306

D301 1N750A 4.7V

2N2907 Q300

+8VDC UNREGULATED SHT 3

C300 .047

R309 27K

C315 100pf

HC132

R306 0.2Ω

BT1 BATTERY

D308

D309

C310 .1

BATTERY BACK-UP

5/8 Hi to turn of CS

Floating

R311 2.0K

R312 4.7K

R313 4.7K

R320 4.7K

R317 4.7K

D2 D4 D3 D1

10K EDIT 6.3K STORE 1K 16K

1.6K 4K 2.5K 250 400 40 63 100 GAIN 63 V

MEMBRANE SWITCH P/N 250015 DWG D412

LED DISPLAY P/N 1A3805 DWG D2945

J.P. DIGIT 1 DIGIT 2 DIGIT 3 DIGIT 4

R340 680 R339 680 R338 680 R334 680 R335 680 R336 680 R337 680 R333 680

Q314 Q313 Q312 Q308 Q309 Q310 Q311 Q307

2N2907A

R332 47 R323 47 R324 47 R325 47 R328 47 R330 47 R332 47

L25 L26 L11 L12 L13 L14 L15 L16

L7 L18 L9 L20 L21 L22 L23 L24

L27 L10 L28 L9

C324 33

C325 .047

Q301 Q302 2N3904 Q304 Q303 Q306 Q305

R315 680 R316 680 R327 680 R325 680 R332 680 R329 680

NOT USED R312 R314

REAR PANEL

XFMR DWG 389A

.5AMP FUSE

D300 D302 D305 D307 D301 D303 D306 IN4001

C339 3300 C334 .047 C335 .047 C336 33

C337 .047 C326 .047 C329 .047 C338 .047

C331 33 C330 .047 C328 33 C327 .047

VR2 7815 VR1 7915 VR4 7805 VR3

+15V -15V +8VDC UNREGULATED +5V DISPLAY +5V

ADA SIGNAL PROCESSORS, INC.

MC-1, MICROPROCESSOR SCHEMATIC

APPROVALS	DATE
EA	5/28/87
EDT	5-20-87

SCALE | SIZE D | DRAWING NO 398-1 | REV A

DO NOT SCALE DRAWING | SHEET 3 of 3

DEPOT http://www.adadepot.com