

The Harman Kardon

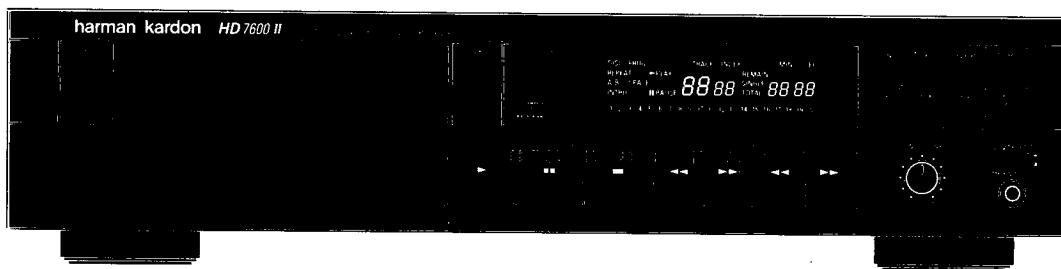
Model HD7600 II

COMPACT DISC PLAYER

Manual 159A

HD7600 II

Technical Manual



The following marks found in the parts list of this manual identify the models as follows.

- UA : North America area model
- BK : North America area model Black version
- I : International model
- IB : International model Black version
- BB : Australia model Black version
- WB : Asia/Oseania model Black version
- NB : Japan model Black version

harman/kardon

240 Crossways Park West, Woodbury, N. Y. 11797
1112-3152159A2 P-079011 2000 Printed in Japan

CLASS 1 LASER PRODUCT

Product complies with DHHS rules CFR subchapter J part 1040:10 at date of manufacture.

DANGER—invisible laser radiation when open and interlock failed or defeated. Avoid direct exposure to the beam.

CAUTION—use of all controls, adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

**CLASS 1
LASER PRODUCT**

Be Careful of the Laser Pickup

Although you cannot see it from the outside, a laser pickup is located under the disc tray and a precision lens is built in it.

Since the laser pickup, including the lens element, is especially sensitive to dust, keep the disc tray closed when not in use. Also do not put your hand inside the unit.

ADVARSEL:

Usynlig laserstråling ved åbning når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

VAROITUS!

Laite sisältää laserdiordin, joka lähetää näkymätöntä silmille vaarallista lasersäteilyä.

SPECIFICATION

System	: Compact Disc Digital Audio	Digital Output Level/Impedance
D/A Converter	: Linear, Pulse Width Modulated Bit Stream, Clock Rate: 33.8688MHz (33,868.8KHz)	Coaxial : 0.5Vp-p @ 75Ω Optical : -18dBm
Signal Detection	: 3-Beam Semiconductor Optical Laser Pickup	Power Supply
Error Correction	: CIRC System	U.S.A. and Canada models : AC120V, 60Hz
Low Level Linearity	: +/-0.2dB @-90dB	International, Australia and
Frequency Response	: 4Hz to 20kHz +0dB/-5dB	Asia/Oseania models : AC220/240V, 50/60Hz
Total Harmonic Distortion (THD)	: 0.003%	Japan model : AC100V, 50/60Hz
Dynamic Range	: 98dB	Power Consumption : 20Watts
Signal to Noise Ratio	: 106dB	Dimensions : 17-3/8" x 4" x 14" 443mm x 103mm x 356 mm
Channel Separation	: 96dB	Weight : 18.9lbs/8.6kg
Line Output Level/Impedance	: 2.0V @ 10kΩ	Specifications and components subject to change without notice. Overall performance will be maintained or improved.

Adjustment of this unit requires following measuring instruments and jigs.

- Oscilloscope (3 or more modes, 100MHz, input Select DC range) 1740A
- Test Disc: EIAJ CD-1

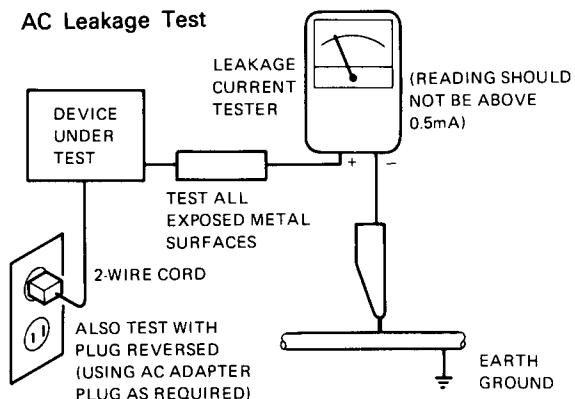
SAFETY PRECAUTIONS

Before returning an instrument to the customer, always make a safety check of the entire instrument, including, but not limited to, the following items:

- a. Be sure that no built-in protective devices are defective and/or have been defeated during servicing.
 - (1) Protective shields are provided to protect both the technician and the customer. Correctly replace all missing protective shields, including any removed for servicing convenience.
 - (2) When reinstalling the chassis and/or other assembly in the cabinet, be sure to put back in place all protective devices, including, but not limited to, nonmetallic control knobs, insulating fishpapers, adjustment and compartment covers/shields, and isolation resistor/capacitor networks.

Do not operate this instrument or permit it to be operated without all protective devices correctly installed and functioning.
- b. Be sure that there are no cabinet openings through which an adult or child might be able to insert their fingers and contact a hazardous voltage. Such openings include, both are not limited to, excessively wide cabinet ventilation slots, and an improperly fitted and/or incorrectly secured cabinet back over.
- c. **Leakage Current Hot Check** — With the instrument completely reassembled, plug the AC line cord directly into a 120V AC outlet. (Do not use an isolation transformer during this test.) Use a leakage current tester or a metering system that complies with American National Standards Institute (ANSI) C101.1 "Leakage Current for Appliances" and Underwriters Laboratories (UL) 1270, (34.6). With the instrument AC switch first in the ON position and then in the OFF position, measure from a known earth ground (metal waterpipe, conduit, etc.) to all exposed metal parts of the instrument (antennas, handle bracket, metal cabinet, screwheads, metallic overlays, control shafts, etc.), especially any exposed metal parts that offer an electrical return path to the chassis. Any current measured must not exceed 0.5 milliamp. Reverse the instrument power cord plug in the outlet and repeat test. **ANY MEASUREMENTS NOT WITHIN THE LIMITS SPECIFIED HEREIN INDICATE A POTENTIAL SHOCK HAZARD THAT MUST BE ELIMINATED BEFORE RETURNING THE INSTRUMENT TO THE CUSTOMER.**

AC Leakage Test

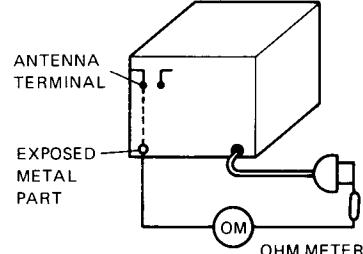


d. Insulation Resistance Test

- (1) Unplug the power supply cord and connect a jumper wire between the two prongs of the plug.
- (2) Turn on the power switch of the instrument.
- (3) Measure the resistance with an ohmmeter between the jumpered AC plug and each **exposed metallic** cabinet part on the instrument, such as screwheads, antenna, control shafts, handle brackets, etc. The reading should be as shown below. If it is not within the limits specified, there is the possibility of a shock hazard, and the instrument must be repaired and rechecked before it is returned to the customer.

e. Insulation Resistance Test Cold Check

- (1) Unplug the power supply cord and connect a jumper wire between the two prongs of the plug.
- (2) Turn on the power switch of the instrument.
- (3) Measure the resistance with an ohmmeter between the jumpered AC plug and each **exposed metallic** cabinet part on the instrument, such as screwheads, antenna, control shafts, handle brackets, etc. When the exposed metallic part has a return path to the chassis, the reading should be between 1 and 5.2 Megohm. When there is no return path to the chassis, the reading must be "infinite". If it is not within the limits specified, there is the possibility of a shock hazard, and the instrument must be repaired and rechecked before it is returned to the customer.



PRODUCT SAFETY NOTICE

Some electrical and mechanical parts have special safety related characteristics which are often not evident from visual inspection, nor can the protection they give necessarily be obtained by replacing them with components rated for higher voltage, wattage, etc. Parts that have special safety characteristics are identified by shading, by (\triangle) on schematics and parts listed. Use of a substitute replacement that does not have the same safety characteristics as the recommended replacement part might create shock, fire, and/or other hazards. Products Safety is under review continuously and new instructions are issued whenever appropriate.

SERVICING PRECAUTIONS

CAUTION: Before servicing instruments covered by this manual and its supplements, read and follow the SAFETY PRECAUTIONS on this page.

NOTE: If unforeseen circumstances created conflict between the following servicing precautions and any of the safety precautions, **always follow the safety precautions**. Remember: Safety First.

General Servicing Precautions

- a. Always unplug the instrument AC power cord from the AC power source before:
 - (1) Removing or reinstalling any component, circuit board, module, or any other instrument assembly.
 - (2) Disconnecting or reconnecting any instrument electrical plug or other electrical connection.
 - (3) Connecting a test substitute in parallel with an electrolytic capacitor in the instrument.

Caution: A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.
- b. Do not defeat any plug/socket B+ voltage interlocks with which instruments covered by this manual might be equipped.
- c. Do not apply AC power to this instrument and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
- d. Always connect a test instrument's ground lead to the instrument chassis ground before connecting the test instrument positive lead. Always remove the test instrument ground lead last.

NOTE: Refer to Safety Precautions on Page 3.

- (1) The service precautions are indicated or printed on the cabinet, chassis or components. When servicing, follow the printed or indicated service precautions and service materials.
- (2) The Components used in the unit has a specified flammability and dielectric strength. When replacing any components, use components which has the same ratings. Components marked (Δ) in the circuit diagram are important for safety or for the characteristics of the unit. Always replace with the appointed components.
- (3) An insulation tube or tape is sometimes used and some components are raised above the printed wiring board for safety. The internal wiring is sometimes clamped to prevent contact with heating components. Install them as they were.
- (4) After servicing, always check that the removed screws, components and wiring have been installed correctly and that the portion around the service part have not been damaged and so on. Further check the insulation between the blades of attachment plug and accessible conductive parts.

Insulation Checking Procedure

Disconnect the attachment plug from the AC outlet and turn the power on. Connect the insulation resistance meter (500V) to the blades of the attachment plug. The insulation resistance between the each blade of the attachment plug and accessible conductive parts (Note 1) should be more than 1M-ohm.

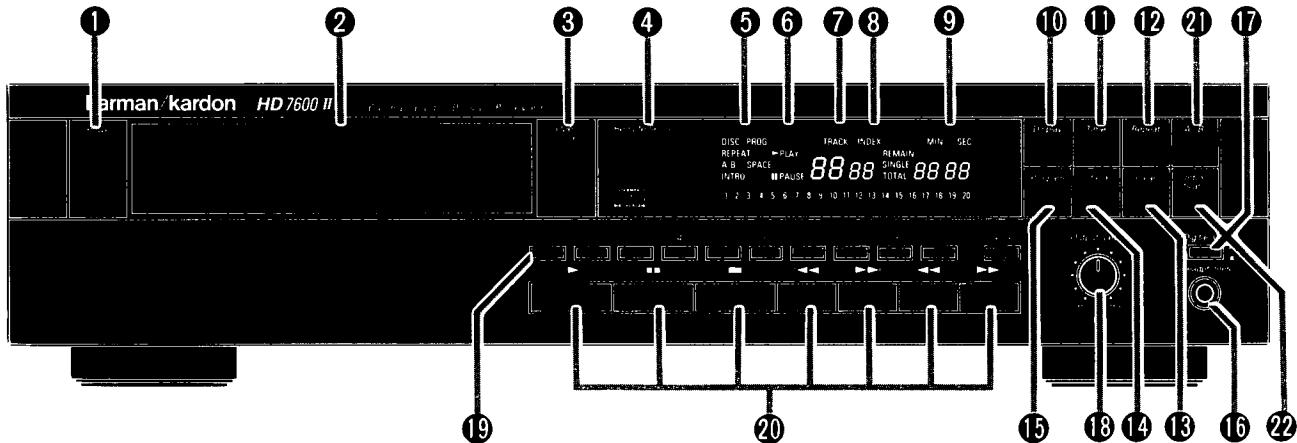
Note 1: Accessible Conductive Parts including Metal panels, Output jacks, etc.

ELECTROSTATICALLY SENSITIVE (ES) DEVICES

Some semiconductor (solid state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically Sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some fieldeffect transistors and semiconductor "chip" components. The following techniques should be used to help reduce the incidence of component damage caused by static electricity.

1. Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed for potential shock reasons prior to applying power to the unit under test.
 2. After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
 3. Use only a grounded-tip soldering iron to solder or unsolder ES devices.
 4. Use only an anti-static solder removal device. Some solder removal devices not classified as "anti-static" can generate electrical charges sufficient to damage ES devices.
 5. Do not use freon-propelled chemicals. These can generate electrical charge sufficient to damage ES devices.
 6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil or comparable conductive material).
 7. Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.
- CAUTION:** Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.
8. Minimize bodily motions when handling unpackaged replacement ES devices. (Otherwise harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device).

CONTROLS AND FUNCTIONS



① POWER switch

The POWER switch turns the unit on and off.

② Compact disc drawer

Load a disc (full-size or 3-inch) into your compact disc player by placing it in this drawer with the label side up. Open and close the drawer using the OPEN/CLOSE button (3).

③ OPEN/CLOSE button

Press this button to open or close the compact disc drawer. The drawer will also close if you press the play button or push the drawer gently toward the chassis of the player; however, we recommend that you do not push the drawer.

④ REMOTE SENSOR

This area receives the signal from the remote control unit. Make sure this area of the front panel is kept free from dirt or other obstructions that might prevent proper reception of the infrared signal from the remote control.

⑤ FUNCTION indicator

DISC-This indicator lights when the disc drawer is open, and when a disc is loaded. It does not light when there is no disc inside.

REPEAT-When this indicator is lit, the unit is in REPEAT mode and will continuously repeat either the entire disc or the programmed sequence.

A-B-When this indicator is lit steadily, the unit is set to repeat a specified section of the disc (from A to B). When repeat indicator is lit, you have already programmed the starting point (point A) of a section, and the unit is waiting for you to program the ending point of the section (point B).

INTRO-When this indicator is lit, the unit is in INTRO SCAN mode and will play only the first 15 seconds of each selection on the disc or in the selected program.

PROG-When this indicator is lit, the unit is in PROGRAM mode, in which you can program a sequence of tracks in the order you'd like to hear them, then play back your programmed sequence.

SPACE-When this indicator is lit, the unit is in SPACE mode and will insert 4 seconds of space between tracks.

⑥ PLAYBACK mode

PLAY-This indicator is lit when the unit is playing a track.

PAUSE-This indicator is lit when the unit is in PAUSE mode (i.e., you have pressed PAUSE to temporarily stop playback).

⑦ TRACK indicator

When a disc is loaded but not playing, the TRACK indicator displays the total number of tracks on that disc. During playback,

the indicator displays the number of the track currently being played. During programming, it displays the number of the track most recently selected.

⑧ INDEX indicator

Some discs that contain very long tracks may have those tracks subdivided into shorter sections. For example, a long classical piece may be divided into movements, each of which may have an index number. The INDEX indicator displays the number of the subdivision of a track currently being played. If the track has no subdivisions, the INDEX indicator will display a 1 during playback. During programming, the INDEX light goes out and the indicator displays the total number of tracks selected in the programmed sequence.

⑨ TIME indicator

When a disc is loaded but not playing, the TIME indicator displays the total amount of playback time on that disc. During playback, depending on the state of the TIME button (9), the indicator displays one of the following.

- The time that has elapsed since the beginning of the current track.
- The time that remains on the currently selected track. The words REMAIN SINGLE will be displayed if the indicator is in this mode.
- The time that remains on either the entire disc or on the programmed selections. The words REMAIN TOTAL will be displayed if the indicator is in this mode. Note that if you have programmed more than 99 minutes in your selection, only dashes will appear in this mode.

⑩ DISPLAY button

This button turns the display on and off. When you turn the display off in PLAY mode, the entire display goes off. In PAUSE mode, most of the display goes off, but the track number is still displayed. In STOP mode, most of the display goes off, but the total number of tracks on the disc is still displayed. In some systems, turning the fluorescent display off may provide the purest possible sound quality during playback.

⑪ TIME button

During playback or programming, this button cycles through the three TIME display modes

- * The time that has elapsed since the beginning of the current track.
- * The time that remains on the currently selected track.
- * The time that remains on either the entire disc or the programmed sequence.

12 REPEAT button

This button puts your compact disc player in and out of REPEAT mode. In REPEAT mode, the unit will continually play back either the entire disc or the programmed sequence of tracks. Press the button once to turn on REPEAT mode; press again to turn off REPEAT mode. When the unit is in REPEAT mode, the FUNCTION indicator (5) will display the word REPEAT.

13 CLEAR button

The CLEAR button removes tracks from your programmed sequence one by one starting with the last track entered. Each time you clear a track from the programmed sequence, that track's time is deleted from the total time displayed. This button is used only in PROGRAM mode.

14 CHECK button

When a disc is loaded but not playing (STOP mode), you can press the CHECK button to see the playback time of each track on the disc. Each time you press the CHECK button, the next track is displayed along with its playback time. When you have stepped sequentially through all the tracks on the disc, another press of the CHECK button displays the total playback time of the entire disc. In PROGRAM mode, the CHECK button displays the cumulative time of the tracks in the programmed sequence. That is, the first press displays the time of the first programmed track, the second press displays the cumulative playback time of the first and second tracks, and so on.

15 PROGRAM button

This button puts your compact disc player in and out of PROGRAM mode. Press the button once to turn on PROGRAM mode; press again to turn it off. In PROGRAM mode, you can program a set of tracks to play in any sequence. (For instructions on programming a sequence of tracks, see the Operations section of instruction manual.) When the unit is in PROGRAM mode, the FUNCTION indicator (5) will display the word PROG in red.

16 HEADPHONES jack

Stereo headphones with a standard 1/4" plug can be connected to this jack.

17 DIGITAL OUT button

This button switches the digital output jacks on and off. Press the button to turn these outputs on only when using the optional digital output connections.

18 OUTPUT LEVEL control

This knob controls the volume of the variable output jacks on the rear panel as well as the headphone volume. You can use this output volume control to match the volume of your compact disc player with the volume of your turntable or tuner. In systems where the compact disc player is connected directly to the power amplifier, the OUTPUT LEVEL control provides the volume control for the system.

19 Track selection buttons

This set of buttons (numbered 1 through 10 and +10) enables you to directly select the track you want to play. To select a track with a number higher than 10, press the +10 button, then press the button that will add up to the number of the track you want. For example, to select track 14, press +10, then press 4. If you select a track number that does not exist on your disc, the TRACK indicator will continue to display the number of the last track on the disc. When PROGRAM mode is off, pressing a track button plays the track you select. When PROGRAM mode is on, pressing a track button programs that track into your sequence.

20 Playback controls

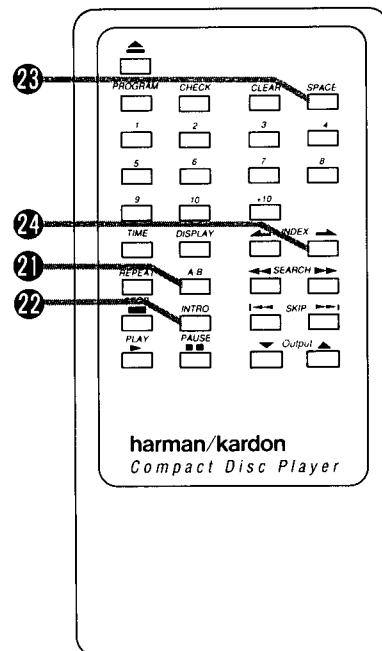
PLAY- ■ The PLAY button starts playback of a track.
PAUSE- ■ ■ Pressing the PAUSE button temporarily stops playback. Pressing the PAUSE button again, or pressing PLAY, resumes playback at the precise point where playback left off.
STOP- ■ The STOP button stops playback. When you press PLAY, playback will start either at the beginning of the disc or programmed sequence, or at any track you select.
SKIP BACK- ■ ■ Pressing this button once skips back to the

beginning of the present track. If you are in PLAY mode, playback will resume. Holding the button down skips backward to the beginning of each preceding track until the first track on the disc is reached.

SKIP FORWARD- ■ ■ ■ Pressing this button once skips forward to the beginning of the next track. If you are in PLAY mode, playback will resume. Holding the button down skips forward to the beginning of each subsequent track until the last track on the disc is reached.

SEARCH BACK- ■ ■ ■ Holding down this button during playback reverses rapidly through the disc. This function is similar to the "Rewind" function on a tape deck.

SEARCH FORWARD- ■ ■ ■ Holding down this button during playback speeds rapidly forward through the disc. This function is similar to the "Fast forward" function on a tape deck.

REMOTE CONTROL

NOTE that the HD7600II remote control also has buttons for remote output level control.

The remote control for the HD7600II provide the same functions found on the front panel of the compact disc player itself. In addition, the remote control provides some functions that are not found on the front panel.

The functions found on the remote control that are not found on the front panel are INDEX and SPACE. These two functions are described below (for other functions, see the descriptions above).

21 A-B button

This button puts your compact disc player in and out of A-B mode. In A-B mode, the unit will continually play back a "phrase" or section of your disc beginning at point A and ending at point B. When the unit is in A-B mode, the FUNCTION indicator (5) will display REPEAT A-B.

22 INTRO button

This button puts your compact disc player in and out of INTRO SCAN mode. When INTRO SCAN is on and you press PLAY, the unit plays only the first 15 seconds of each track before moving on to the next. Press the button once to turn on INTRO SCAN mode; press again to turn it off and stop the scanning. When the unit is in INTRO SCAN mode, the FUNCTION indicator (5) will display the word INTRO.

23 SPACE button

This button puts your compact disc player in and out of SPACE mode. Press the button once to turn on SPACE mode; press

again to turn it off. In SPACE mode, the unit inserts 4 seconds of blank space between tracks. This is particularly useful if you are recording music from a compact disc onto a tape. Tape decks with a Music Search feature will be able to use the 4 seconds of space to correctly identify the beginning of each selection on the tape. When the unit is in SPACE mode, the FUNCTION indicator (5) will display the word SPACE.

24 INDEX buttons

Some discs contain long selections that may subdivided into shorter sections (for example, operas). Just as the tracks are marked by the track number, these shorter sections may be marked by index numbers. When you have started playback of the desired track, you can use the INDEX UP and INDEX DOWN buttons to jump to a desired index selection. Each time you press an INDEX button, the INDEX indicator displays the next higher or lower index number. If you continue pressing the

button until the indicated index number is higher than the actual number of index selections on the disc, playback will start from the nearest index selection.

The remote control operates within a 30° angle of the remote sensor on the front panel of the unit. Make sure that the remote's front projection "lens" as well as the REMOTE SENSOR area on the front panel of the unit are kept free from dirt to ensure proper transmission and reception of the infrared beam. Also make sure that no objects (e.g., stereo cabinet doors, end tables) block the path from the remote control to the REMOTE SENSOR area.

Avoid using the remote control near fluorescent lamps, as they may shorten its operating range.

DISASSEMBLY PROCEDURES (REFER TO PAGES 39, 40 AND 44)

[1] CABINET TOP (123) REMOVAL

Remove 7 screws (A) and then remove the Cabinet Top (123).

[2] FRONT PANEL ASS'Y (AA) REMOVAL

1. Remove the Cabinet Top (123), referring to the previous step [1].
2. Disconnect the connector with lead wires (LCN109, LCN110, LCN111, LCN112 and LCN116) from connectors (CN109, CN110, CN111, CN112 and CN116) on the Main P. C. Board (PCB-1).
3. Open the lid of connectors (CN106, CN107 and CN108) on the Main P. C. Board (PCB-1), then disconnect the jumper leads (W106, W107 and W108).
4. Remove 7 screws (B) and then remove the Front Panel Ass'y (AA) with the Front P. C. Board (PCB-2) and the Output Level & Jack P. C. Board (PCB-3).
5. Remove a nut (C) fixing the output level volume and then remove the Output Level & Jack P. C. Board (PCB-3) from the Front Panel Ass'y (AA).
6. Remove 9 screws (D) and then remove the Front P. C. Board (PCB-2) from the Front Panel Ass'y (AA).

[3] MAIN P. C. BOARD (PCB-1) REMOVAL

1. Remove the Cabinet Top (123), referring to the previous step [1].
2. Disconnect the connector with lead wires (LCN115, LCN114, LCN113, LCN117, LCN109, LCN110, LCN111, LCN112 and LCN116) from connectors (CN115, CN114, CN113, CN117, CN109, CN110, CN111, CN112 and CN116) on the Main P. C. Board (PCB-1).
3. Open the lid of connectors (CN119, CN106, CN107 and CN108) on the Main P. C. Board (PCB-1), then disconnect the jumper leads (JL1, JL2, W106, W107 and W108).
4. Remove 9 screws (E) and then remove the Main P. C. Board (PCB-1).
5. Remove the Front Panel Ass'y (AA), referring to the previous step [2]-4 and then remove the Shaft (169).
6. Open the lid of connector (CN119) on the Main P.C. Board (PCB-1), then disconnect the jumper leads (JL1 and JL2).
7. Remove 5 screws (F) and then remove the Power Switch P. C. Board (PCB-4).
8. Disconnect the connector with lead wire (LCN910) from connector (CN910) on the Remote Jack P.C. Board (PCB-5).
9. Remove 1 screw (G) and then remove the Remote Jack P. C. Board (PCB-5).

[4] CD PLAYER MECHANICAL ASSEMBLY (103) REMOVAL

1. Remove the Cabinet Top (123), referring to the previous step [1].
2. Disconnect the connector with lead wires (LCN115, LCN114, LCN113 and LCN117) from connectors (CN115, CN114, CN113 and CN117) on the Main P. C. Board (PCB-1).
3. Remove 3 screws (H) and then remove the CD Player Mechanical Assembly (103) backward.

DISASSEMBLING THE CD PLAYER MECHANICAL ASSEMBLY

[5] DISC TRAY (16) REMOVAL

1. Remove the Cabinet Top (123), referring to the previous step [1].
2. Connect the Power cord and turn on the power by pressing the Power switch.
3. Open the Disc Tray (16) by pressing the Open/Close button.
4. With the Disc Tray (16) opened as it is, pull out the power plug.
5. Push the Disc Tray (16) by hand to slide it in once.
6. Loosen the screws (I) and then slide out the Disc Tray (16) by hand.

[6] OPTICAL PICK-UP (52) REMOVAL

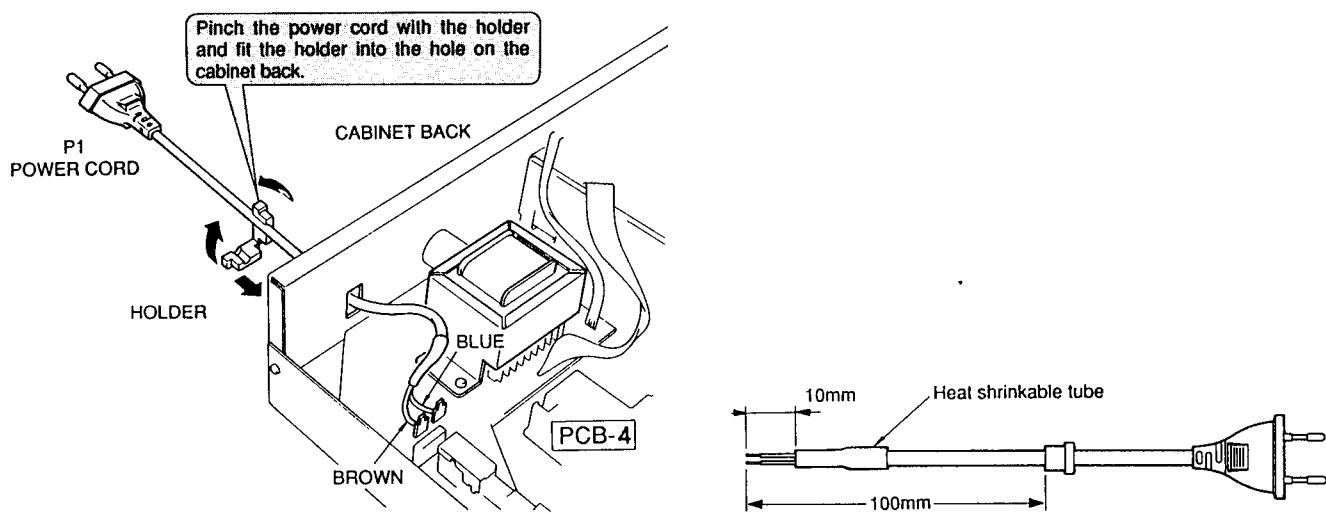
1. Remove the CD Player Mechanical Assembly (103), referring to the previous step [4].
2. Remove 4 screws (J) and then remove the Disc Motor Ass'y (63) with Pick-up, Slide Motor and etc.
3. Remove 2 screws (K) and then remove the Gears (59 and 60).
4. Remove the 2 screws (L) and then remove the Shaft Clamps (62), then remove the Pick-up (52) with shaft.

[7] SLIDE MOTOR (57) REMOVAL

1. Remove the Gears (59 and 60), referring to the previous step [6]-3.
2. Unsolder the terminal of Disc and Slide Motors, then remove the Motor P. C. Board (65).
3. Remove 2 screws (M) and then remove the Slide Motor (57).

POWER CORD REPLACEMENT (FOR SERVICE ENGINEERS OTHER THAN NORTH AMERICA)

In order to prevent fire or shock hazard when replacing the power cord, follow the procedure below to replace the part with the standard supply parts.



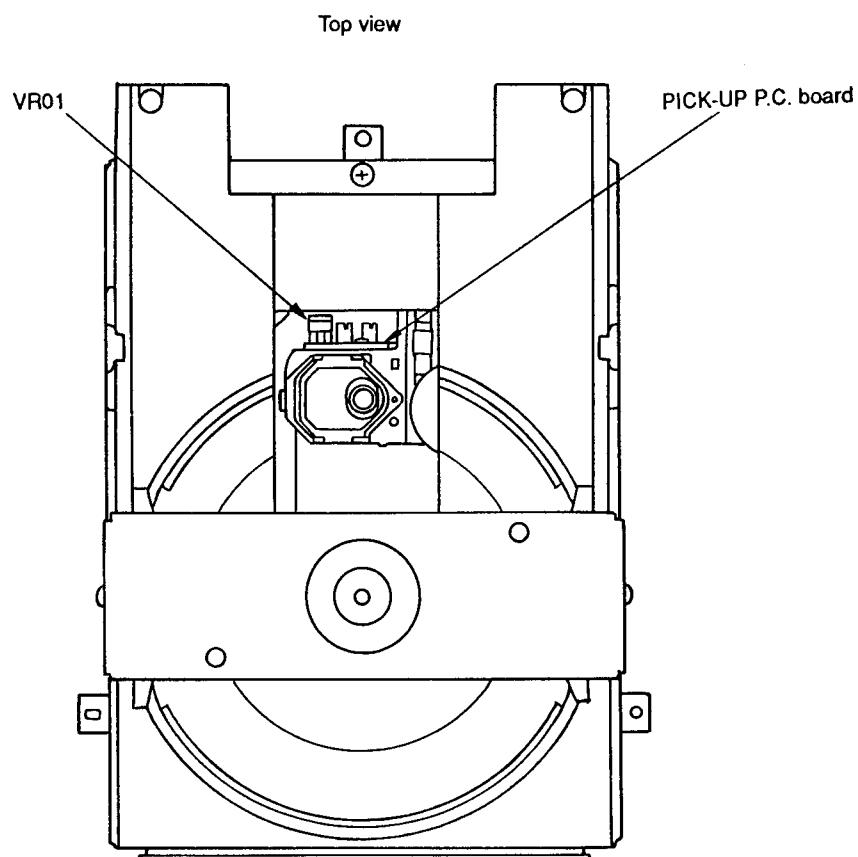
ADJUSTMENT PROCEDURE

1. Instruments required

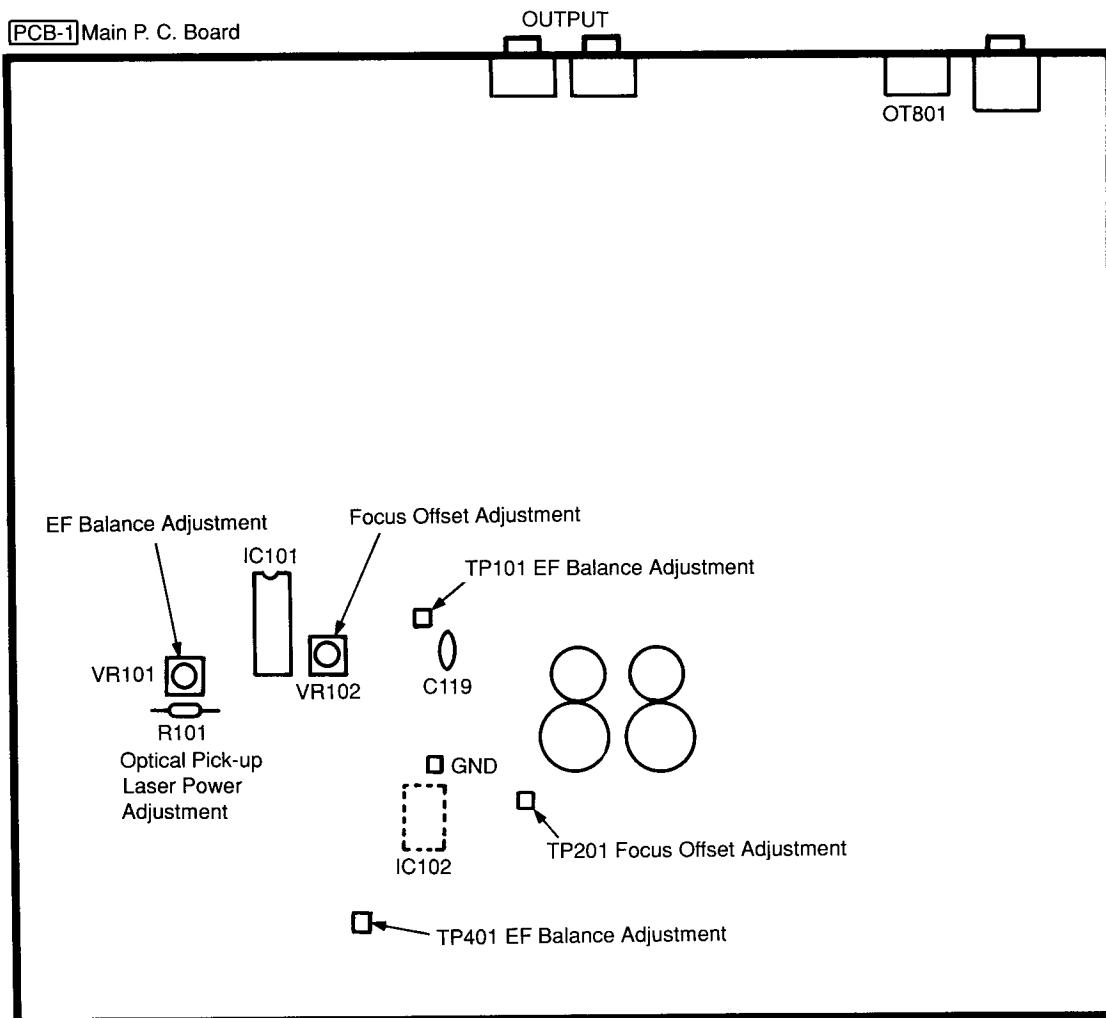
- DC voltmeter
- Oscilloscope (3 or more modes, 100 MHz, input select DC range)
- Jitter meter
- Test disc: EIAJ CD-1

2. Adjustment points

- 2-1 Adjusting screw layout for CD player mechanical assembly.



2-2 VR and test point layout for main P.C. board.



3. Optical Pick-up Laser Power Adjustment

The specified value for adjustment varies with the type of the pick-up in use. Obtain the voltage value according to the following procedure.

(a) Read the current value on the label of the pick-up P.C. board.

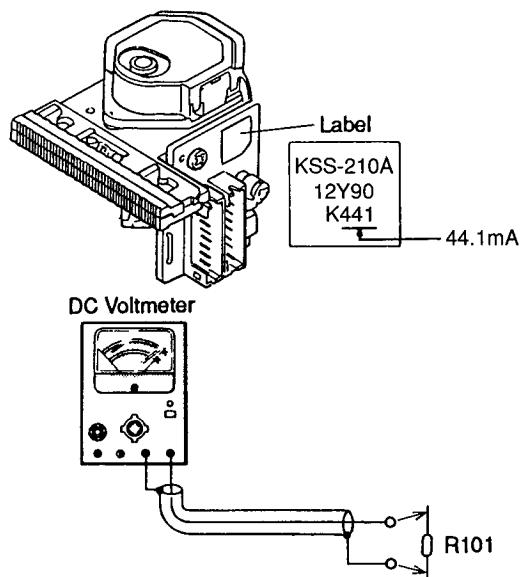
(b) Using the amperage of the current and the following equation, calculate the voltage value.

When the current is 44.1mA: $V=0.0441A \times 22=0.9702V$

(1) Connect the DC voltmeter to both ends of R101.

(2) Insert EIAJ CD-1 test disc and place the unit in the Play mode.

(3) Adjust VR01 in the pick-up P.C. board to the voltage obtained in step (b) above.



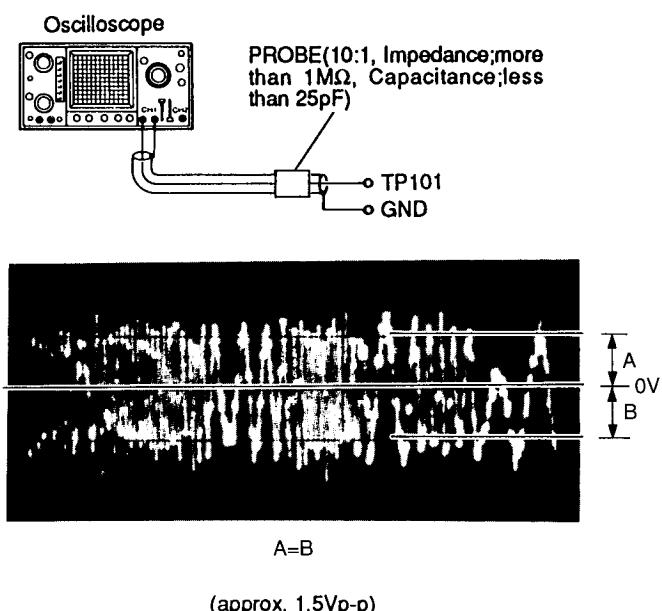
4. EF Balance Adjustment

(1) Connect the oscilloscope (0.5V/div, 5 msec./div, DC mode) to TP101 and GND.

Short-circuit TP401 and GND.

(2) Insert EIAJ CD-1 test disc and place the unit in the Play mode.

(3) Adjust VR101 so that the amplitude above and below the zero DC line becomes equal. (Amplitude A = Amplitude B)

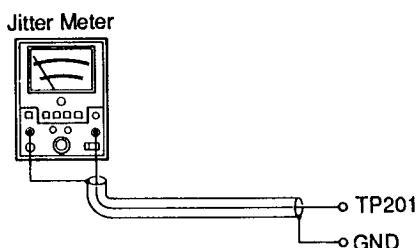


5. Focus Offset Adjustment

(1) Connect the Jitter meter to TP201 and GND.

(2) Insert EIAJ CD-1 test disc and place the unit in the play mode.

(3) Adjust VR102 until the jitter for optimum sensitivity.

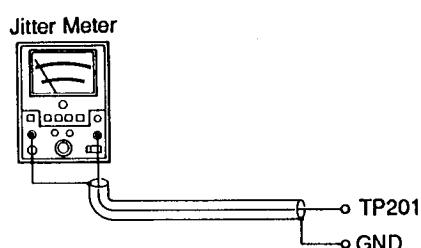


6. Jitter Inclination Confirmation

(1) Connect the Jitter meter (sigma range) to TP201 and GND.

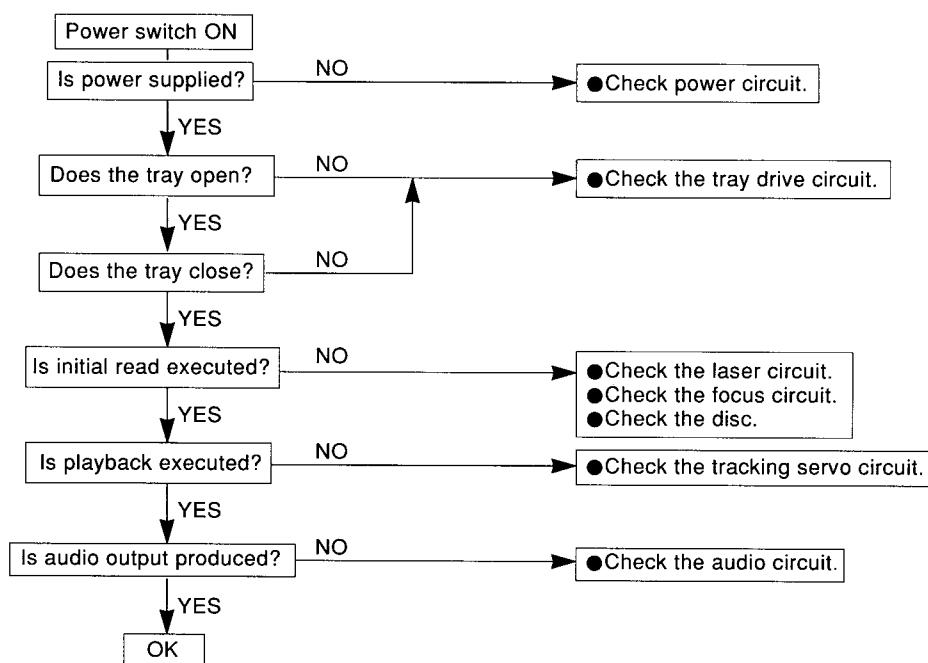
(2) Insert EIAJ CD-1 test disc and place the unit in the Play mode.

(3) Confirm the jitter meter reading is less than 30ns.

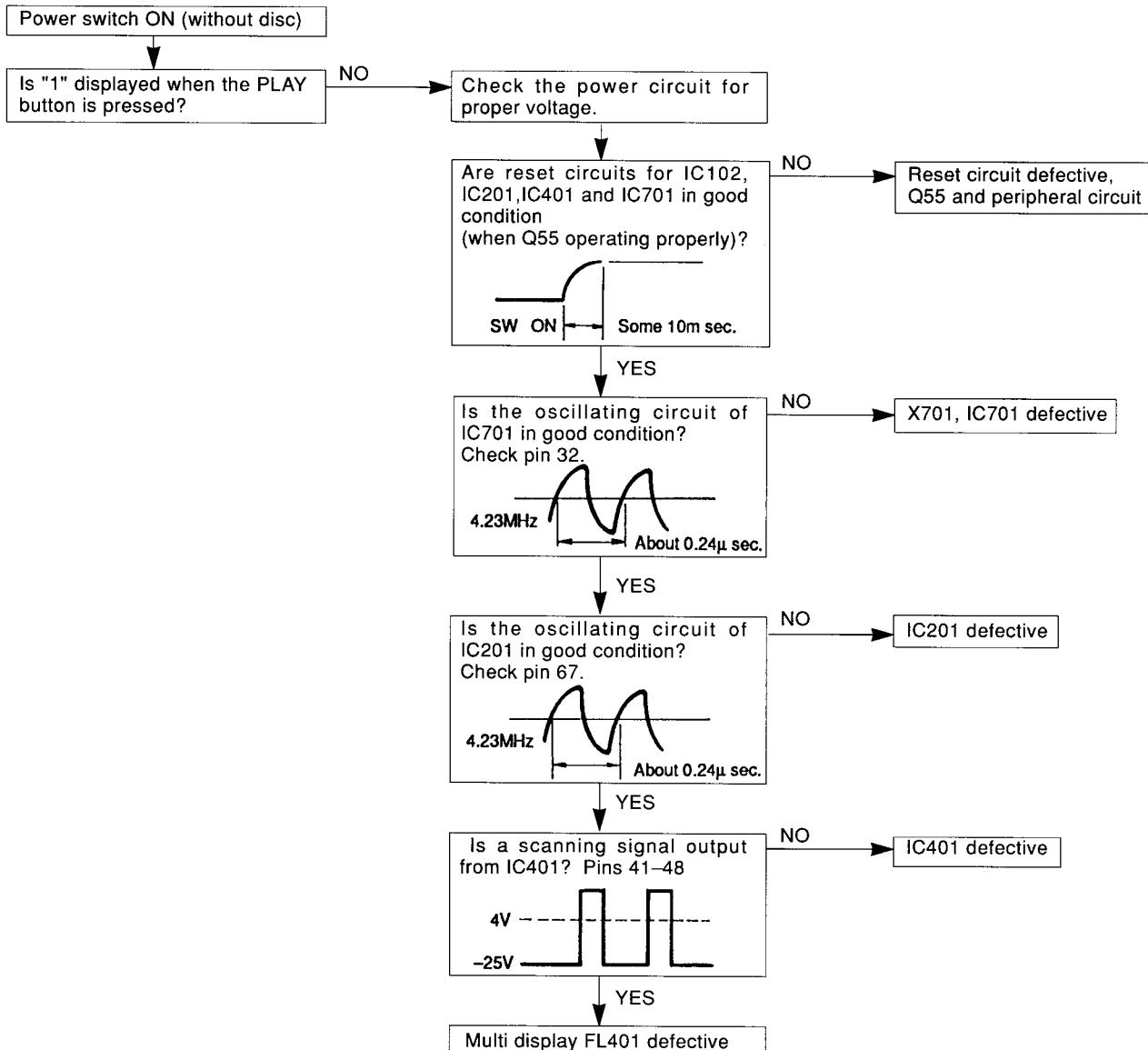


TROUBLE SHOOTING

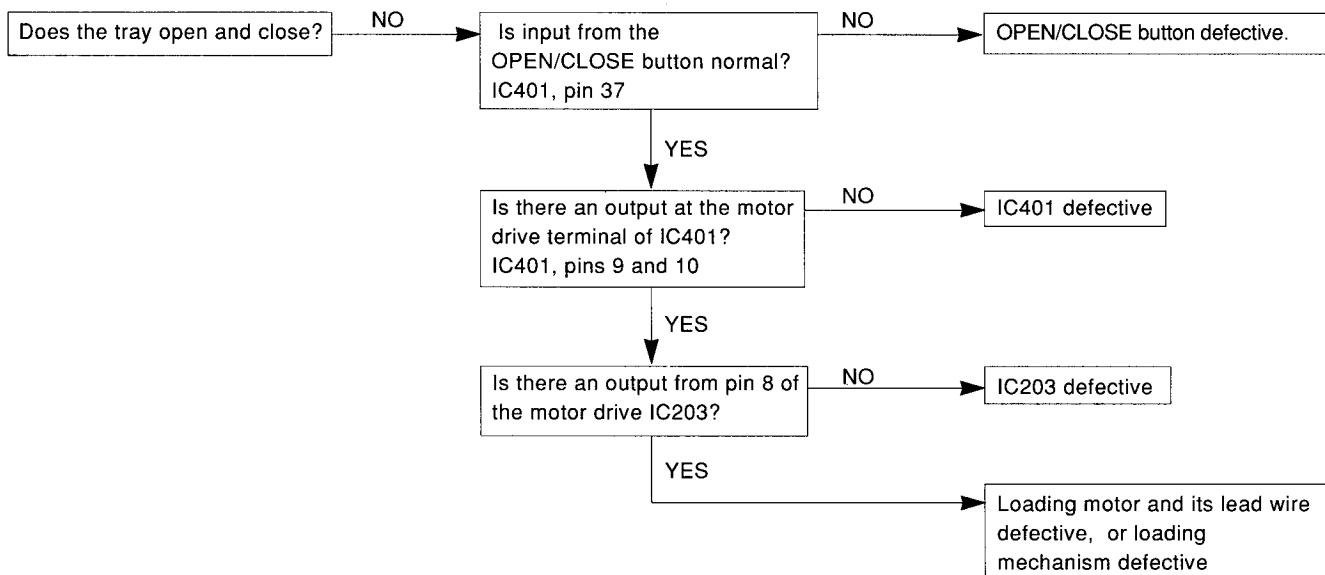
When a trouble has occurred, first check the pick-up lens for dirt and each connector for tight and secure connection. If the problem persists after checking both of these items, use the following check procedures.



(1) When Multi Display fails to light properly

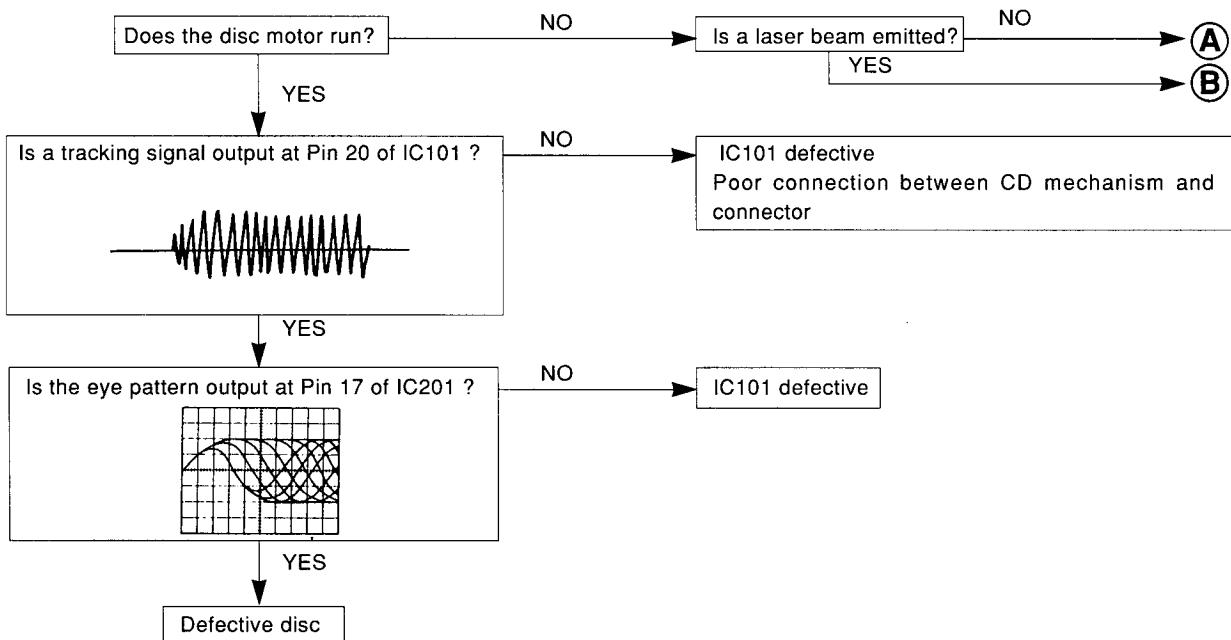


(2) When the tray fails to operate properly

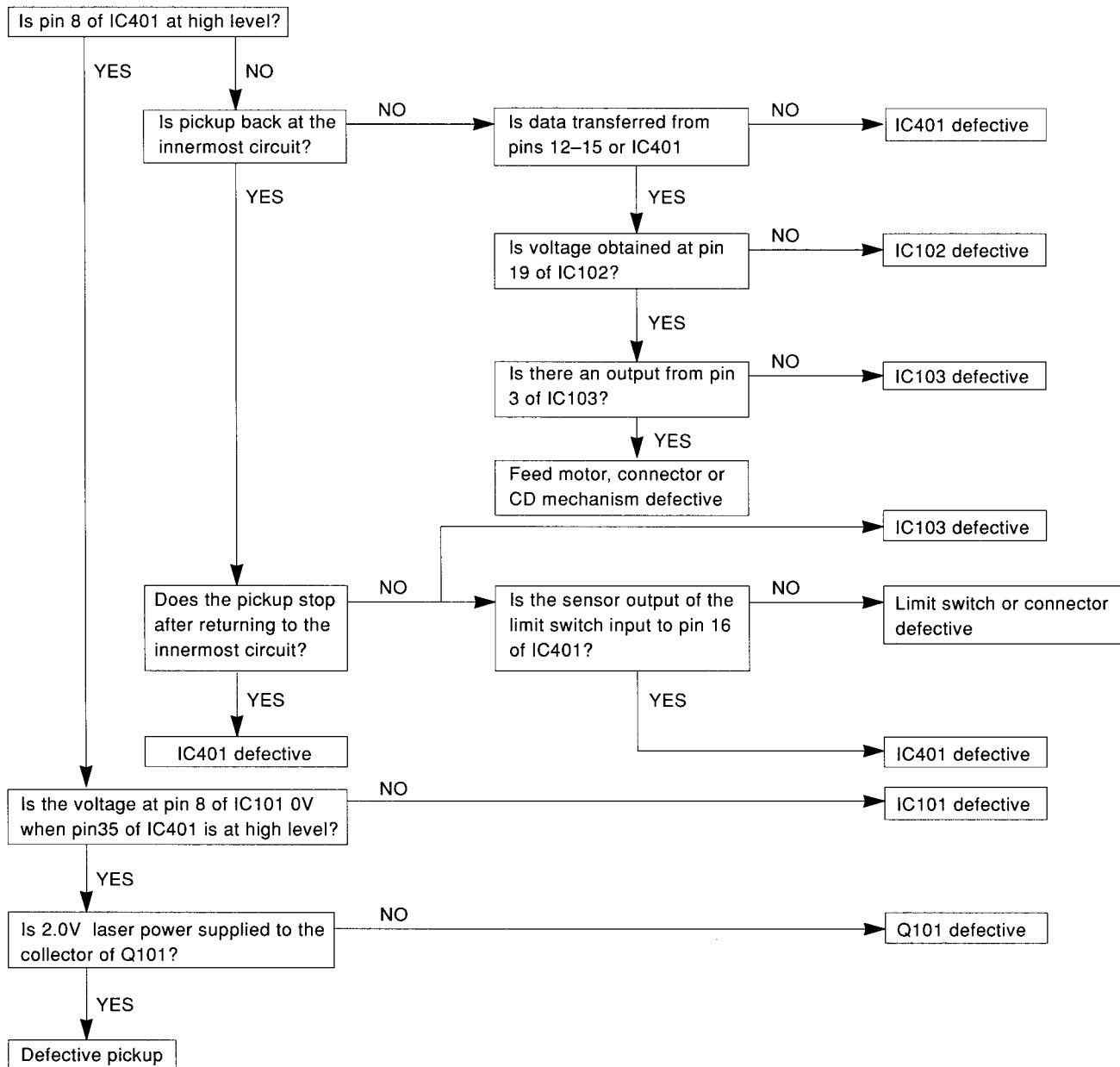


(3) When initial read cannot be executed

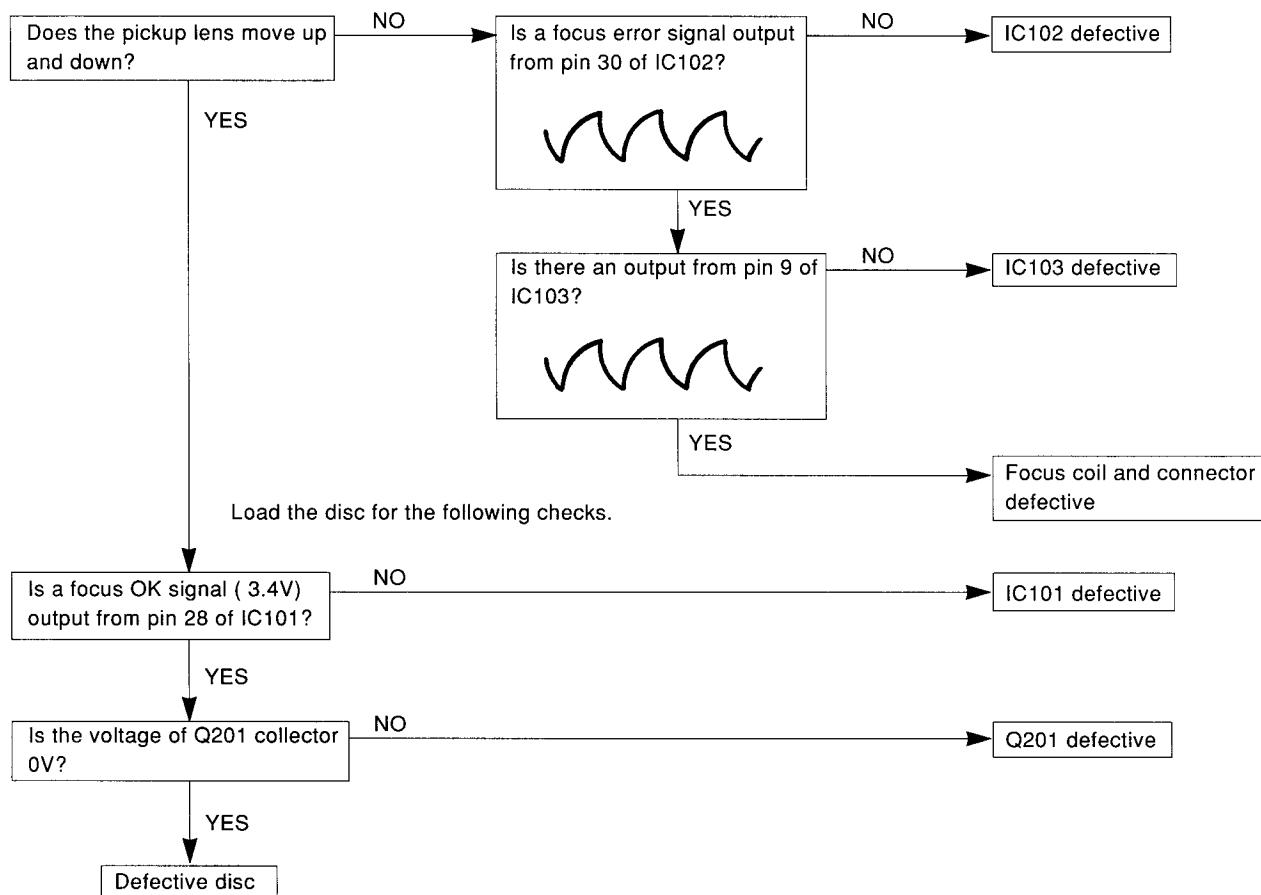
The following checks can be done without a disc loaded.



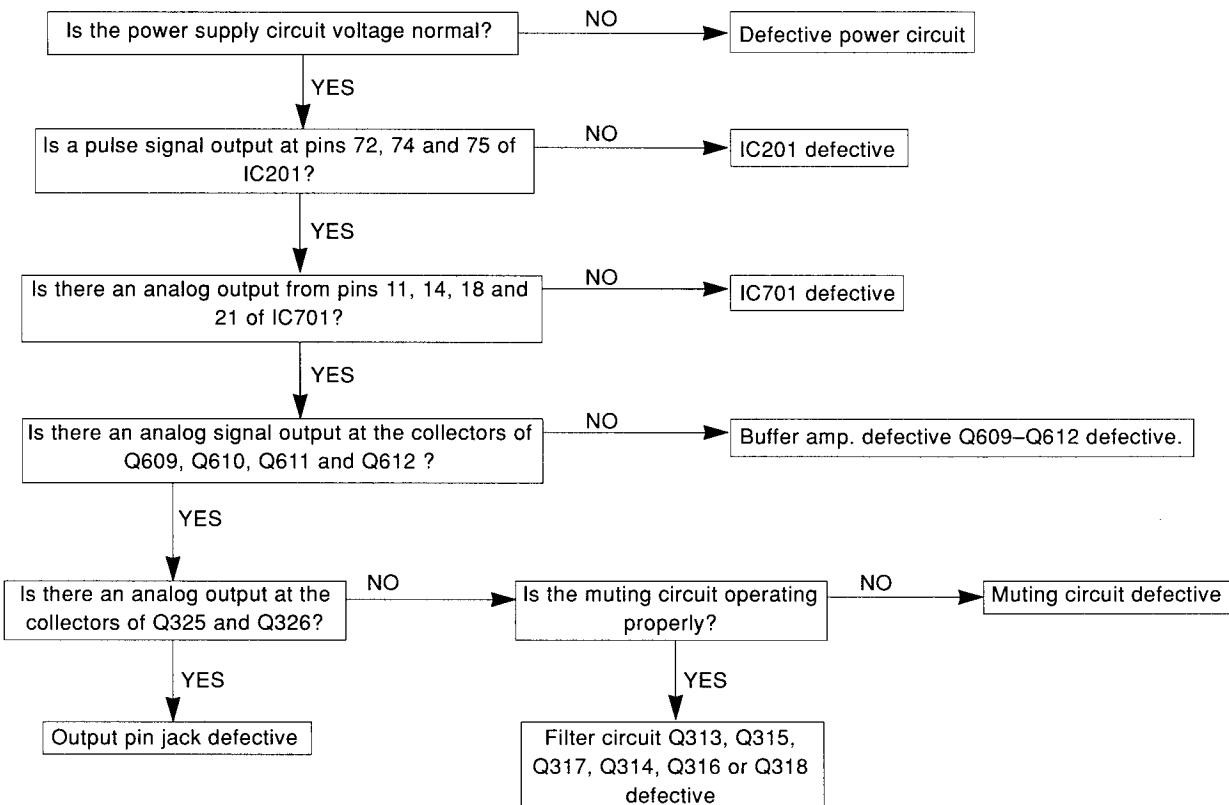
(A) When laser beam is not emitted



B When laser beam is emitted



When there is no audio output



IC TERMINAL FUNCTIONS

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC101	1	RFI		I	RF summing amp. input terminal.
	2	RFO		O	RF summing amp. output terminal.
	3	RF-		I	RF summing amp. return input terminal.
	4	P/N		I	Laser diode P/N select terminal.
	5	LD		O	APC LD amp. output terminal.
	6	PD		I	APC PD amp. input terminal.
	7	PD1		I	RF I-V amp. (1) invert input terminal.
	8	PD2		I	RF I-V amp. (2) invert input terminal.
	9	VC		—	GND terminal.
	10	F		I	F I-V amp. invert input terminal.
	11	E		I	E I-V amp. invert input terminal.
	12	EO		O	E I-V amp. output terminal.
	13	EI		I	E I-V amp. return input terminal.
	14	VR		O	DC voltage output terminal.
	15	CC2		I	DEFECT signal input terminal.
	16	CC1		O	DEFECT signal output terminal.
	17	V _{EE}		—	Negative power terminal.
	18	FE BIAS		I	Focus error amp. bias terminal.
	19	FE		O	Focus error amp. output terminal.
	20	TE		O	Tracking error amp. output terminal.
	21	DEFECT		O	DEFECT comparator output terminal.
	22	MIRR		O	MIRR comparator output terminal.
	23	CP		I	MIRR hold capacitor connect terminal.
	24	CB		I	DEFECT hold capacitor connect terminal
	25	DGND		—	GND terminal.
	26	ASY		I	Auto asymmetry control input terminal.
	27	EFM		O	EFM comparator output terminal.
	28	FOK		O	Focus OK comparator output terminal.
	29	LD ON		I	Laser diode on/off select terminal.
	30	Vcc		—	Positive power terminal.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC102	1	TE IN	Pre amp. input	I	Tracking error signal input terminal.
	2	TC IN	Pre amp. input	I	Track cross signal input terminal.
	3	SHOCK IN	Pre amp. input	I	Shock detector signal input terminal.
	4	HF OK	Pre amp. input	I	HF OK signal input terminal.
	5	MR	Pre amp. input	I	Mirror detected signal input terminal.
	6	Jump Flag	Mi-com I/O	O	Outputs "H" under jump function.
	7	<u>HFD</u>	Mi-com I/O	O	"1" indicates that MR input is "1" and track servo loop is off (TS OFF, JF, JR).
	8	T•HLD	Track servo	I	Direct control terminal for TS1 switch. "1" indicates that TS1 switch is "b". "0" indicates that priority is given to controller command.
	9	DATA OUT	Mi-com I/O	O	Inner condition output changed by command modes.
	10	<u>JP1</u>	Mi-com I/O	I	1 track jump control signal. Usually "H".
	11	MSD	Mi-com I/O	I	Serial data input terminal (LSB first, 8-bit data).
	12	<u>MLA</u>	Mi-com I/O	I	Latch signal of serial data from mi-com to servo IC.
	13	MCK	Mi-com I/O	I	Clock signal of serial data from mi-com to servo IC. Receives data on leading edge.
	14	<u>ACLR</u>	Mi-com I/O	I	All clear input. (Clear inner registers and flip-flops by "L" signal.)
	15	Bias	Power supply	O	Vcc/2 bias power supply output when single-pole power supply is used.
	16	COM	Power supply	I	Common terminal. Connect to GND when double-pole power supply is used and to BIAS when single-pole power supply is used.
	17	GND	Power supply	I	GND terminal.
	18	VEE	Power supply	I	Negative power terminal. Connect to GND when single-pole power supply is used.
	19	SS OUT	Slide servo	O	Operation amplifier SS output.
	20	SS ⊖	Slide servo	I	Operation amplifier SS reverse input.
	21	SS ⊕	Slide servo	I	Operation amplifier SS non-reverse input.
	22	TS OUT	Track servo	O	Operation amplifier TA output.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC102	23	TS⊖	Track servo	I	Operation amplifier TA reverse input.
	24	TG2	Track servo	—	Track gain select switch TG2 output. Set to open or common level.
	25	TS⊕	Track servo	I	Operation amplifier TA non-reverse input.
	26	TG1	Track servo	—	Track gain select switch TG1 output. Set to open or common level.
	27	TE OUT	Track servo	O	Operation amplifier TE output.
	28	TE⊖	Track servo	I	Operation amplifier TE reverse input.
	29	FSR IN	Focus servo	I	Focus search voltage level detector input.
	30	FS OUT	Focus servo	O	Operation amplifier FA output.
	31	FS⊖	Focus servo	I	Operation amplifier FA reverse input.
	32	FS⊕	Focus servo	I	Operation amplifier FA non-reverse input.
	33	FG	Focus servo	—	Focus gain select switch FG output. Set to open or common level.
	34	C-FSR	Focus servo	—	Connect capacitor which determines time constant of focus search waveform.
	35	I-Ref	Power supply	I	Terminal for connecting resistor for setting current value of reference current source.
	36	Vcc	Power supply	I	Positive power terminal.
IC103	1	+VIN 1		I	Positive input terminal 1 for amp. 1.
	2	-VIN 1		I	Negative input terminal 1 for amp. 1.
	3	OUT 1	O	O	Output terminal 1.
	4	OUT 2	O	O	Output terminal 2.
	5	-VIN 2		I	Negative input terminal 2 for amp. 2.
	6	+VIN 2		I	Positive input terminal 2 for amp. 2.
	7	+VIN 3		I	Positive input terminal 3 for amp. 3.
	8	-VIN 3		I	Negative input terminal 3 for amp. 3.
	9	OUT 3	O	O	Output terminal 3.
	10	NC		—	

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC103	11	NC		—	
	12	Vcc		—	Positive power supply.
IC201	1	EMP		O	Emphasis code output. Emphasis = "1"
	2	PWM1		O	Disc motor drive PWM output 1. -
	3	PWM2		O	Disc motor drive PWM output 2. +
	4	DOTX		O	Digital output.
	5	ACRCY		I	Channel status clock input.
	6	TEST1		I	Test mode select input.
	7	DOBSEL		I	Output data bit select.
	8	DASEL1		I	D/A converter interface select input 1.
	9	DASEL2		I	D/A converter interface select input 2.
	10	DASEL3		I	D/A converter interface select input 3.
	11	DASEL4		I	D/A converter interface select input 4.
	12	MSD		I	Microcomputer interface serial data input.
	13	MCK		I	Microcomputer interface shift clock input.
	14	MLA		I	Microcomputer interface data latch clock input
IC202	15	ACLR		I	Microcomputer interface register clear input.
	16	HFD		I	Play signal absence signal input.
	17	HF		I	Play signal input.
	18	IREF		I	Detection/PLL circuit reference current input.
	19	TLC		O	Slice level control output.
	20	LPF		I/O	PLL loop filter connection terminal.
	21	LOCK/DRD		O	Synchronous/low disc rotate status output.
	22	SYCLK		O	Frame synchronous status output.
	23	VDD2		I	5V power supply used for Detection/PLL circuit analog area only.
	24	DRD		O	Low disc rotate status output.
	25	EFFK		O	EFM frame clock output.
	26	SCINT		O	Subcode Q interrupted signal output.
	27	SQRO		O	Subcode Q register output.
	28	SQRCK		I	Subcode Q register data shift clock input.
	29	SCOR		O	Subcode synchronous signal output.

IC number	Terminal number	Port name	Terminal Code	I/O	Outline of functions
IC201	30	CRCF		O	Subcode Q CRC check result output.
	31	SCCK		I	Shift clock input for serial subcode data output.
	32	Vss2		I	GND terminal.
	33	SCOE2		I	Subcode parallel output channel P to S enable input.
	34	SCOE1		I	Subcode parallel output channel T to W enable input.
	35	SBCW		O	Subcode channel W output.
	36	SBCV		O	Subcode channel V output.
	37	SBCU		O	Subcode channel U output.
	38	SBCT		O	Subcode channel T output.
	39	SBCS		O	Subcode channel S output.
	40	SBCR		O	Subcode channel R output.
	41	SBCQ		O	Subcode channel Q output.
	42	SBCP		O	Subcode channel P output.
	43	RAS		O	Row address strobe signal output.
	44	NC		—	
	45	RDB2		I/O	External memory data I/O 2.
	46	NC		—	
	47	RDB1		I/O	External memory data I/O 1.
	48	RDB4		I/O	External memory data I/O 4.
	49	CAS		O	Column address strobe signal output.
	50	RDB3		I/O	External memory data I/O 3.
	51	WE		O	Write enable signal output.
	52	NC		—	
	53	RAD1		O	External memory address output 1.
	54	RAD2		O	External memory address output 2.
	55	RAD3		O	External memory address output 3.
	56	RAD7		O	External memory address output 7.
	57	RAD4		O	External memory address output 4.
	58	RAD5		O	External memory address output 5.
	59	RAD6		O	External memory address output 6.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC201	60	RAD0		O	External memory address output 0.
	61	EST2		O	Error status 2.
	62	EST1		O	Error status 1.
	63	VDD1		I	5V power supply.
	64	DOFK		O	OSC frame clock output.
	65	FSCK		O	Clock output. 44.1kHz
	66	C846		O	Clock output. 8.4672MHz
	67	C423		O	Clock output. 4.2336MHz
	68	C16MI		I	1/2 divider input.
	69	C8MO		O	1/2 divider output.
	70	XI		I	Crystal oscillator input.
	71	XO		O	Crystal oscillator output.
	72	DO1		O	D/A converter serial data output.
	73	Vss1		I	GND terminal.
	74	DSCK		O	D/A converter data shift clock output.
	75	LRCK		O	D/A converter left/right clock output.
	76	DO2		O	Dual D/A converter serial data output.
	77	WDCK		O	D/A converter word clock.
	78	DLRCK		O	D/A converter left/right clock output.
	79	APTL		O	D/A converter deglitch clock L.
	80	APTR		O	D/A converter deglitch clock R.
IC202	1	OE		I	Output enable input.
	2	DQ1		I/O	Data I/O.
	3	DQ2		I/O	Data I/O.
	4	W		I	Write control input.
	5	RAS		I	Row address strobe input.
	6	A6		I	Address input.
	7	A5		I	Address input.
	8	A4		I	Address input.
	9	Vcc		—	Power supply (5V).
	10	A7		I	Address input (row address only).

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC202	11	A3		I	Address input.
	12	A2		I	Address input.
	13	A1		I	Address input.
	14	A0		I	Address input (row address only)
	15	DQ3		I/O	Data I/O.
	16	CAS		I	Column address strobe input.
	17	DQ4		I/O	Data I/O.
	18	Vss		—	Power supply (0V).
IC203	1	VSENCE1			
	2	OUT1		O	Output terminal 1.
	3	-VIN1		I	Negative power supply.
	4	+VIN1		I	Positive input 2.
	5	V _{EE}			Negative input 2.
	6	+VIN2		I	Positive power supply.
	7	-VIN2		I	Negative input 2.
	8	OUT2		O	Output terminal 2.
	9	VSENCE2			
	10	Vcc			Positive power supply.
IC401	1	Vcc			Positive power supply.
	2	P ₆₅		I/O	
	3	P ₆₄		I/O	
	4	P ₆₃		I/O	
	5	P ₆₂		I/O	
	6	P ₆₁		I/O	
	7	P ₆₀		I/O	
	8	P ₂₇	I/O port	I/O	Disc tray open direction driving output.
	9	P ₂₆	I/O port	I/O	Disc tray close direction driving output.
	10	P ₂₅	I/O port	I/O	
	11	P ₂₄	I/O port	I/O	Serial data output.
	12	P ₂₃	I/O port	I/O	Serial data output.
	13	P ₂₂	I/O port	I/O	Data latch clock output.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC401	14	P21	I/O port	I/O	
	15	P20	I/O port	I/O	
	16	P37	OUT PORT	O	Display output i.
	17	P36	OUT PORT	O	Display output g.
	18	P35	OUT PORT	O	Display output f.
	19	P34	OUT PORT	O	Display output e.
	20	P33	OUT PORT	O	Display output d.
	21	P32	OUT PORT	O	Display output c.
	22	P31	OUT PORT	O	Display output b.
	23	P30	OUT PORT	O	Display output a.
	24	P53			
	25	P52			
	26	CNVss			Usually connected to Vss.
	27	RESET		I	Reset input.
	28	XIN	CLOCK IN	I	Clock input.
	29	XOUT	CLOCK OUT	O	Clock output.
	30	XCIN	CLOCK IN	I	Serial data input.
	31	XCOUT	CLOCK OUT	O	Clock output.
	32	Vss			Power Supply.
	33	Ø		O	Timing output.
	34	P57			
	35	P56			
	36	P55			
	37	P54			
	38	VP		I	Pull down voltage input.
	39	P51			
	40	P50			
	41	P17	OUT PORT	O	Laser diode control output.
	42	P16	OUT PORT	O	Display output 7.
	43	P15	OUT PORT	O	Display output 6.
	44	P14	OUT PORT	O	Display output 5.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC401	45	P1 ₃	OUT PORT	O	Display output 4.
	46	P1 ₂	OUT PORT	O	Display output 3.
	47	P1 ₁	OUT PORT	O	Display output 2.
	48	P1 ₀	OUT PORT	O	Display output 1.
	49	P0 ₇	I/O port	I/O	Serial data input.
	50	P0 ₆	I/O port	I/O	Frame synchronous state output.
	51	P0 ₅	I/O port	I/O	Low disc rotate state output.
	52	P0 ₄	I/O port	I/O	Sub-code synchronous signal output.
	53	P0 ₃	I/O port	I/O	Sub-code Q check.
	54	P0 ₂	I/O port	I/O	Sub-code channel Q output.
	55	P0 ₁	I/O port	I/O	Interrupt input.
	56	P0 ₀	I/O port	I/O	
	57	P4 ₇			
	58	P4 ₆			
	59	P4 ₅			
	60	P4 ₄			
	61	P4 ₃			
	62	P4 ₂			
	63	P4 ₁			
	64	P4 ₀			
IC501	1	OUT1		O	Output terminal 1.
	2	I ₁ ₁		I	Negative input terminal 1 for amp. 1.
	3	I _N ₁		I	Positive input terminal 1 for amp. 1.
	4	V-			Negative power supply.
	5	I _N ₂		I	Positive input terminal 2 for amp. 2.
	6	I ₁ ₂		I	Negative input terminal 2 for amp. 2.
	7	OUT2		O	Output terminal 2.
	8	V+			Positive power supply.
IC701	1	MLD		I	Mi-com command load input terminal. Low level=load
	2	RSTB			Reset terminal. Low level=active
	3	IE		I	Low level=LSI format. High level=I2S format.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC701	4	TP1		O	Digital filter test output terminal 1.
	5	TP2		O	Digital filter test output terminal 2.
	6	TEST 1		I	Digital filter test signal input terminal 1. Nominal=Low level
	7	TEST 2		I	Digital filter test signal input terminal 2. Nominal=Low level
	8	NC			
	9	NC			
	10	AVDD 4			Analog power supply terminal 4. (+5V)
	11	OUTL(-)		O	L ch. negative PWM output terminal.
	12	AVSS 4			Analog ground terminal 4.
	13	AVSS 3			Analog ground terminal 3.
	14	OUTL(+)		O	L ch. positive PWM output terminal.
	15	AVDD 3			Analog power supply terminal 3. (+5V)
	16	NC			
	17	AVDD 2			Analog power supply terminal 2. (+5V)
	18	OUTR(+)		O	R ch. positive PWM output terminal.
	19	AVSS 2			Analog ground terminal 2.
	20	AVSS 1			Analog ground terminal 1.
	21	OUTR(-)		O	R ch. negative PWM output terminal.
	22	AVDD 1			Analog power supply terminal 1. (+5V)
	23	DVDD 1			Digital power supply terminal 1 for oscillator. (+5V)
	24	DVSS 1			Digital ground terminal 1 for oscillator.
	25	X2			Crystal oscillator.
	26	X1			Crystal oscillator (external clock input terminal).
	27	NC			
	28	DVDD 2			Digital power supply terminal 2. (+5V)
	29	DVSS 2			Digital ground terminal 2.
	30	NSUB			Connect to the D-VDD terminal.
	31	ZFLGB		O	Zero signal detect output terminal.
	32	192FS		O	Clock output. 8.4672MHz
	33	LRPOL			Polarity (L ch. and R ch.) select terminal. L level=R H level=L
	34	LRCLK		I	LRCLK input terminal.
	35	BCLK		I	Serial input bit clock.

IC number	Terminal number	Port name	Terminal code	I/O	Outline of functions
IC701	36	SRDATA		I	Serial data input terminal.
	37	DVSS 3			Digital ground terminal 3.
	38	DVDD			Digital power supply terminal. (+5V)
	39	384FS		O	Clock output. 16.9344MHz
	40	PD			Power down terminal. High level=active
	41	MDATA		I	Mi-com command data input terminal.
	42	MCLK		I	Clock input terminal for mi-com command.
IC801	1	1Y		O	Output terminal 1.
	2	1A		I	Input terminal 1.
	3	1B		I	Input terminal 1.
	4	2Y		O	Output terminal 2.
	5	2A		I	Input terminal 2.
	6	2B		I	Input terminal 2.
	7	GND			Ground terminal.
	8	3A		I	Input terminal 3.
	9	3B		I	Input terminal 3.
	10	3Y		O	Output terminal 3.
	11	4A		I	Input terminal 4.
	12	4B		I	Input terminal 4.
	13	4Y		O	Output terminal 4.
	14	Vcc			Power supply terminal.
IC901	1	GND			Ground terminal
	2	VOUT1			Output terminal 1
	3	VZ1			Capacitor connecting terminal
	4	VR			Voltage control terminal
	5	FIN			Operating control terminal
	6	RIN			Operating control terminal
	7	VCC1			Power supply terminal 1
	8	VCC2			Power supply terminal 2
	9	VZ2			Capacitor connecting terminal
	10	VOUT2			Output terminal 2

CIRCUIT DESCRIPTION

1. APC CIRCUIT

A semiconductor laser is used as the light source for the optical pickup. As the output from the semiconductor laser changes radically with changes in temperature, a circuit must be provided to stabilize this output. For this purpose, a monitor diode which detects the optical output of the laser diode is used in the semiconductor laser.

As the laser diode emits light from its bonded surface, light is emitted both in front and behind. The light emitted behind is monitored with the monitor diode installed on its rear surface, and the optical output is thus controlled. The light emitted in front becomes the light source for the pickup.

Fig. 1 shows the APC circuit.

When the temperature rises and the optical output decreases, the monitor diode current (I_S) decreases, the electric potential of IC101 pin 5 rises, the base current of the driving transistor increases, and the laser diode current increases. This causes the reduced optical output to return to its former level.

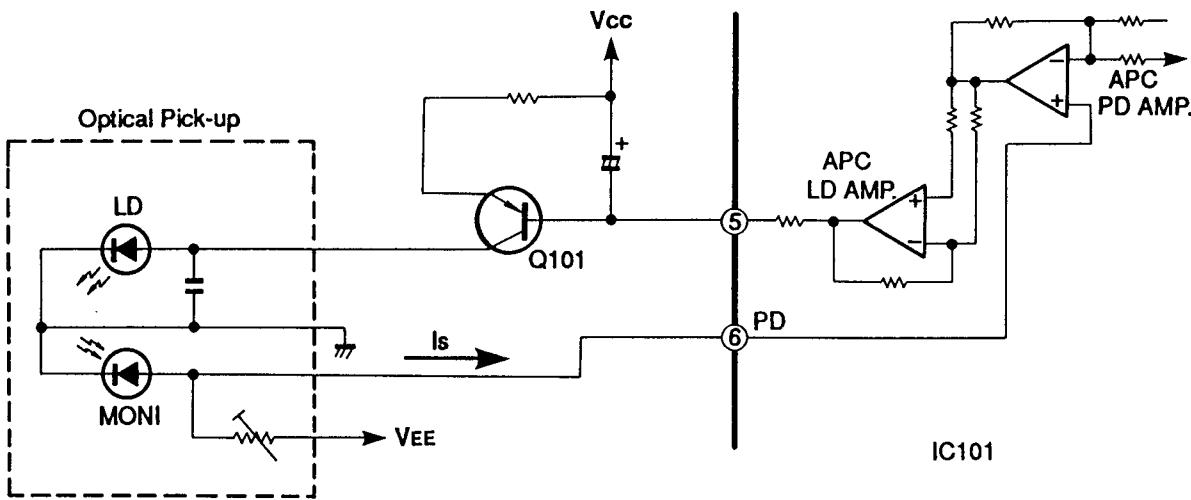


Fig. 1

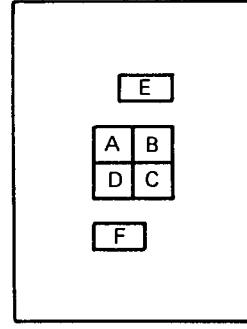
2. FOCUS SERVO

2-1. Optical pickup

This set employs a three-beam optical pickup comprised of six division photodiodes, A through F as shown in Fig. 2. The four photodiodes (A through D) at the center provide focus error detection by using their property to allow the beam to focus into a round image only at a certain point.

The sums of outputs from diagonal two elements of four division photodiodes (A+C and B+D) are compared by the differential amplifier in IC101 to detect the shape of the beam image.

The remaining two diodes (E and F) provide tracking error detection by means of sub-beam spots.



Three spotted (six-division)
photo diodes

Fig. 2

2-2. Focus error detecting operation

The reflected laser beam from a disc is polarized 90° with the beam-splitter and sent to the cylindrical lens. The beam passed through this cylindrical lens is then sent to the four division photodiodes and focuses into an image whose shape varies with the distance between the disc and the objective lens. Such change in the beam shape causes the current flowing from the photodiodes to vary.

Shown in Fig. 3 is the principle of the focus error detection.

The currents from the photodiodes (A+C and B+D) are applied to pins 7 and 8 of IC101 and converted to voltage by RF I-V amplifiers (1) and (2) included in IC101.

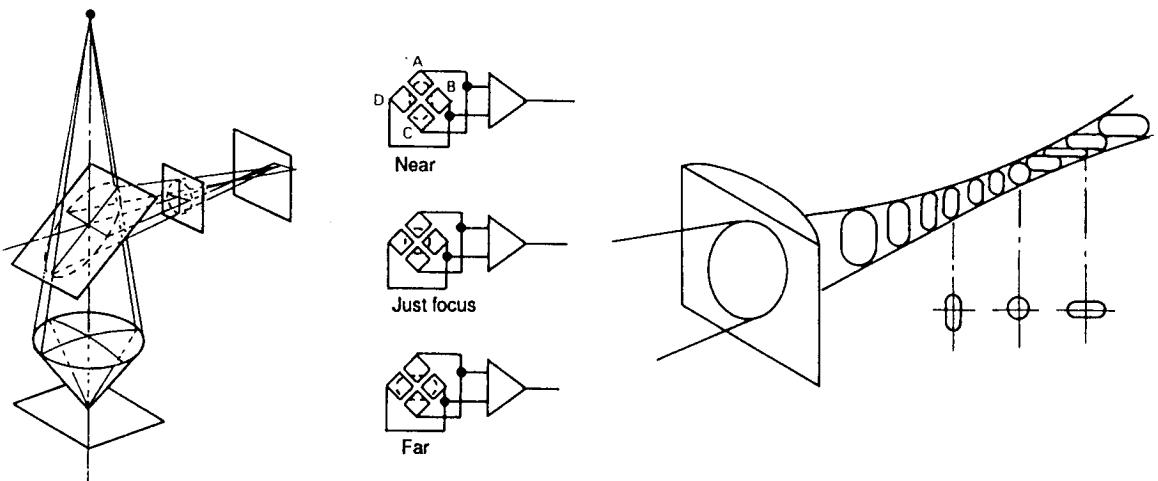


Fig.3

2-3. Focus servo control operation

The focus error signal, after being converted to voltage by the RF I-V amplifier, is transmitted to the operation amplifier in the IC and output from pin 19.

When the disc to objective lens distance is in just focus, the beam forms a true round. In this state, the beams applied to four elements of four division photodiodes become equal and thus the output provided then is 0(zero). When the disc to objective lens distance is too close (near focus), the beam is reflected divergently to form an oval in crosswise direction. In this state, the outputs provided from photodiodes A and C are higher than those from B and D, resulting in negative (-) output voltage. On the other hand, when the distance is too far (far focus), the beam is reflected convergently to form an oval in longitudinal direction. Then the outputs from photodiodes B and D are higher, resulting in positive (+) output.

The output voltage(focus error signal) from pin 19 of IC101 passes through IC102, in from pin 32 and out from pin 30, and IC103, in from pin 7 out from pin 9 as shown in Fig. 4. It is amplified in each IC and fed to the focus coil which then drives the objective lens of the pickup.

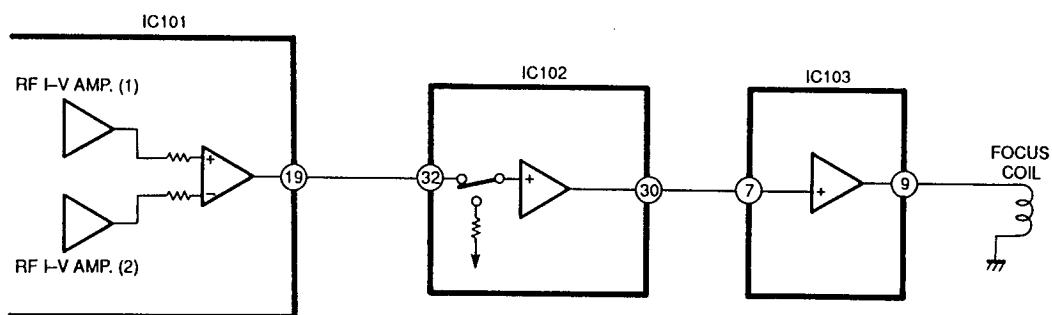


Fig. 4

2-4. Tracking error detection system

Fig. 5 shows the principle of the tracking error detection system which employs the three beam system.

The laser beam is divided into the main beam and two sub-beams by diffraction grating and they are arranged on one line. The center line connecting these three beams has a slight offset angle against the main beam. The main beam is received by photodiodes A, B, C and D and two sub-beams by E and F respectively.

Fig. 5-A shows the on-track state. As both auxiliary beams 1 and 2 are slightly on the track in this state, the outputs of photodiodes E and F are equal and the tracking signal is 0(zero). When the track is shifted to the left (Fig. 5-B), the auxiliary beam 1 is off the pit. This allows more light to be received by the photodiode E, resulting in positive (+) tracking signal output. On the other hand, when the track is shifted to the right (Fig. 5-C), the amount of light received by the photodiode F increases, resulting in negative (-) tracking signal output. And these extreme signals are detected as tracking error signals.

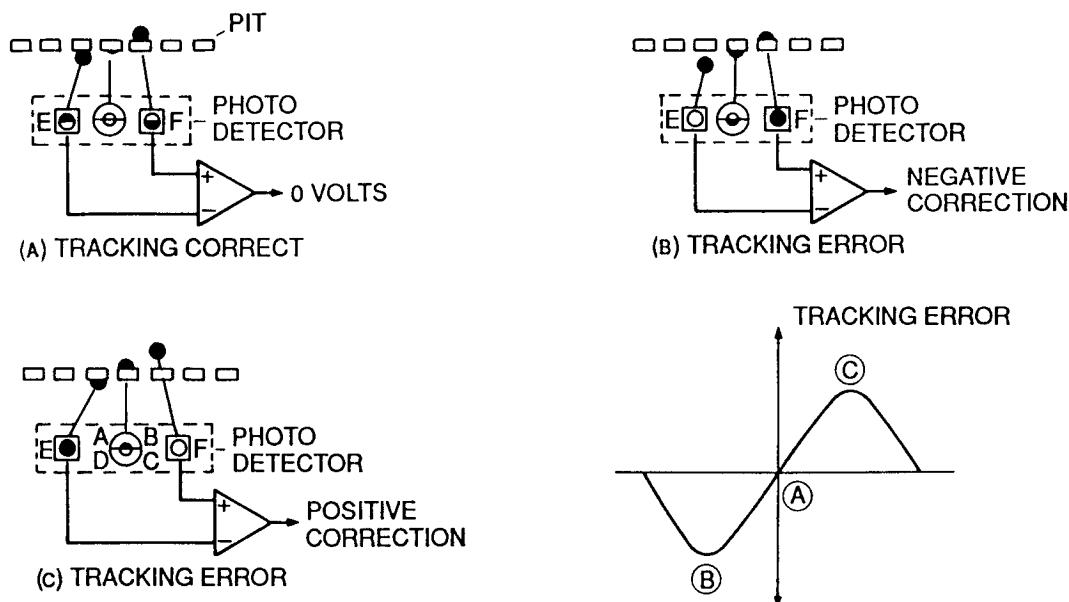


Fig. 5

2-5. Tracking servo control operation

When a tracking error signal is detected by photodiodes E and F, it is fed to pins 11 and 10 of IC101 respectively as shown in Fig. 6. In IC101, the signal is converted into voltage by the E I-V amplifier and F I-V amplifier, transmitted to the tracking error amplifier and output through pin 20. While it passes through IC102, in from pin 1 and out from pin 22, and IC103, in from pin 6 and out from 4, it is amplified in each IC and sent to the tracking coil to adjust pickup so that the amount of track shift is reduced as closely to none as possible.

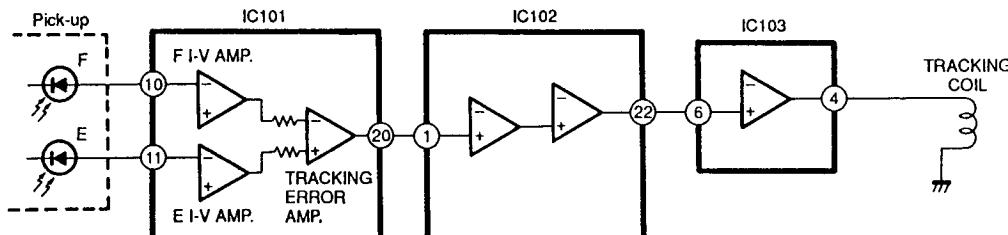


Fig. 6

3. Regenerative Circuit

3-1. RF circuit

The currents from photodiodes (a, b, c and d) are fed to IC101 through pins 7 and 8 and converted to voltage by RF I-V amplifiers (1) and (2) respectively there, added by the RF summing amplifier and output from pin 2 as a signal. As it is sent to pin 7 of IC201, it can be checked at the test point (TP201) provided on its way by means of the eye pattern check.

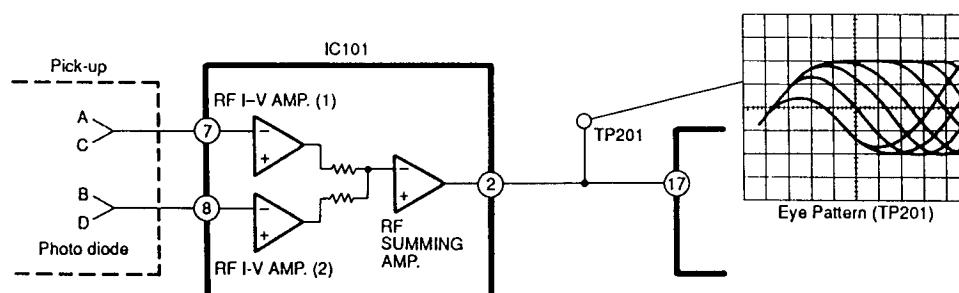


Fig. 7

3-2. EFM demodulation, error correction, serial/parallel conversion

EFM demodulation, error correction and serial/parallel conversion are performed by the internal circuitry of IC201. The eye-pattern signals from pin 2 of IC101 are sent to pin 17 of IC201, then demodulated from 14 bits to 8 bits by EFM readjustment. At the same time any error, if found, is corrected (CIRC) and the signals are sent to the D/A converter interface. After that, they are output as 18-bit digital signals from pins 72, 74 and 75 of IC201 and fed to the D/A converter of IC701.

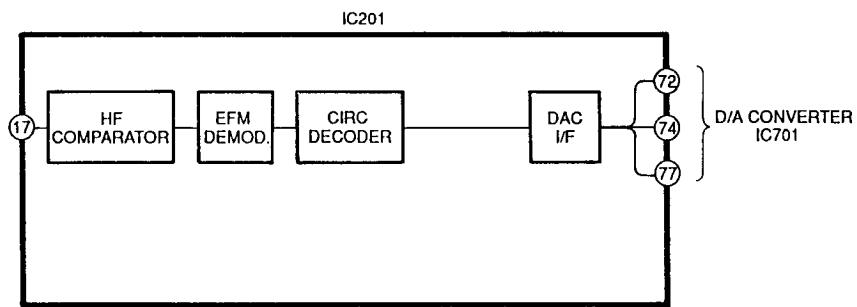


Fig. 8

4. 1-bit D/A Converter

Conventionally, high-precision D/A conversion was mainly carried out using resistor ladder systems. There was, however, one main drawback with these systems, which was that zero cross distortion was likely to be generated. To achieve production of a high-precision D/A converter with a minimum amount of zero cross distortion, it would be necessary to carry out complicated and bothersome processes such as laser trimming processing. In addition, use of such D/A converter would require a sampling-and-hold circuits(or de-glitcher circuit) and an intricate analog filter with special characteristics when mounting it.

To solve these difficulties, we used a 1-bit D/A converter with 3rd order noise shaping technology (IC701: MN 6471M).

4-1. Features and Configuration of MN6471M

- Features
1. No zero cross distortion
 2. No non linear distortion
 3. Built-in 4 times oversampling digital filter
 4. 2 channels (left and right) built in
 5. 4DAC configuration possible
 6. Single 5V power supply operation

The block diagram is shown in Fig. 9. The MN6471M is configured of a digital filter, a 3rd order noise shaping circuit, and a PWM.

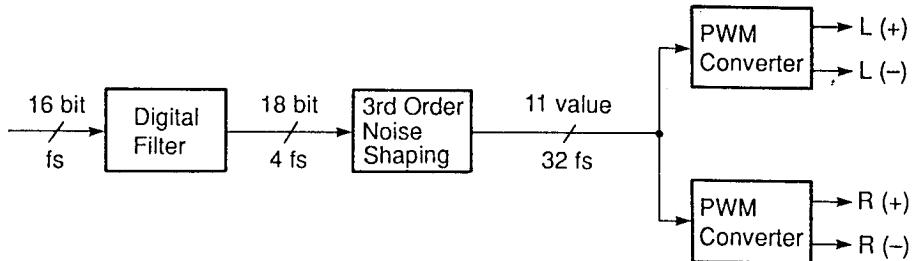


Fig. 9

4-2. Configuration of MN6471M

Fig. 10 shows the configuration of the MN6471M. The sampling frequency of the input data is expressed in fs, so the 3rd order noise shaping circuit operates at 32fs. This means that a 32-times oversampling filter is required. In this LSI, however, oversampling is carried out first at 4fs in the first digital filter, and following that, a 0 order hold takes place in the 3rd order noise shaping circuit. This enables conversion of the 4fs signal to a 32fs signal.

The digital filter, using 384fs as clock signals, and the noise shaping section, uses 64fs, carry out time division processing on the data for the left and right channels. The PWM section, using 768fs as a clock signal, carries out signal processing for the left and right channels independently.

In the noise shaping section, the 19-bit 32fs signal is converted to 11 values and pulse width modulation (PWM) is carried out on these signals in the PWM section. D/A conversion is carried out in this way.

Following are the descriptions of the various blocks.

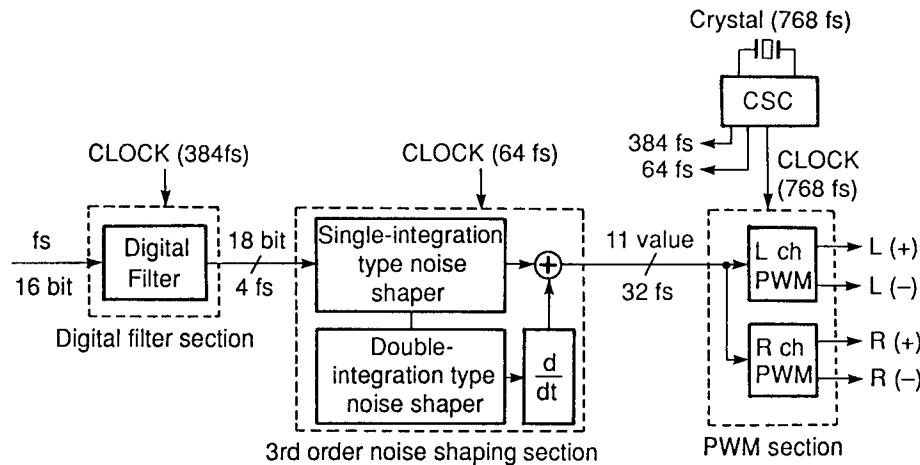


Fig. 10

5. Digital filter

Fig. 11 shows the signal data of an audio signal up to 20kHz that has been digitalized, along with the frequency distribution of the signal data. The graphs show the conditions for a sampling frequency of 44.1kHz, plus its doubled frequency, 88.2kHz, and its quadrupled frequency, 176.4kHz.

As the figure shows, for the same signal up to 20kHz, the noise portion of the digitalized signal component tends to shift toward the higher range of the signal if the sampling frequency is increased. However, at any sampling frequency, the volume of necessary audio signal information remains constant up to 20kHz. This allows certain important results to be derived; that is, if the information represented in section (a) is obtained, then it should be possible to create a signal in the form shown in (b) or (c).

When the noise caused by sampling shifts to the higher frequency range, as shown in (b) or (c), the low pass filter characteristic to eliminate noise during re-conversion to an audio signal need not be steep but can be rather gradual as shown. It is comparatively simple to provide a high audio quality low pass filter of such characteristic with little phase fluctuation or distortion.

The question now becomes how to make a signal sampled at 44.1kHz resemble one sampled at a much higher frequency. Fig. 12 shows the signal sequence sampled at the same 44.1kHz as in Fig. 11 and its frequency distribution.

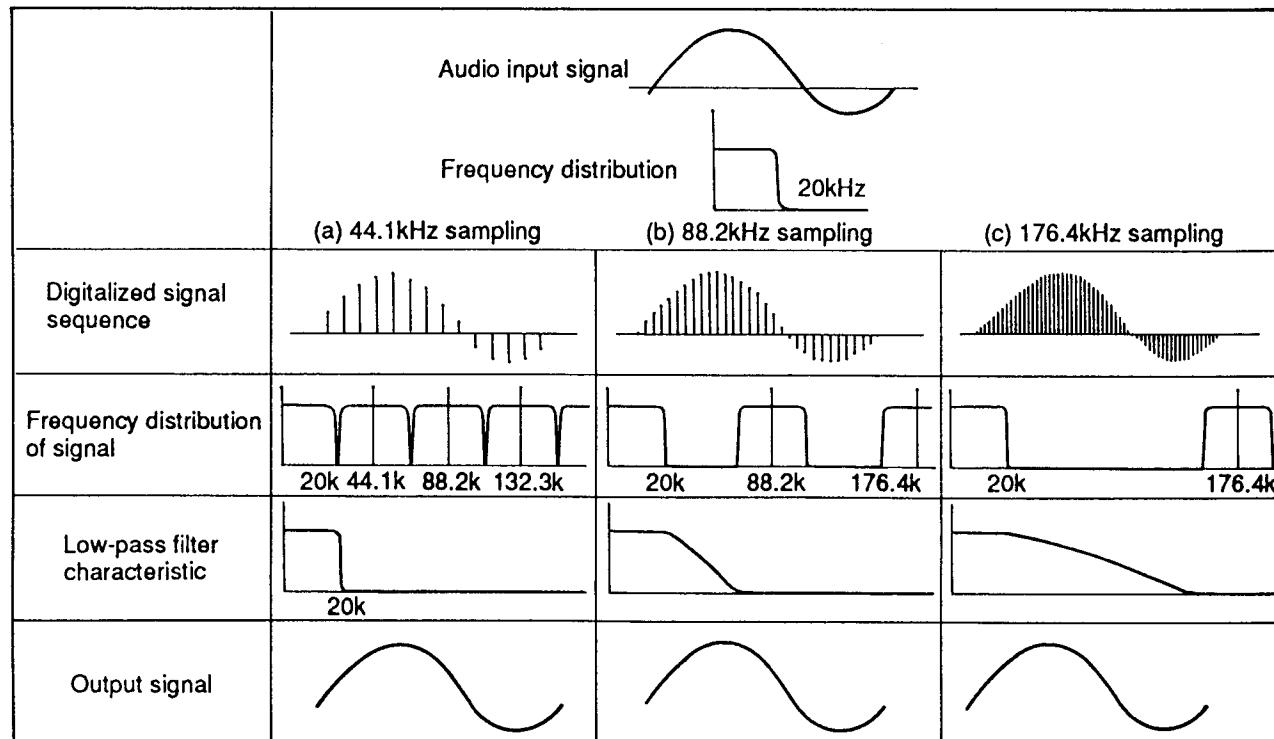


Fig. 11

If the frequency is to be doubled as the first step in increasing the sampling frequency of the signal, zero-level data is added between the data marked with X in Fig. 12(b). In the original signal sequence sampled at 44.1kHz, there are data only at the points of the sampling timing, while the intervals between those points have all been made zero-level. Introducing zero data in these intervals does not change the original data in any way, nor is the frequency distribution altered. Only the sampling frequency is doubled.

Passing this data in its modified form through a digital filter with the characteristic shown in Fig. 12(c) causes the portion corresponding to N₁ to be eliminated, resulting in a signal sequence with the frequency distribution shown in (d). This signal sequence possesses exactly the same shape as that obtained for the signal in Fig. 11(b), sampled at 88.2kHz.

In other words, this method enables the sampling frequency to be doubled.

The digital filter used in this unit is a Finite Impulse Response type. Its circuit diagram is shown in Fig. 13.

The sampling frequency of this unit has been quadrupled, and the phase characteristic has been improved by using a softer analog low-pass filter.

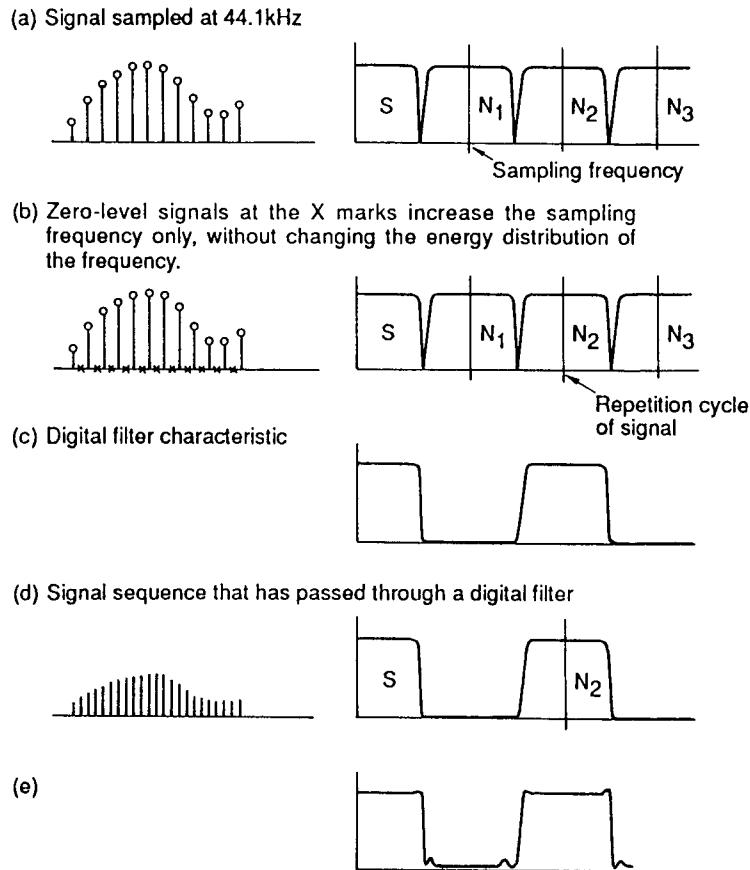


Fig. 12

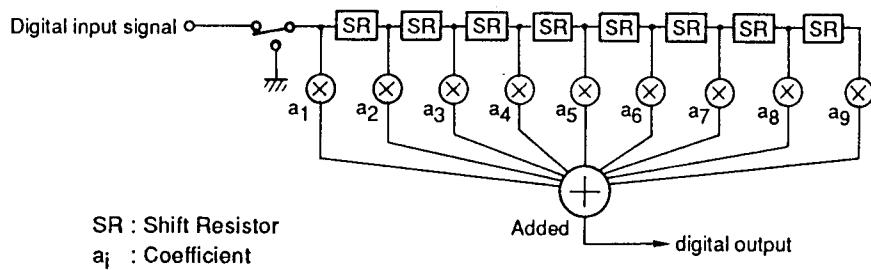


Fig. 13

6. Noise shaper

a) Single-integration noise shaper

The block diagram is shown in Fig. 14.

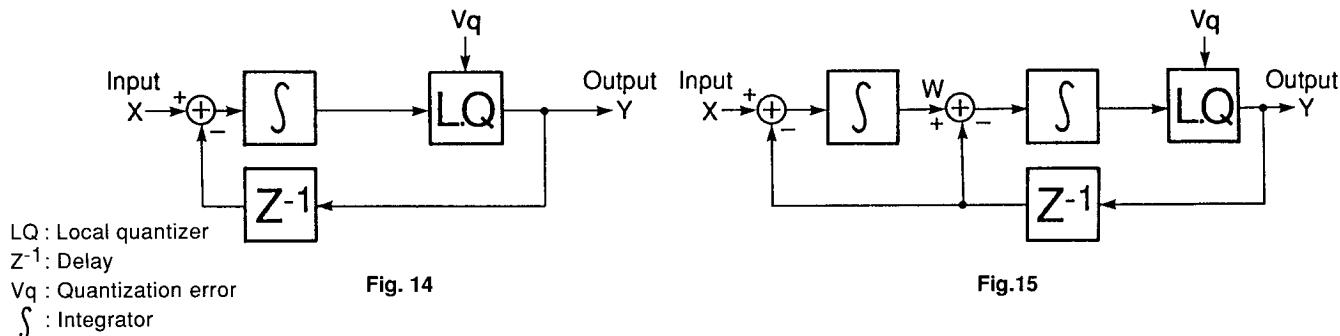
According to the figure, the relation between input X and output Y is as follows:

$$Y = X + (1 - Z^{-1}) Vq \quad \dots \dots \dots (1)$$

The quantization error Vq is a random value, and $(1 - Z^{-1})$ expresses the differential characteristic. Thus, according to equation (1), the spectrum of the quantization error Vq for the single-integration noise shaper has a characteristic of 6dB/oct, and the lower the frequency is, the greater the attenuation becomes. (This is because the noise distribution can be changed by the noise shaper).

b) Double-integration noise shaper

The block diagram is shown in Fig. 15.



In Fig. 15, the path to the output seen from W has a configuration identical to that of the single-integration noise shaper, so that relation between W and Y is:

$$Y = W + (1 - Z^{-1}) Vq \quad \dots \dots \dots (2)$$

The relation between X and Y is:

$$W = \frac{1}{1 - Z^{-1}} (X - Z^{-1} Y) \quad \dots \dots \dots (3)$$

And the result obtained from above equations (2) and (3) is:

$$Y = X + (1 - Z^{-1})^2 Vq \quad \dots \dots \dots (4)$$

Comparison with equation (1) shows that the term $(1 - Z^{-1})$ is a square of itself. In other words, with the double-integration noise shaper, the spectrum of the quantization error Vq is attenuated at a slope of 12dB/oct.

Fig. 16 shows the output spectrum of the noise shaper.

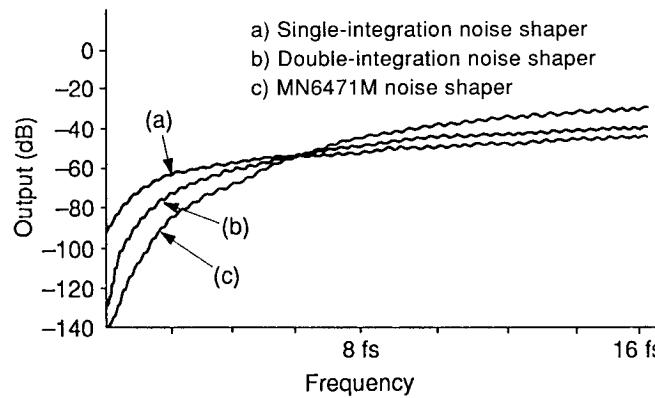


Fig. 16

6-2. 3rd order noise shaper

The block diagram of the 3rd order noise shaper is shown in Fig. 17.

This configuration shows that the first stage uses a single-integration noise shaper and the following stage uses a double-integration noise shaper. The quantization error of the first stage is input at the second stage.

In this configuration, single-integration and double-integration noise shapers are connected at several stages, and the quantization error of the previous stage is quantified again at the following stage, so that the quantization error included in the output of the previous stage is negated. In this way, compensation is carried out.

In this noise shaping circuit, the input is expressed as X, the output as Y, and the re-quantized error as Vq , and their relation for

each order is shown in the following equations.

$$(1\text{st order}) \quad Y = X + (1 - Z^{-1}) Vq$$

$$(2\text{nd order}) \quad Y = X + (1 - Z^{-1})^2 Vq$$

$$(3\text{rd order}) \quad Y = X + (1 - Z^{-1})^3 Vq$$

In noise shaping, as the order of the transfer coefficient called $(1 - Z^{-1})$ becomes larger, the noise in the 1/2 fs audio band moves higher in the frequency range. The result is that, within a narrow audio band, an 18-bit performance can be obtained even from a 1-bit DAC.

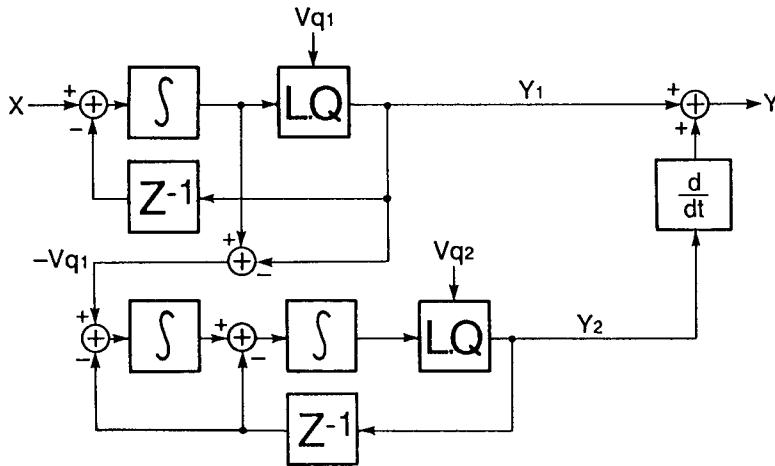


Fig. 17

$$Y_1 = X + (1 - Z^{-1}) Vq_1 \dots \dots \dots (5)$$

$$Y_2 = -Vq_1 + (1 - Z^{-1})^2 Vq_2 \dots \dots \dots (6)$$

$$Y = Y_1 + (1 - Z^{-1}) Y_2 \dots \dots \dots (7)$$

The result obtained from above equations (5) ~ (7) is:

$$Y = X + (1 - Z^{-1})^3 Vq_2 \dots \dots \dots (8)$$

7. PWM Output Section

In the output from the MN6471M noise shaper, the 11 value data of the 32fs is output. In the PWM section, pulse width modulation (PWM) is carried out on that signal, enabling D/A conversion.

Fig. 18 shows the PWM section of the MN6471M.

The 11 value digital data output from the noise shaper is converted (1-bit data stream) to pulse signal with 11 pulse widths precisely controlled by the crystal OSC and output as an analog signal. In the PWM output section, signals from both left and right channels are output as differential output so that the synchronous-phase noise is eliminated and the 2nd order high-frequency distortion is reduced.

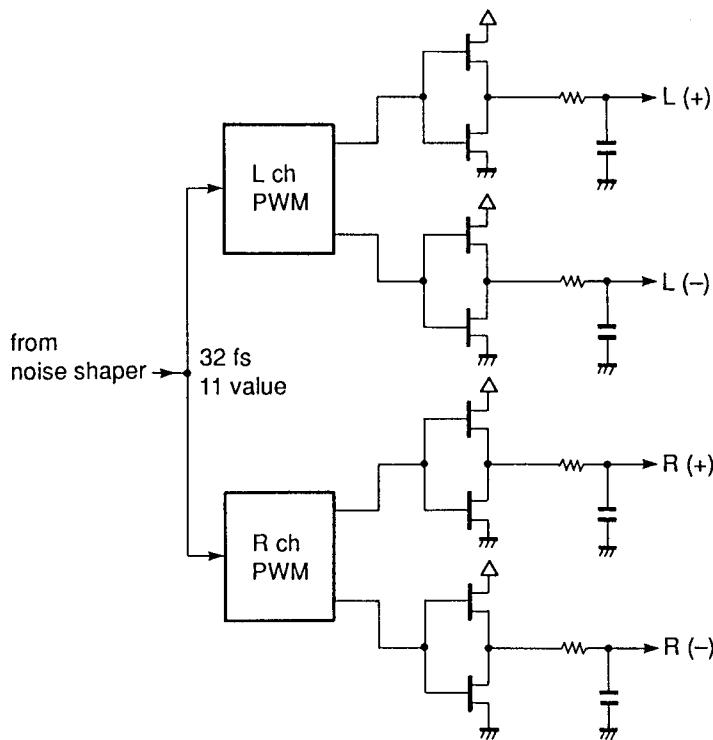


Fig. 18

8. Audio Circuit

Fig. 19 shows a block diagram of the audio circuit.

The output from pin 21 (OUT R (-)) and pin 18 (OUT R (+)) of the IC701 D/A converter MN6471M passes through the buffer circuit of the discrete circuit configured of Q604, 608, and 612; and Q602, 606, and 610 respectively. It is then input to the differential input amplifier, which is symmetrical in the up and down directions, of the discrete circuit configured of the following stage, which includes Q310, 312, 302, 304, 306, 308, 310, 312, 314, and 316. The output undergoes differential synthesis in this circuit, and after synchronous-phase noise has been eliminated, the resulting signal is output to the low-pass filter of the discrete circuit configuration of the following stage as an audio signal with very little 2nd order high-frequency distortion. Q605 and 609 (left channel), and Q606 and 610 (right channel) are an inverted darlington buffer circuit, while Q601 (left channel) and Q602 (right channel) are a FET-controlled constant-current circuit. Q309 and 311 (left channel) and Q310 and 312 (right channel) are, similarly, a FET-controlled constant-current circuit. Fig. 20 shows the buffer circuit and the differential input amplifier (left channel only).

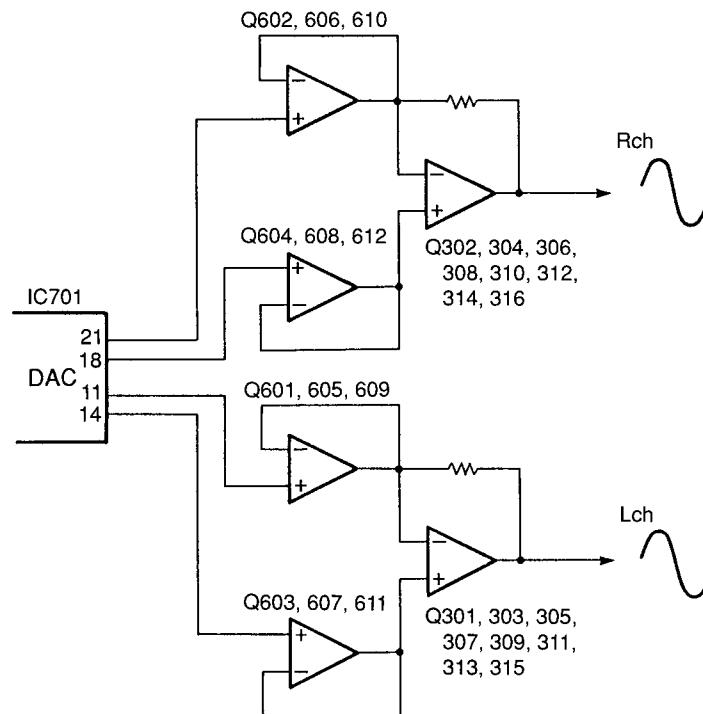


Fig. 19

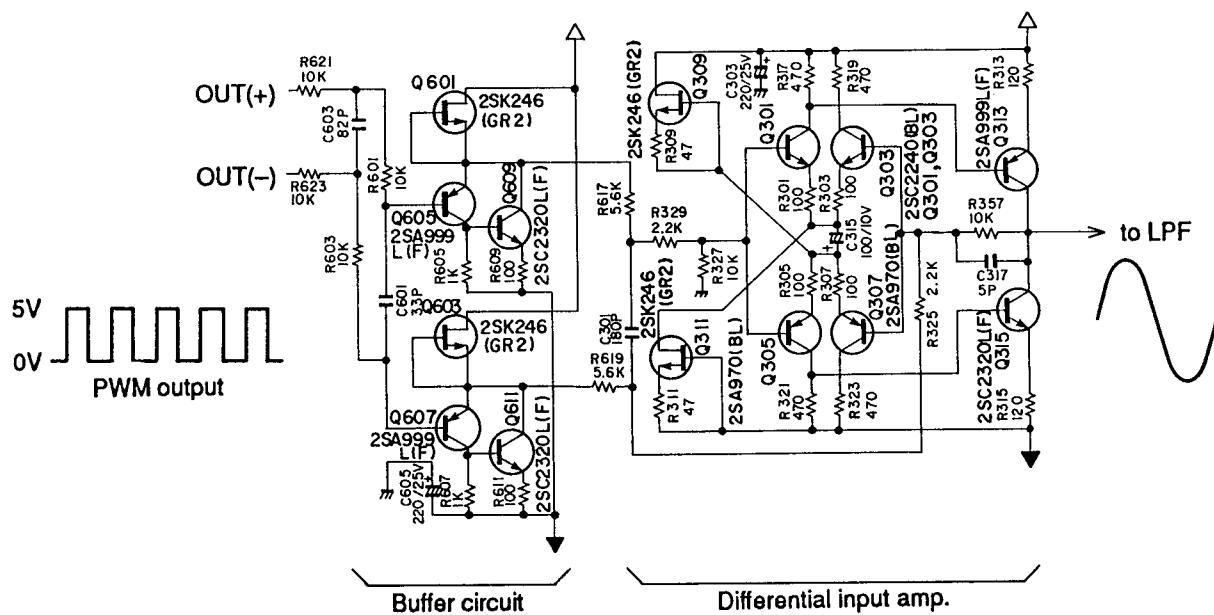


Fig. 20

9. Low-pass filter

Fig. 21 shows a final-stage circuit which includes a low-pass filter and other elements.

The emphasis data from the disc is output through the EMP terminal (pin 1) of IC201. When a disc to which emphasis is applied is played back, this terminal is set high and Q202 turns on. Following this, Q317 (left channel) and Q318 (right channel) are also turned on. Then connecting C305 (left channel) and C306 (right channel) to the ground provides the de-emphasis characteristic.

The portion of Fig. 21 enclosed by the broken line is a 3rd-order active LPF. This LPF causes noise in the high range to be cut. Q321 and Q323 (left channel) and Q322 and Q324 (right channel) are buffer circuits of inverted darlington configuration. Q319 and Q320 are FET controlled constant current circuits.

Q325, Q326, Q51 and Q56 are power muting circuits.

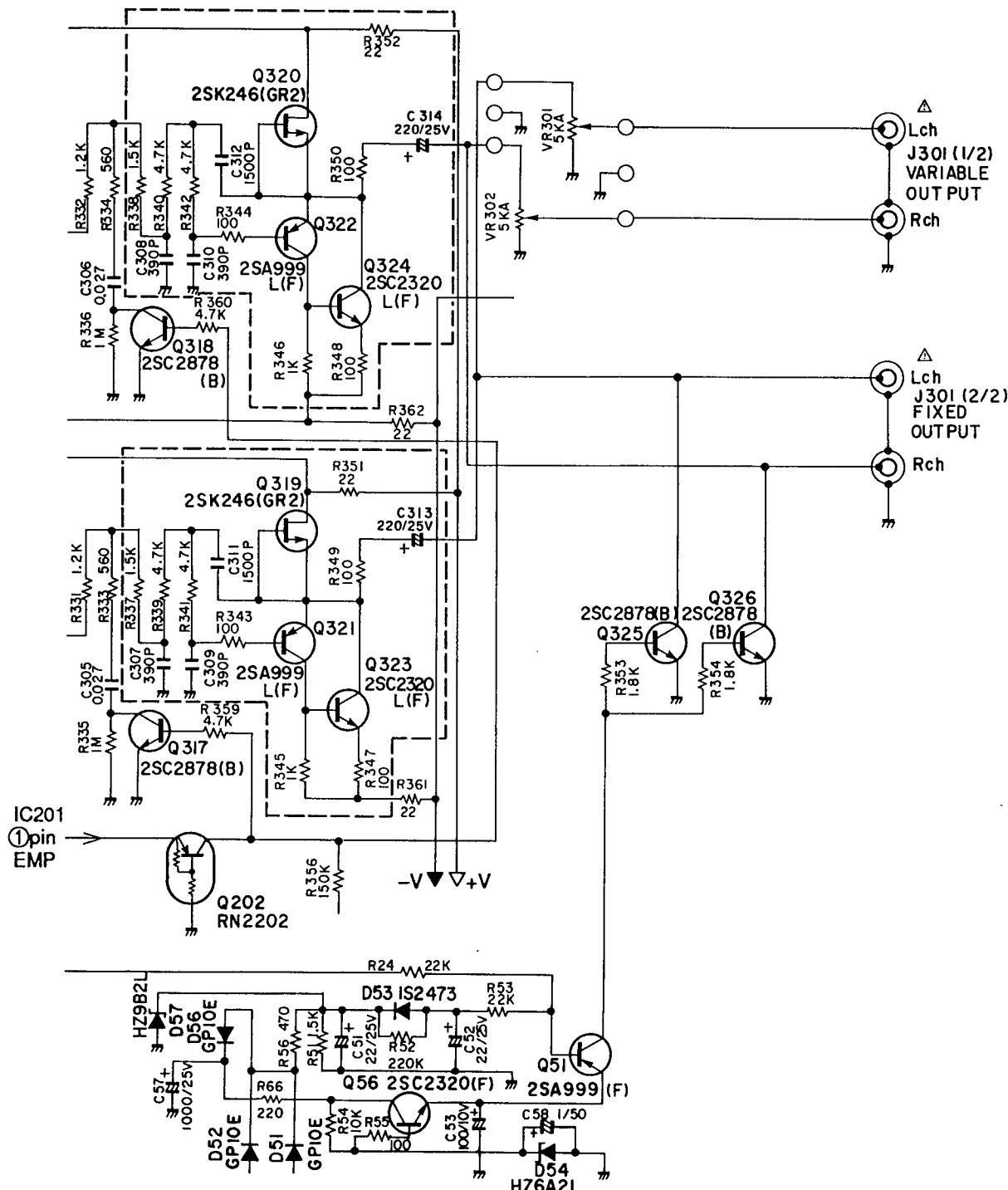
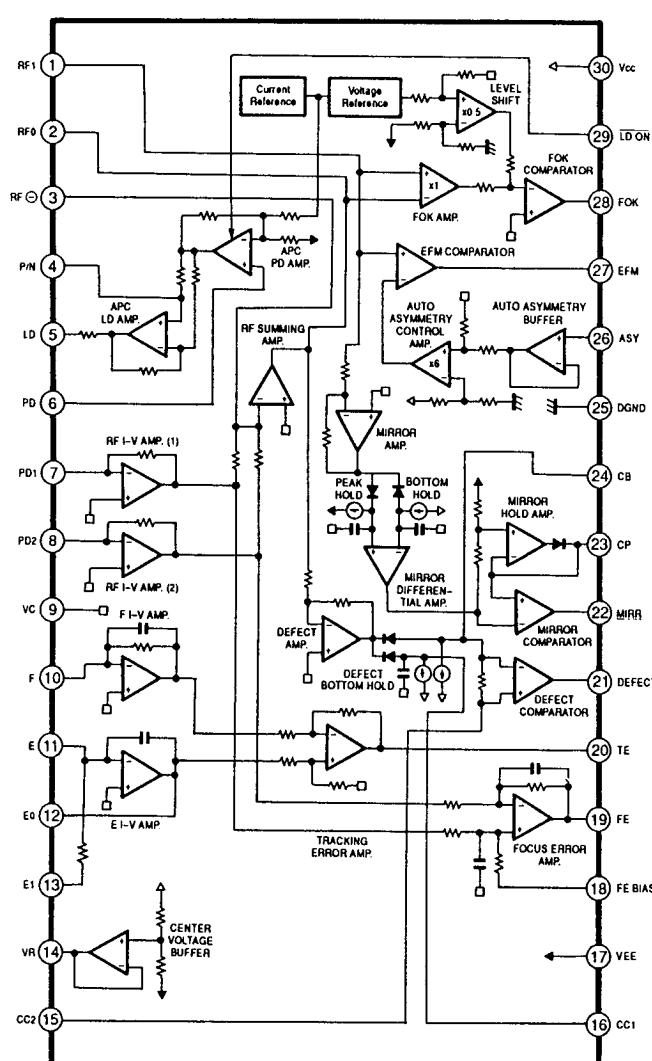


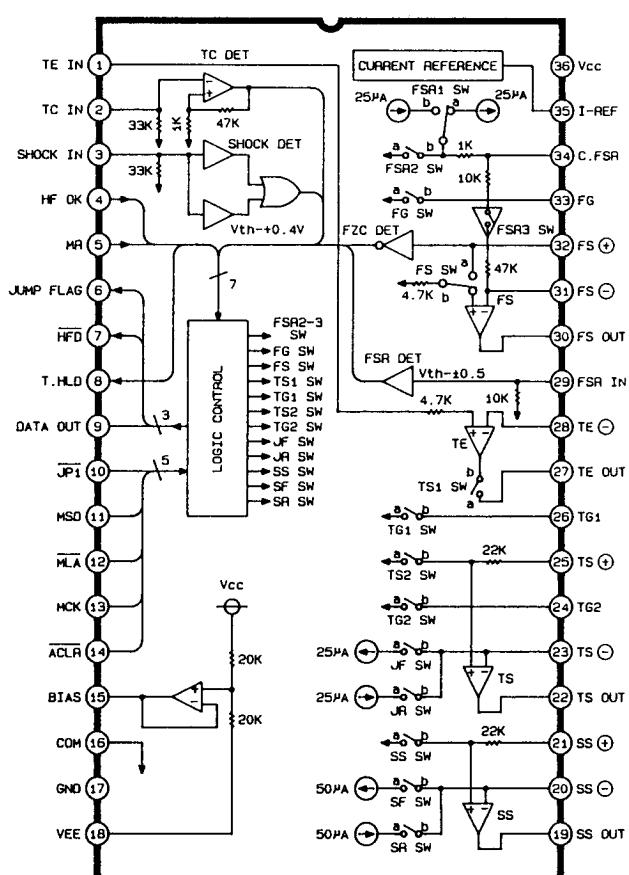
Fig. 21

IC BLOCK DIAGRAM

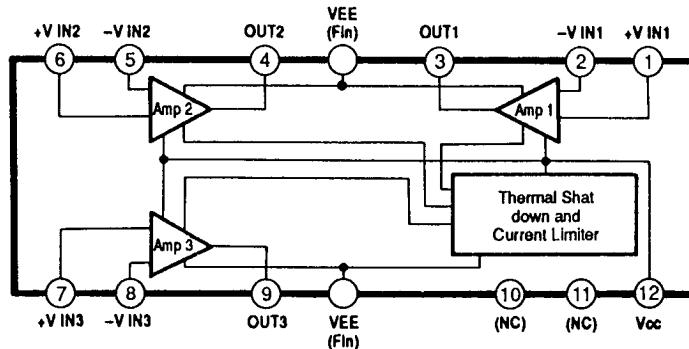
IC101 : CXA1081S
RF Amp.



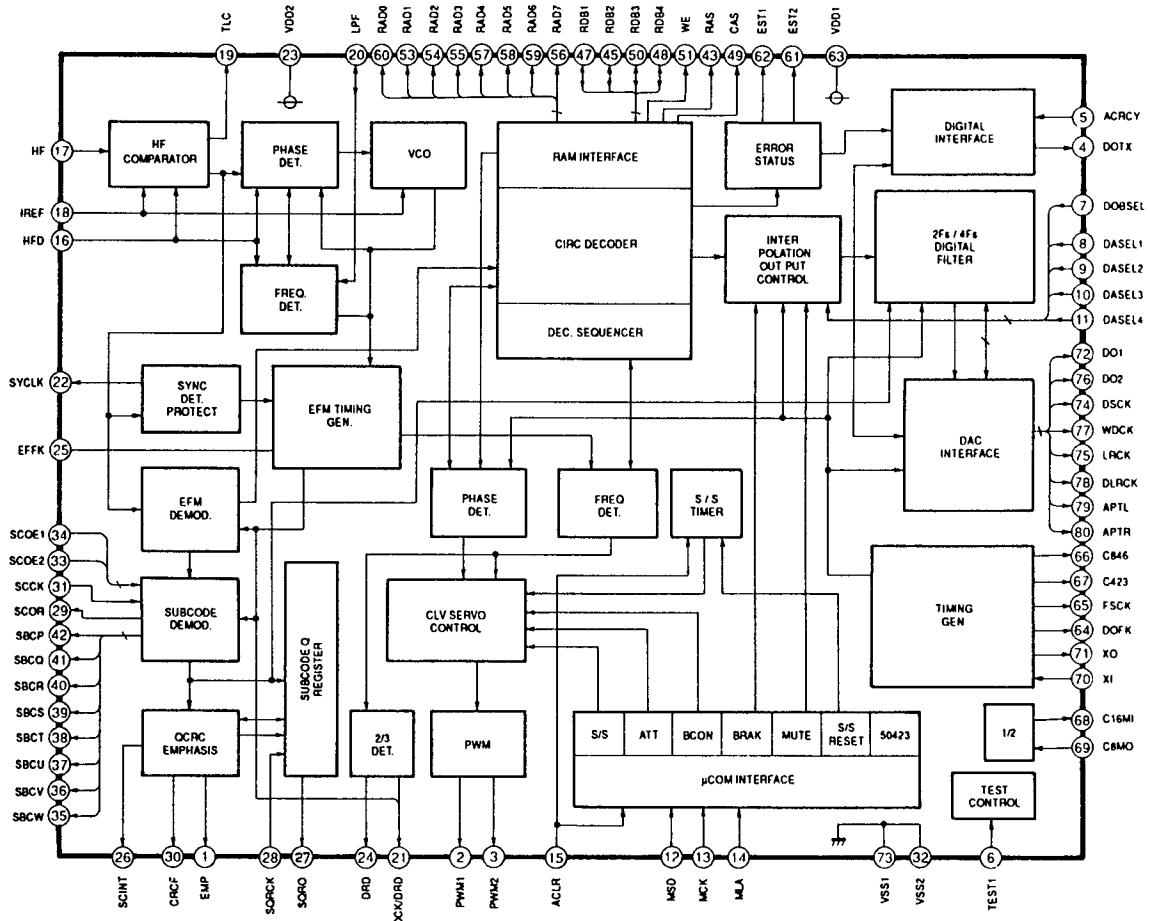
IC102
M51564P
Optical Pick-up Servo Control



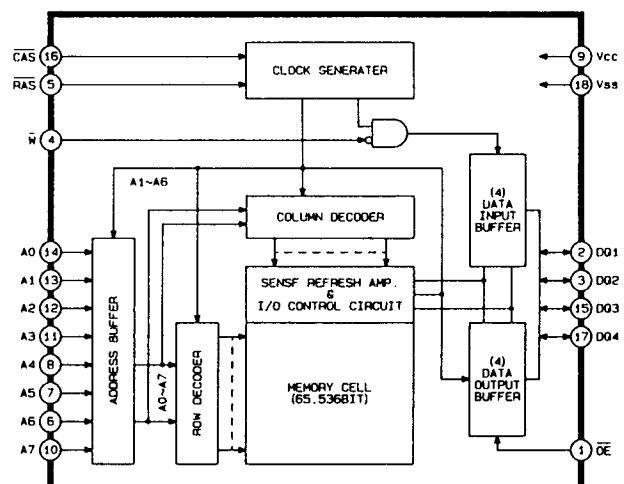
IC103 : LA6520
3 channel Power OP-Amp.



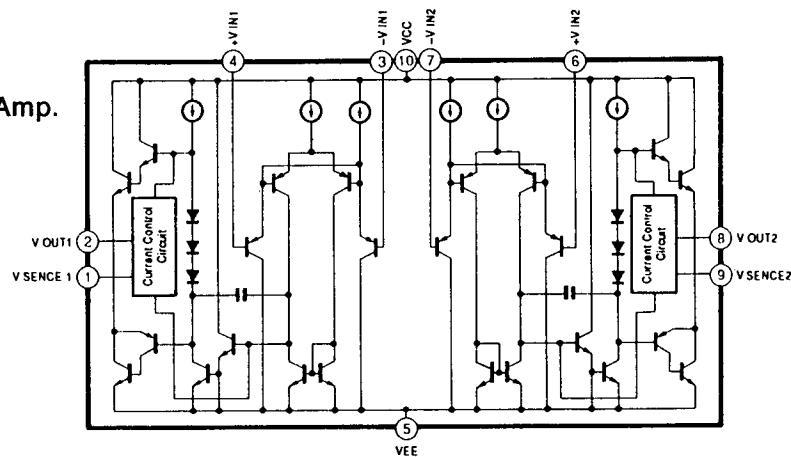
IC201 : M50423FP
Digital Signal Processor



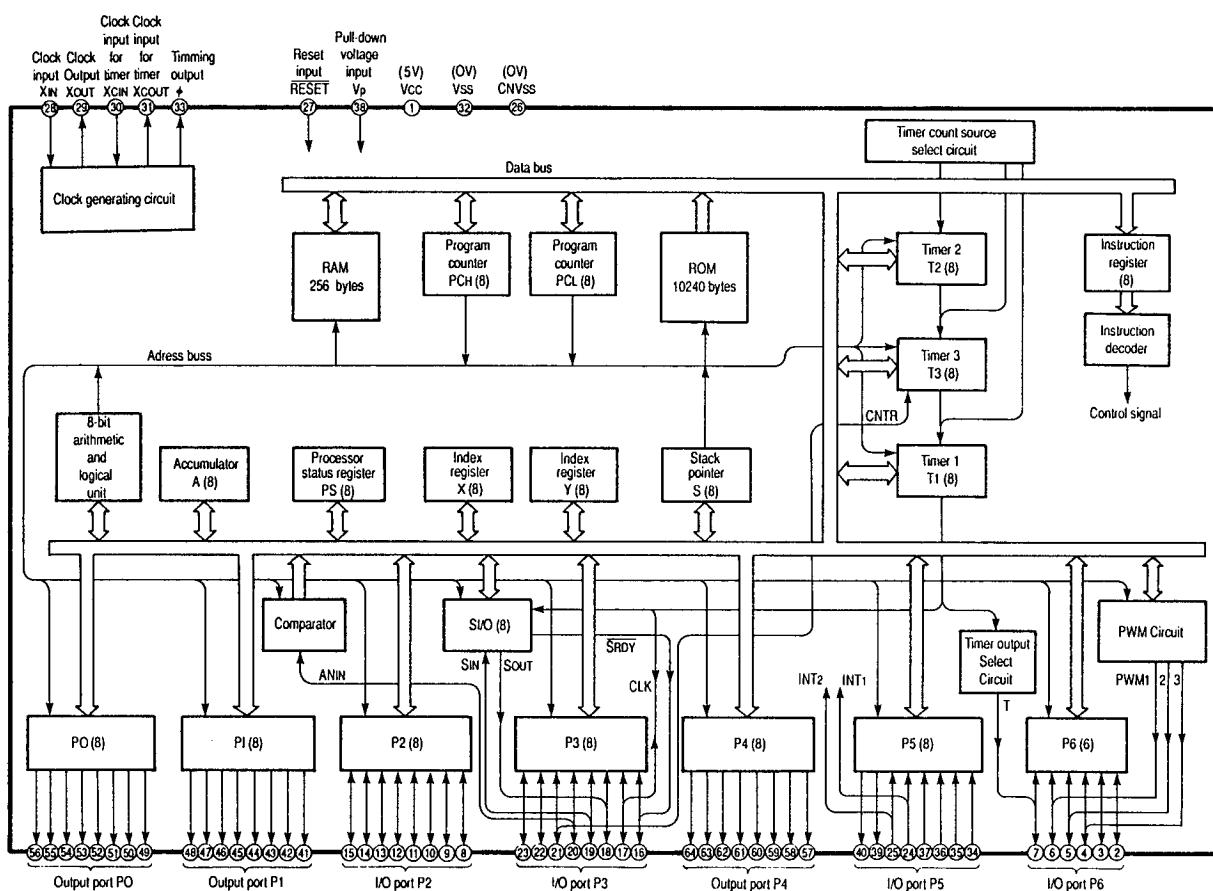
IC202
M5M4416P
65536-Bit (16384-Word by 4-Bit)
Dynamic RAM



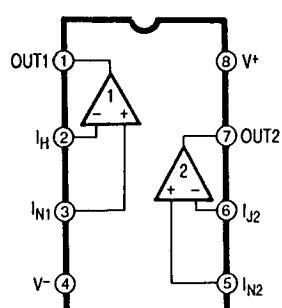
IC203 : LA6515
2 channel Power OP-Amp.



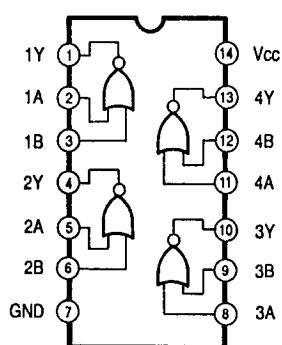
IC401 : M50957 Microcomputer



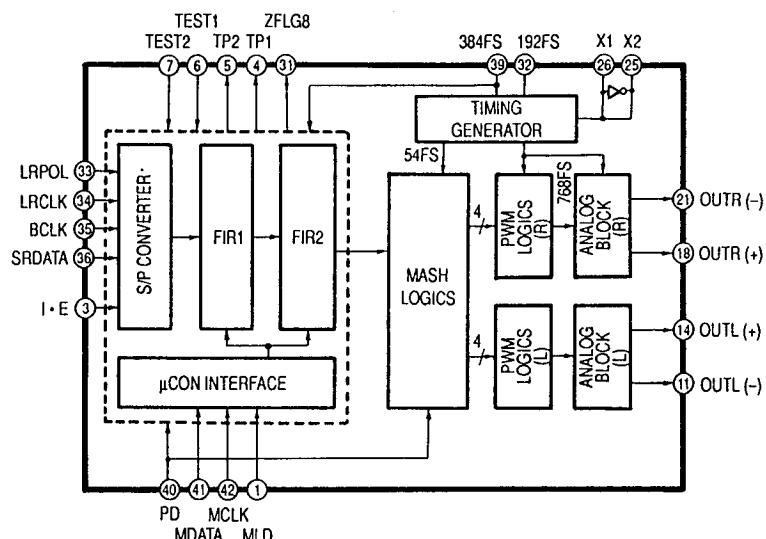
IC501 : NJM4556D Headphone Amp.



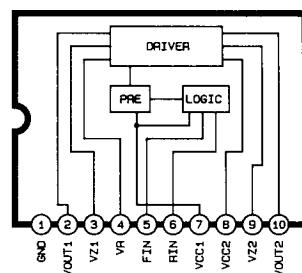
IC801 : T74HC02P Buffer Amp.



IC701 : MN6471M Digital Filter & D/A Converter



IC901 : BA6209N Motor Driver



A

B

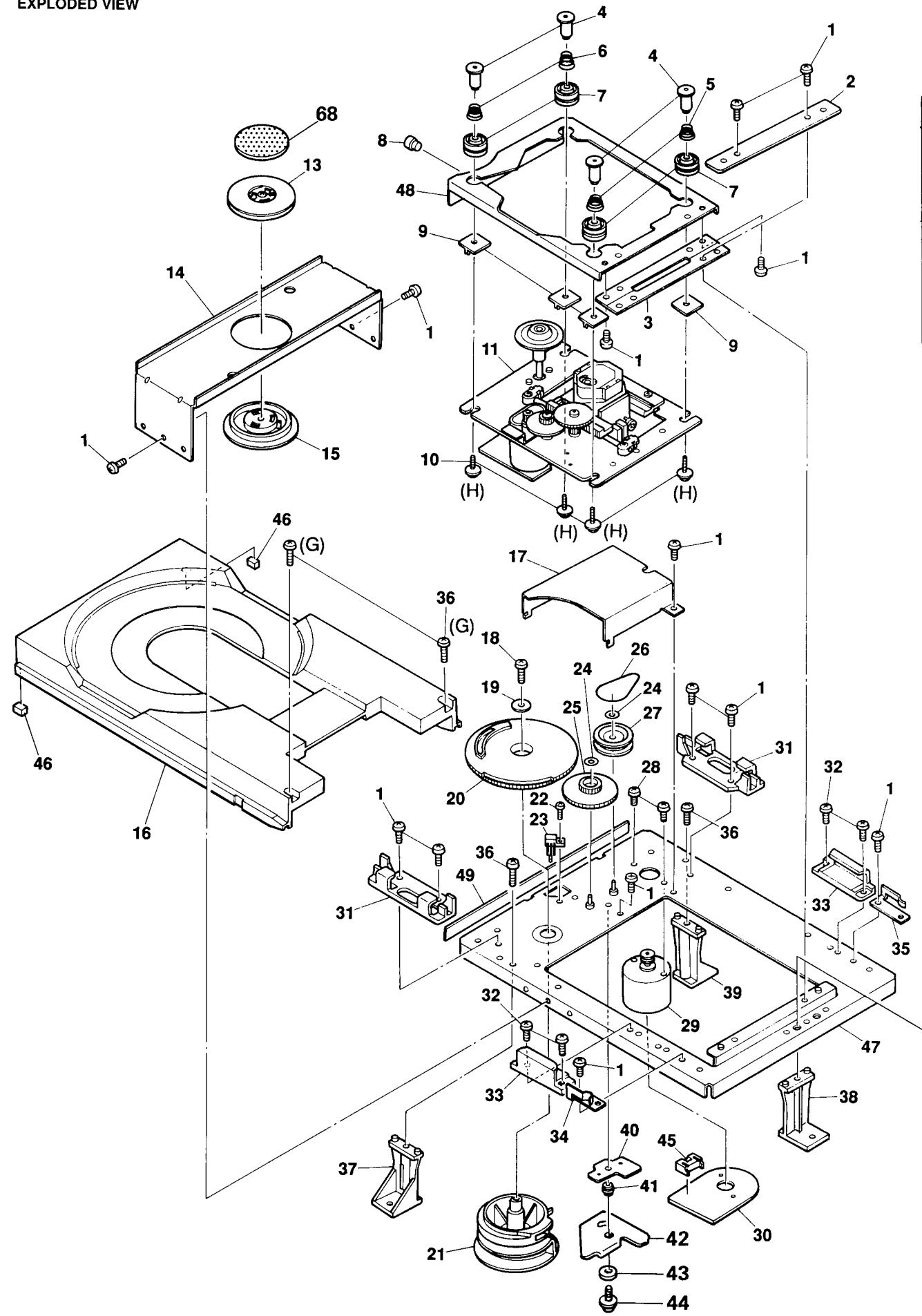
C

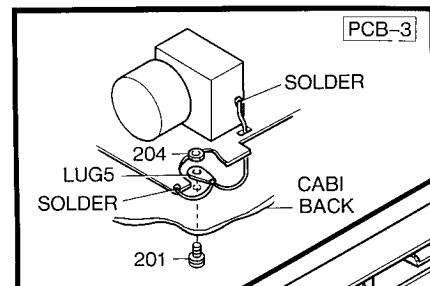
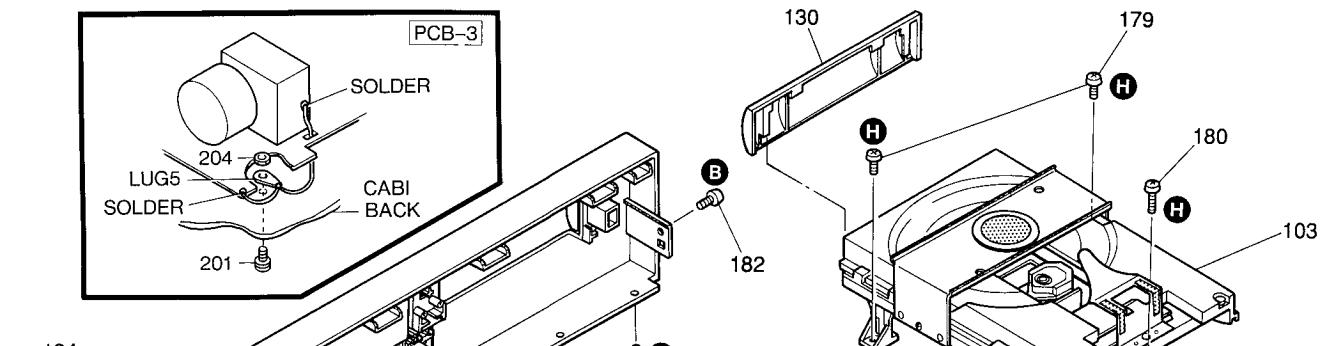
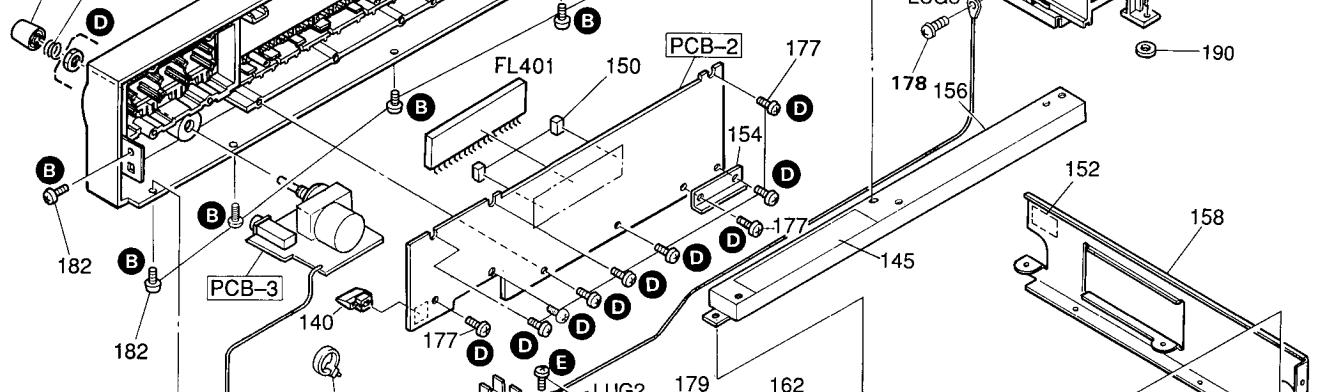
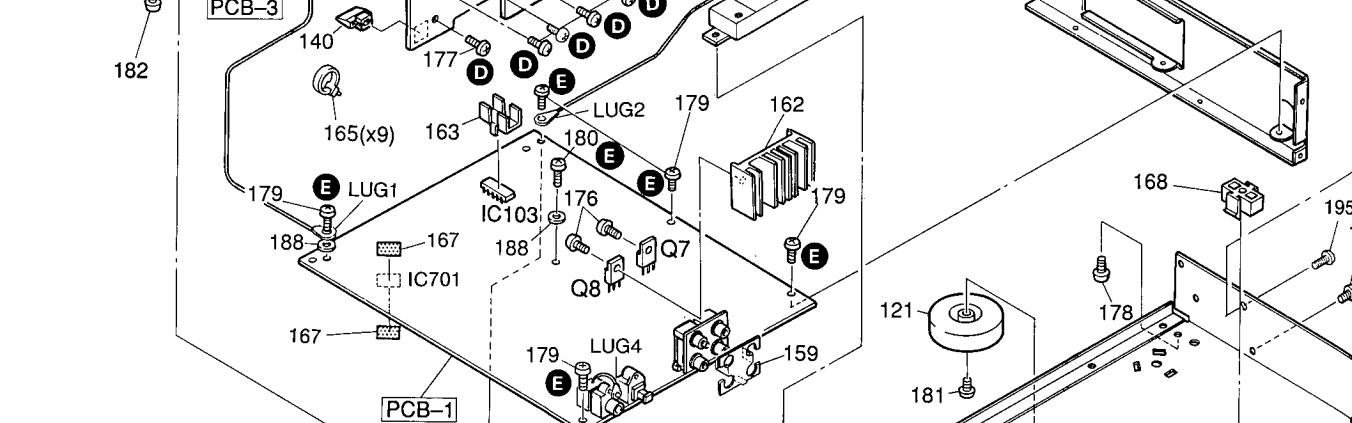
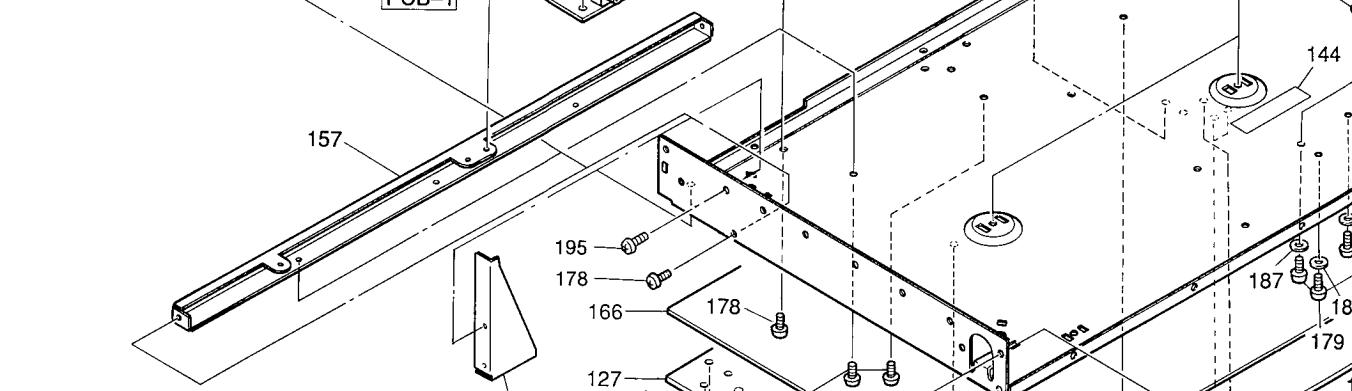
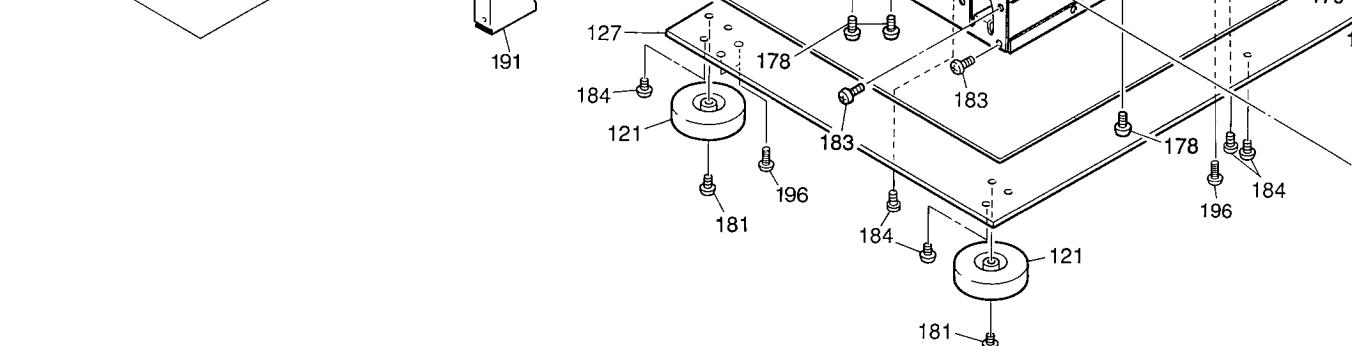
D

E

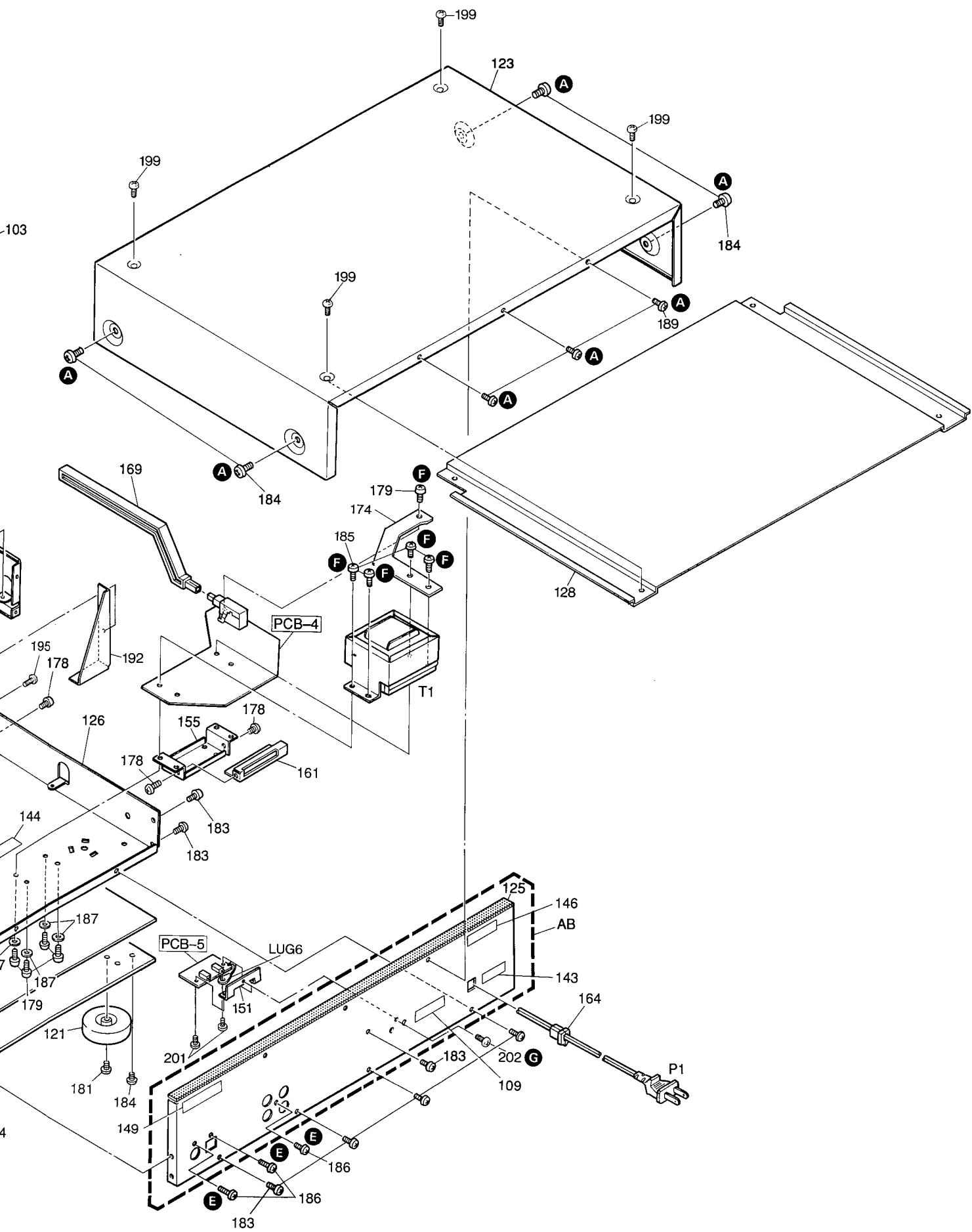
CD MECHANISM

EXPLODED VIEW



A**B****C****D****E**
GENERAL UNIT
EXPLODED VIEW
1**2****3****4****5****6****7**

F G H I J



F

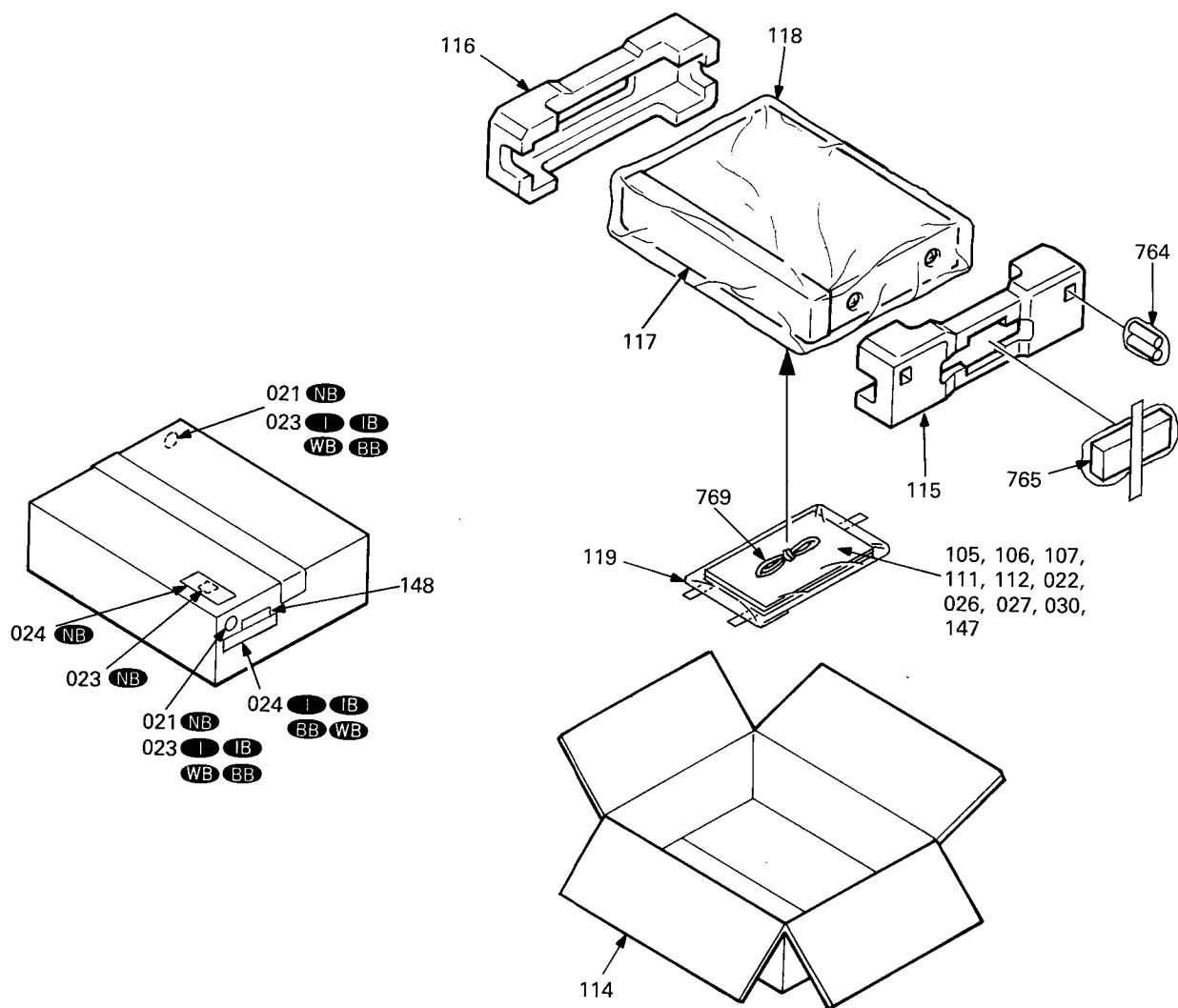
G

H

I

J

PACKAGE



PARTS LIST PACKAGE

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
021	1756-03113	LABEL NB	111	1119-03102	ATTACH SHEET UA BK
022	1113-OC	OWNER CARD NB	112	1119-03402	ATTACH SHEET UA BK
023	1756-03108	LABEL I IB WB	114	1221-28114	CARTON BOX UA I
023	1756-03111	LABEL BB	114	1221-28115	CARTON BOX BK IB BB
023	1116-GC	GUARANT CARD NB	114	1221-28123	CARTON BOX WB NB
024	1756-06303	LABEL I IB BB WB	115	1222-7366	CUSHION
024	1241-PB	POLYETHY BAG NB	116	1223-7367	CUSHION
026	1111-M30253	OWNER GUIDE I IB	117	1223-11729	SOFT SHEET
026	1111-J30253	OWNER GUIDE WB	118	1241-C1491	POLYETHY BAG
027	1111-M30319	OWNER GUIDE BB	119	1241-R0123350	POLYETHY BAG
030	1111-J30320	OWNER GUIDE WB	147	1756-11604	LABEL UA BK
105	1111-M30333	OWNER GUIDE UA BK BB WB	148	1756-11701	LABEL UA BK
105	1111-M30334	OWNER GUIDE I IB	764	4191-0355	BATTERY, DRY
105	1111-J2054	OWNER GUIDE NB	765	6142-05506	REMOTE CONTROL ASSY UA BK I
106	1119-01203	ATTACH SHEET UA BK	765	4142-05504	REMOTE CONTROL ASS'Y WB NB
107	1113-717004	OWNER CARD UA BK	769	4161-04601102	CORD W/PLUG, RCA TYPE

A

B

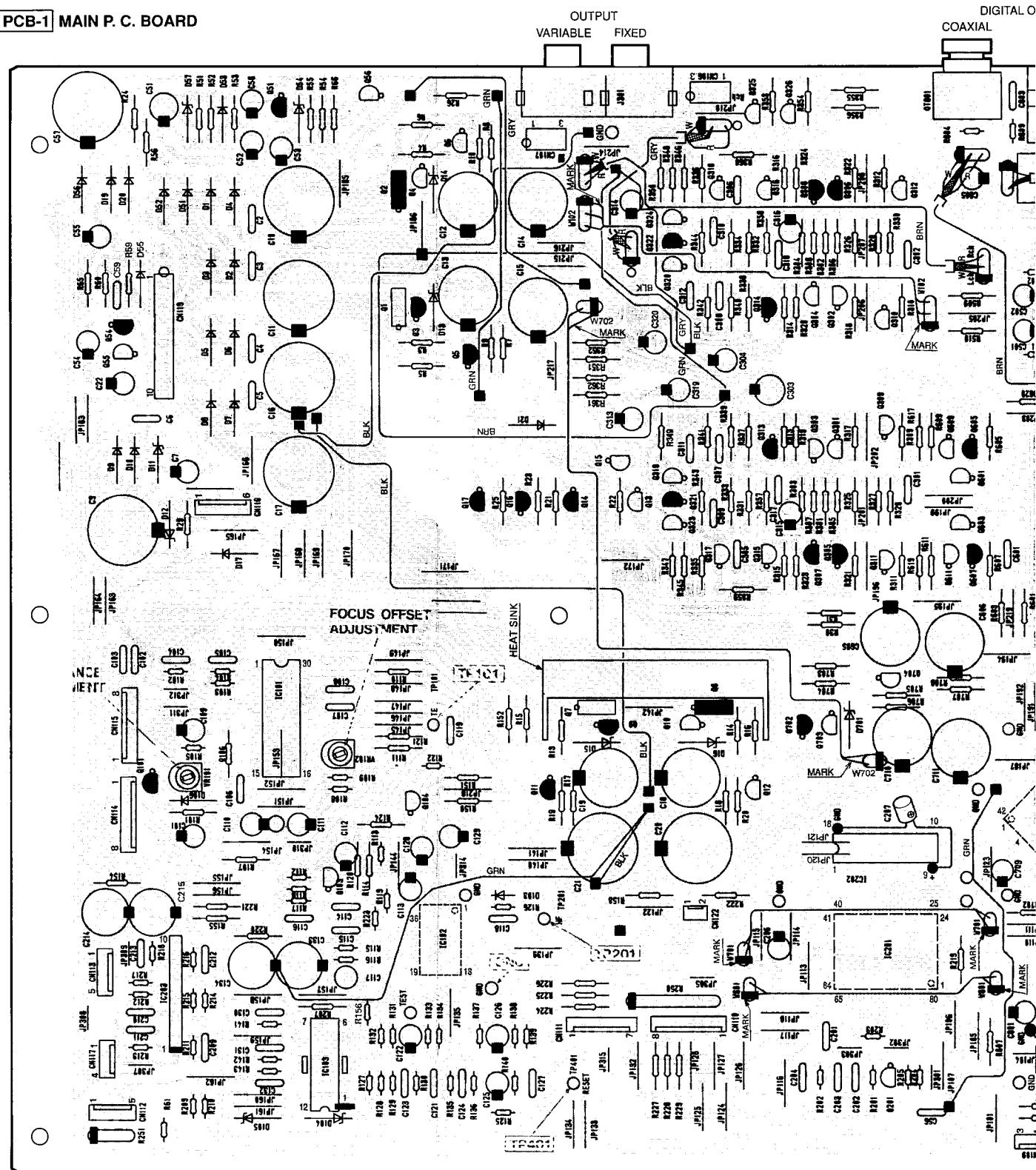
C

D

E

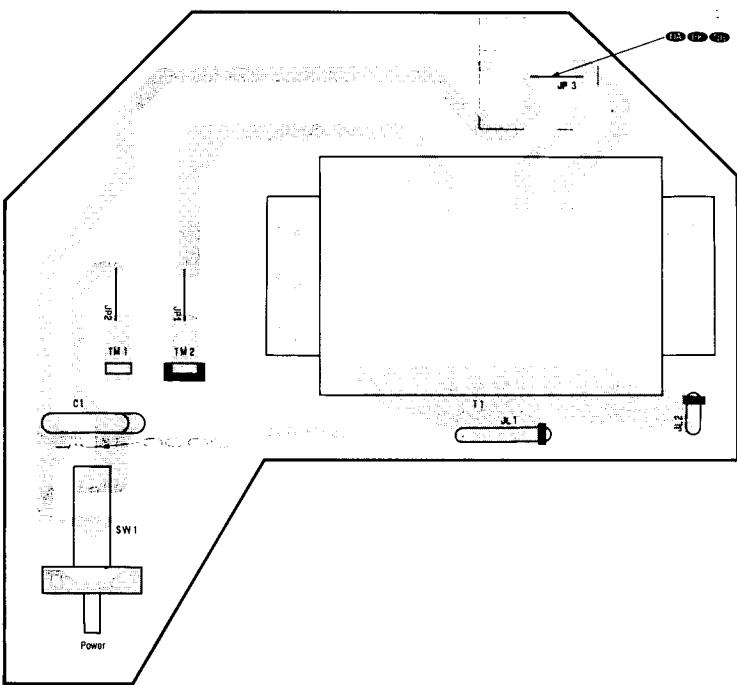
P. C. BOARDS

PCB-1 MAIN P. C. BOARD

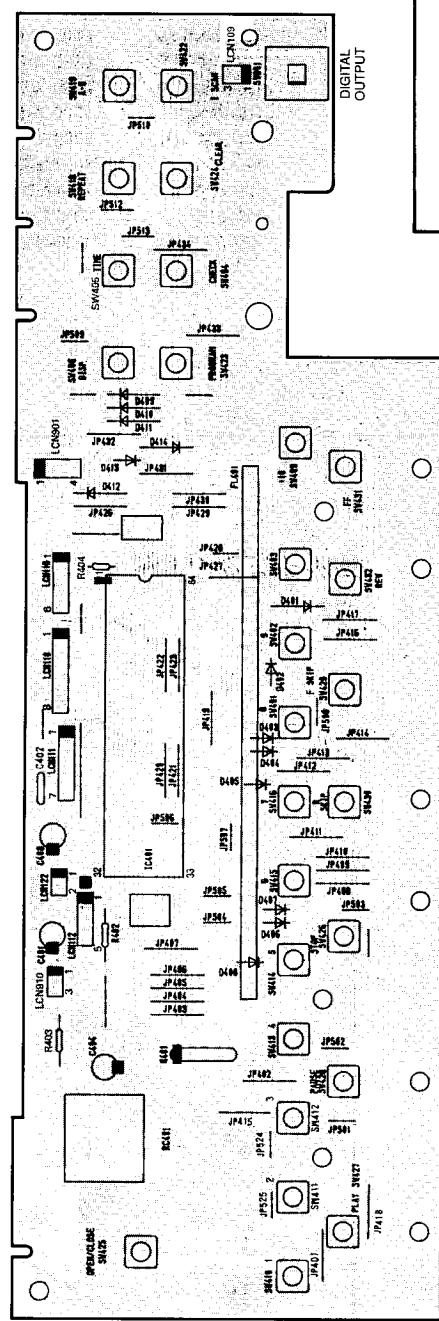


E F G H I J

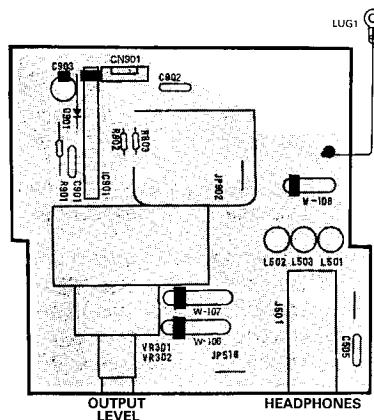
PCB-4 POWER SWITCH P. C. BOARD



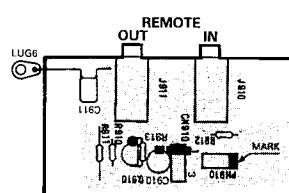
PCB-2 FRONT P. C. BOARD



PCB-3 OUTPUT LEVEL & JACK P. C. BOARD



PCB-5 REMOTE JACK P. C. BOARD



A B C D E

BLOCK DIAGRAM

1

2

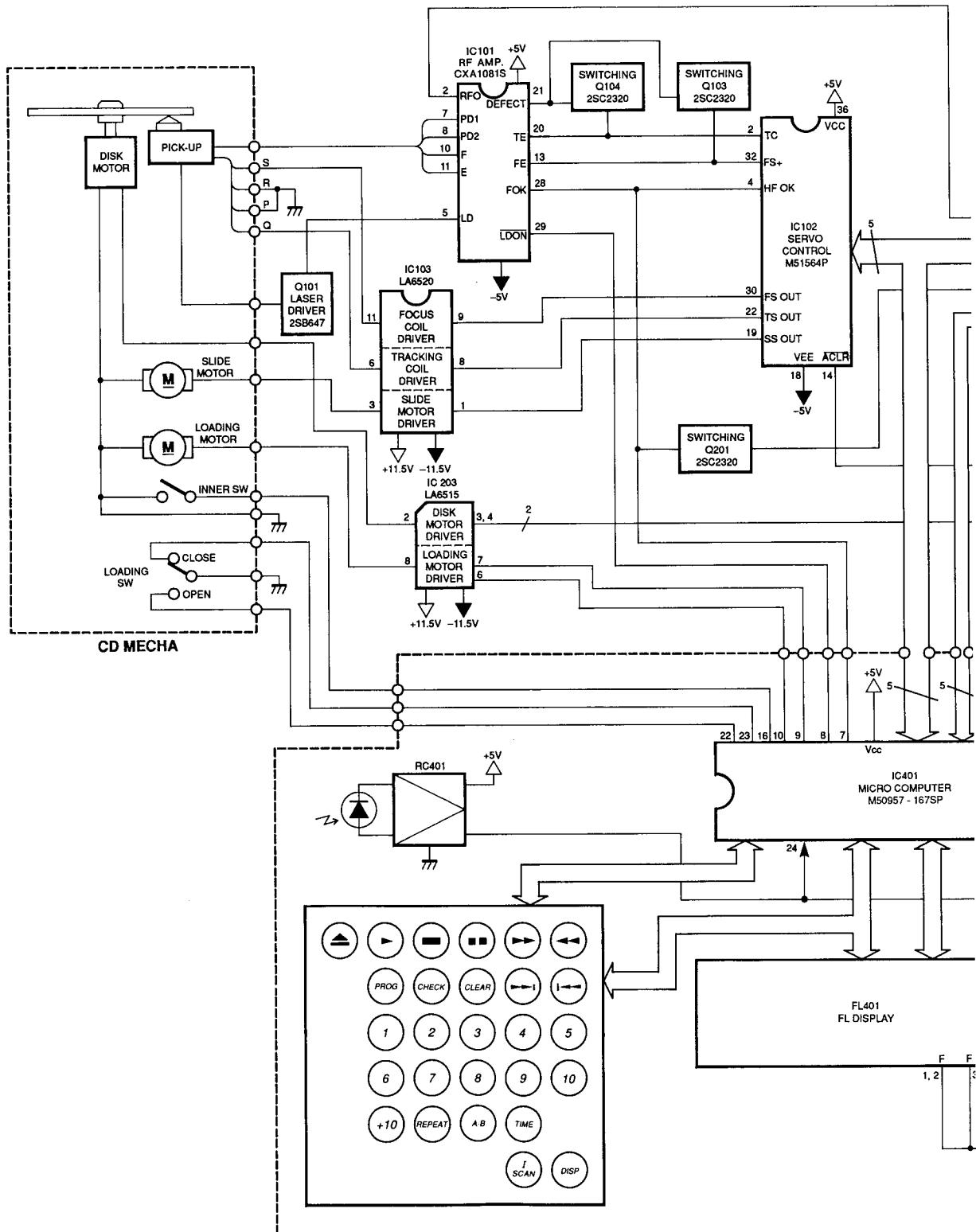
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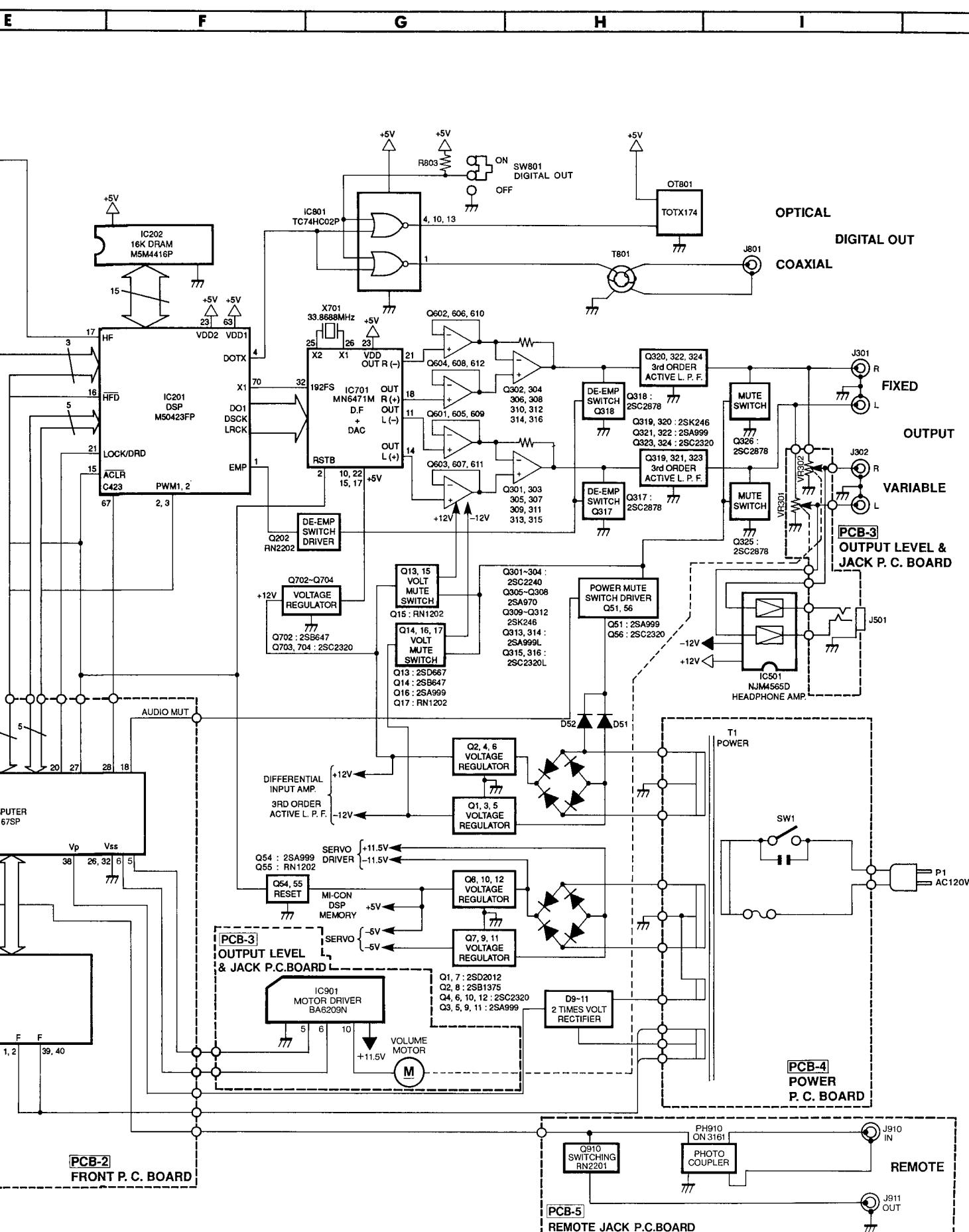
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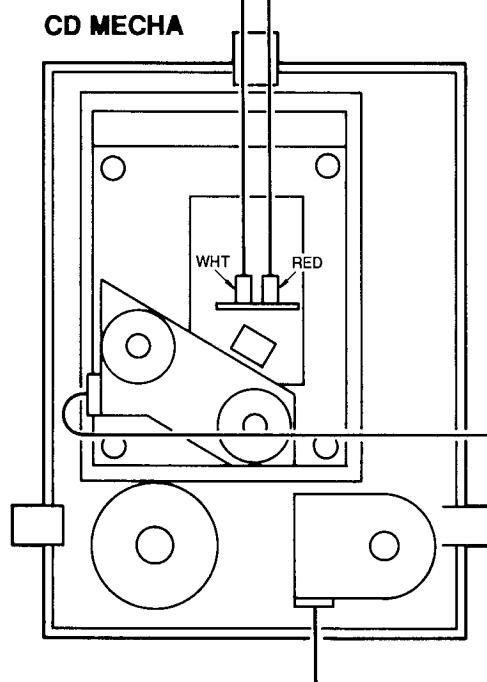
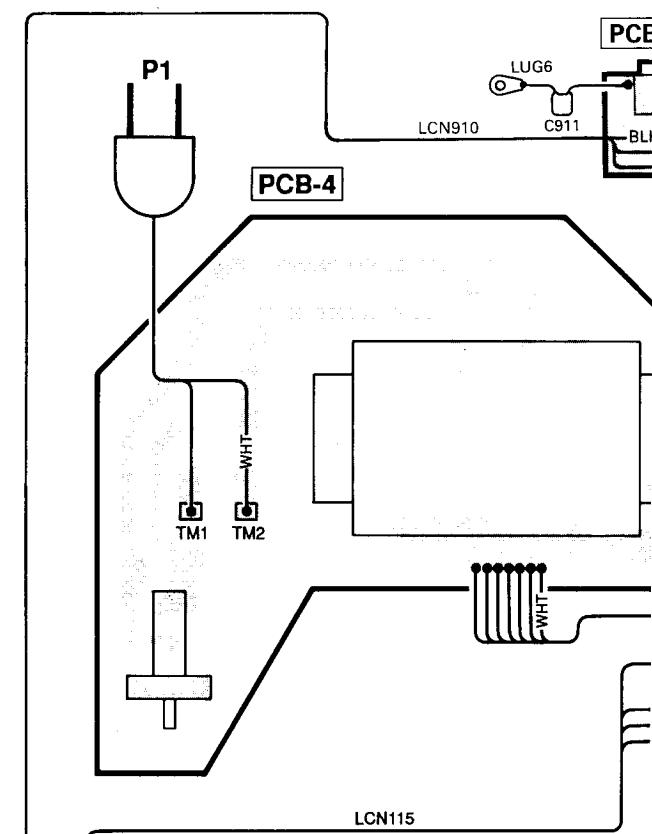
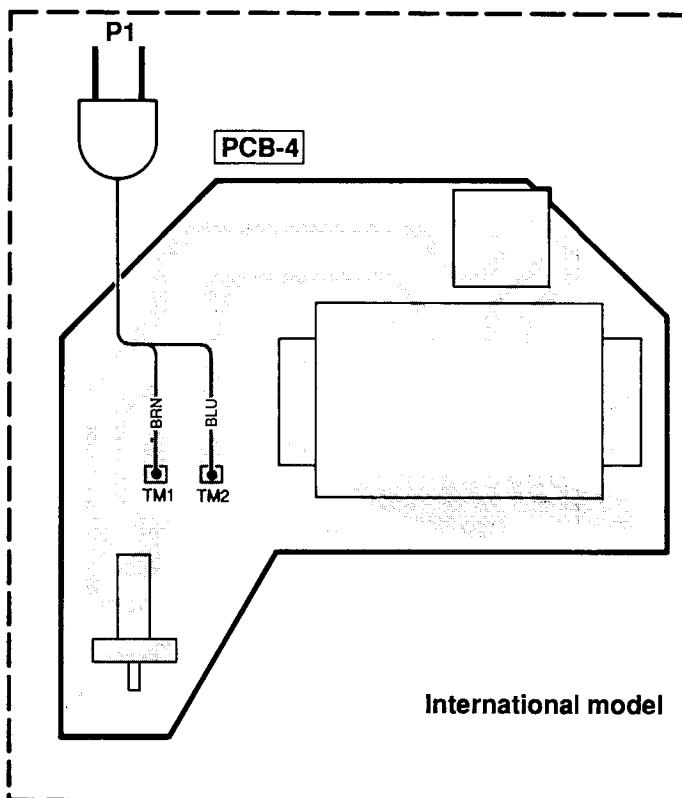
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7



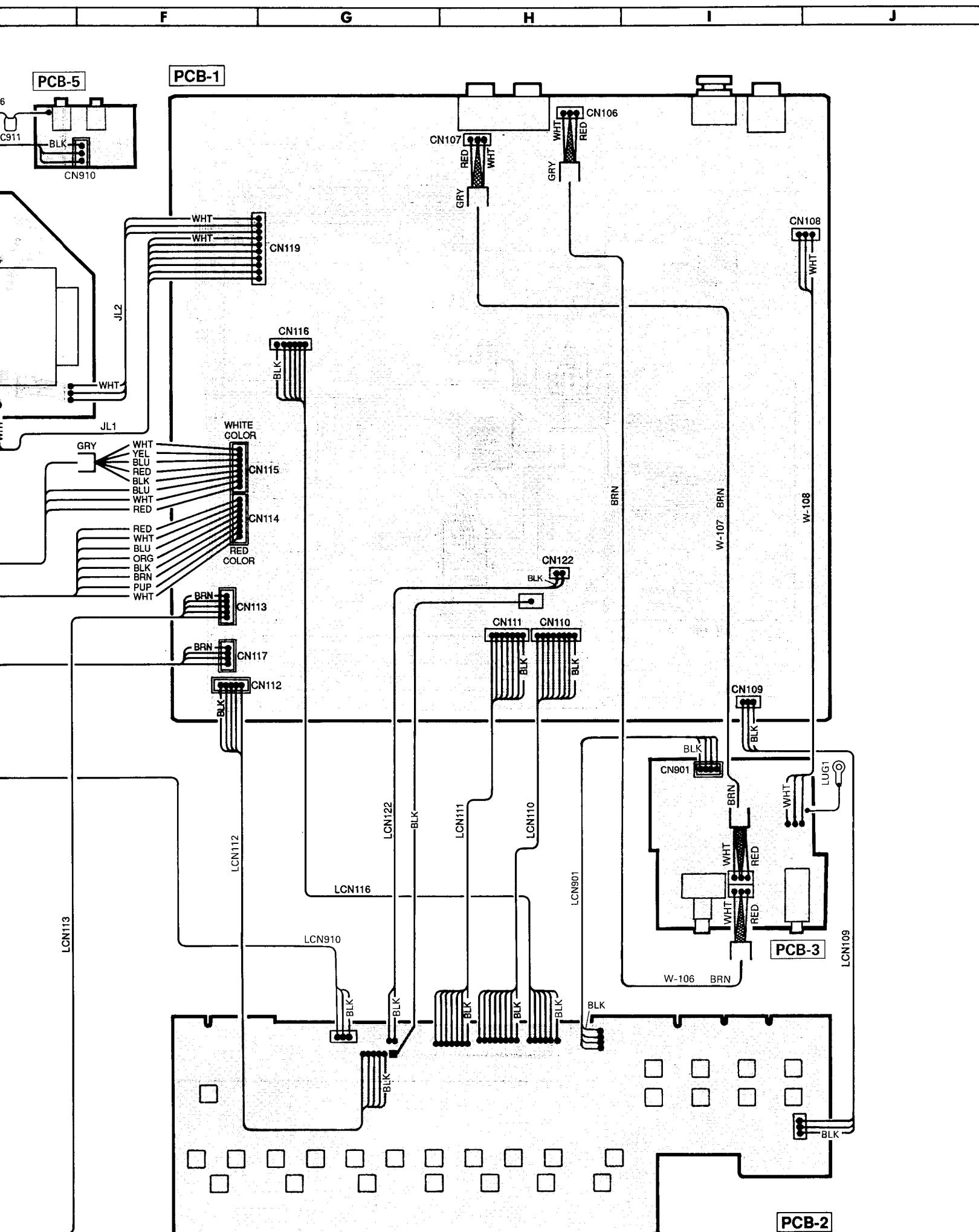


WIRING DIAGRAM



WIRE COLOR ABBREVIATIONS

RED	: Red	YEL	: Yellow
ORG	: Orange	PUP	: Purple
BLU	: Blue	PIK	: Pink
WHT	: White	GRY	: Gray
GRN	: Green	BRN	: Brown
BLK	: Black		



Ser. No.	Ref. No.	Part No.	Description
PCB-4 POWER SWITCH P.C. BOARD			
CAPACITORS			
749	△C1	5352-S010M103	CAP, MTL .01 μ UA BK NB
749B	△C1	5352-1030961	CAP, MTL .01 μ I IB BB WB
RESISTOR			
022B	△R1	5135-335J50P	RES, CBN 1/2P 3.3M I IB BB WB
MISCELLANEOUS			
751	JL1	4242-R0407201	JUMPER LEAD
750	JL2	4242-R0403201	JUMPER LEAD
748	△SW1	4433-01301	SWITCH, PU-PW
021B	△SW2	4411-1047111	SWITCH, ROTARY I IB BB WB
741	△T1	5584-S7001	XFORMER, POWER UA BK
741B	△T1	5584-S7002	XFORMER, POWER I IB BB
741B	△T1	5584-S6302	XFORMER, POWER WB
741C	△T1	5584-S6303	XFORMER, POWER NB
752	TM1	4214-122	TERMINAL
752	TM2	4214-122	TERMINAL

PCB-5 REMOTE JACK P.C. BOARD			
		CAPACITORS	
529	C910	5345-226C0962	CAP, MINI ELE 22 μ /16V
638	C911	5354-104593	CAP, MYL .1 μ
		RESISTORS	
677	R910	5232-473J16P	RES, CBN 1/6P 47K
624	R911	5134-470J25P	RES, CBN 1/4P 47
678	R912	5232-271J16P	RES, CBN 1/6P 270
679	R913	5232-392J16P	RES, CBN 1/6P 3.9K
		TRANSISTOR	
523	Q910	5611-RN2201	XISTOR, PNP R
		MISCELLANEOUS	
698	CN910	4443-0301140	CONNECTOR
747	J910	4451-00184	JACK, 1P
747	J911	4451-00184	JACK, 1P
639	LUG6	4211-4	LUG
524	PH910	5624-ON3161	PHOTO COUPLR

Ser. No.	Ref. No.	Part No.	Description
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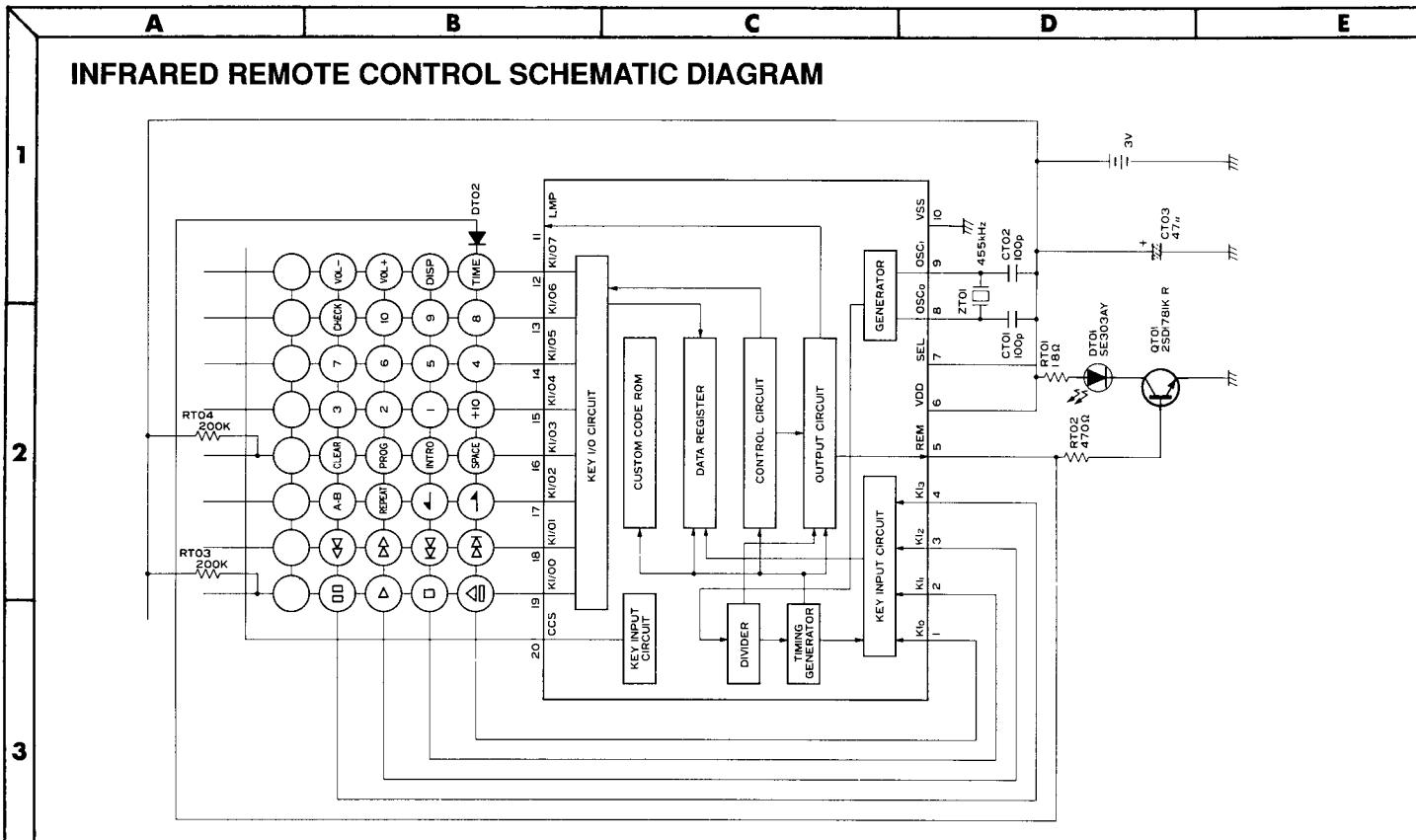
CHASSIS MISCELLANEOUS PARTS LIST			
701	LCN113	4163-03212005	CONNECTOR W/W
700	LCN114	4163-03210008	CONNECTOR W/W
699	LCN115	4163-03211008	CONNECTOR W/W
702	LCN117	4163-03207004	CONNECTOR W/W
740	LUG2/3	4162-00201800	LUG W/WIRE
768	△P1	4161-03601202	CORD W/PLUG UA BK
768B	△P1	4161-03701220	CORD W/PLUG I IB WB
768D	△P1	4161-04100	CORD W/PLUG BB
768C	△P1	4161-00401192	CORD W/PLUG NB

ABBREVIATIONS IN PARTS LIST

CAPACITORS	RESISTORS
CAP, MINI ELE	RES, CBN 1/6P : Carbon 1/6W
CAP, CER	RES, FUSE : Fuse
CAP, PPP	RES, CEM 5P : Cement 5W
CAP, MYL	RES, MTL 1P : Metal 1W
CAP, MCA	2.2K : 2.2K Ω
CAP, MINI BP	220 : 220 Ω
CAP, ELE BP	TRANSISTORS
470 μ	XISTOR : Transistor
6800p	FET : Field Effect Transistor
.047 μ	CONTROLS
470 μ F	RES, SEMI FIX : Semi-fixed Resistor
6800pF	
.047 μ F	

NOTE

 SAFETY RELATED COMPONENT. USE ONLY EXACT REPLACEMENT PART AS SPECIFIED.



A

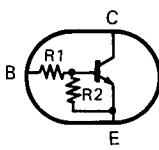
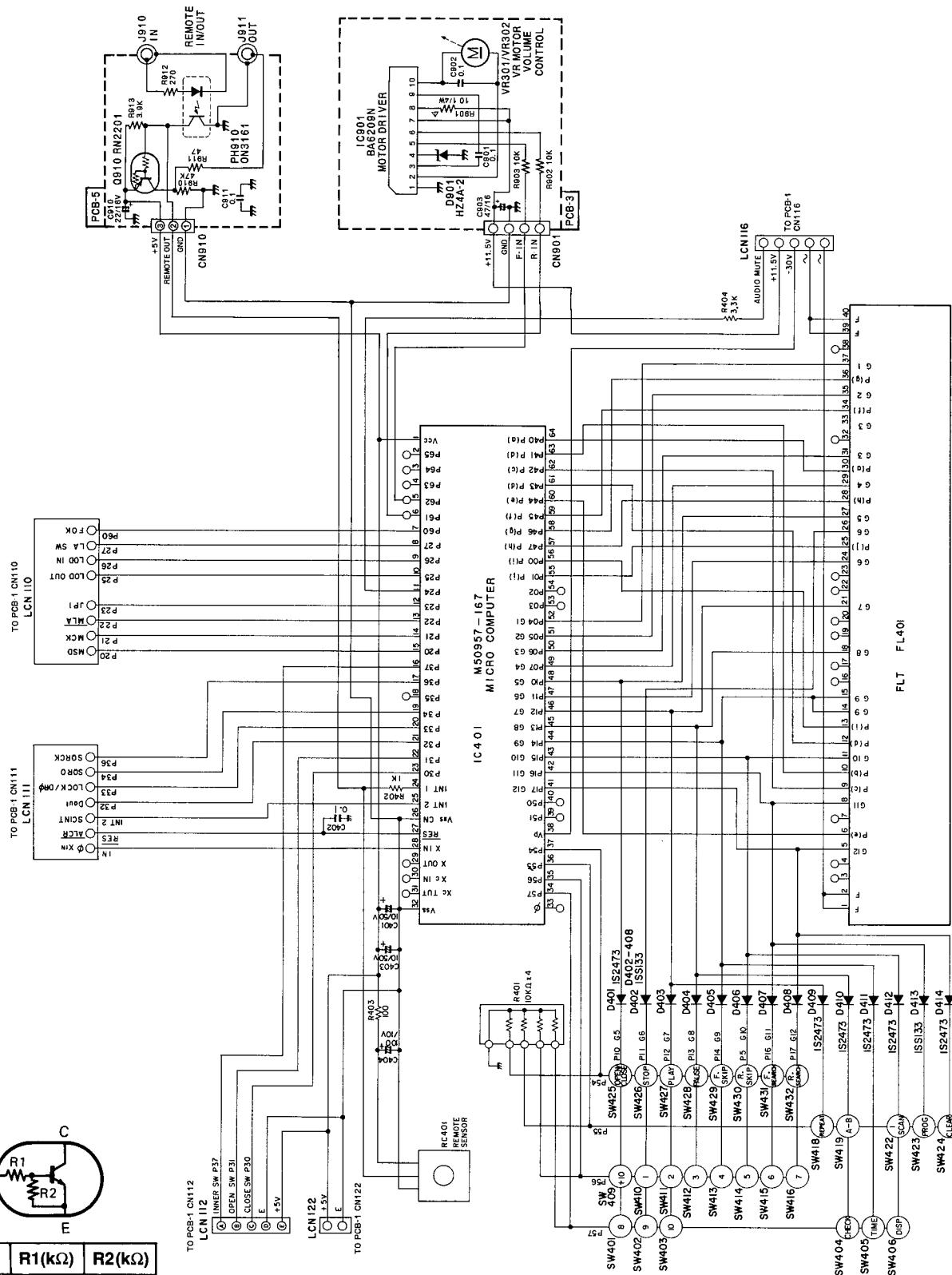
B

C

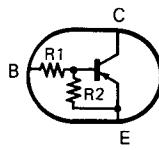
D

E

SCHEMATIC DIAGRAM



Type	R1(kΩ)	R2(kΩ)
RN1202	10	10



Type	R1(kΩ)	R2(kΩ)
RN2202	10	10
RN2203	22	22

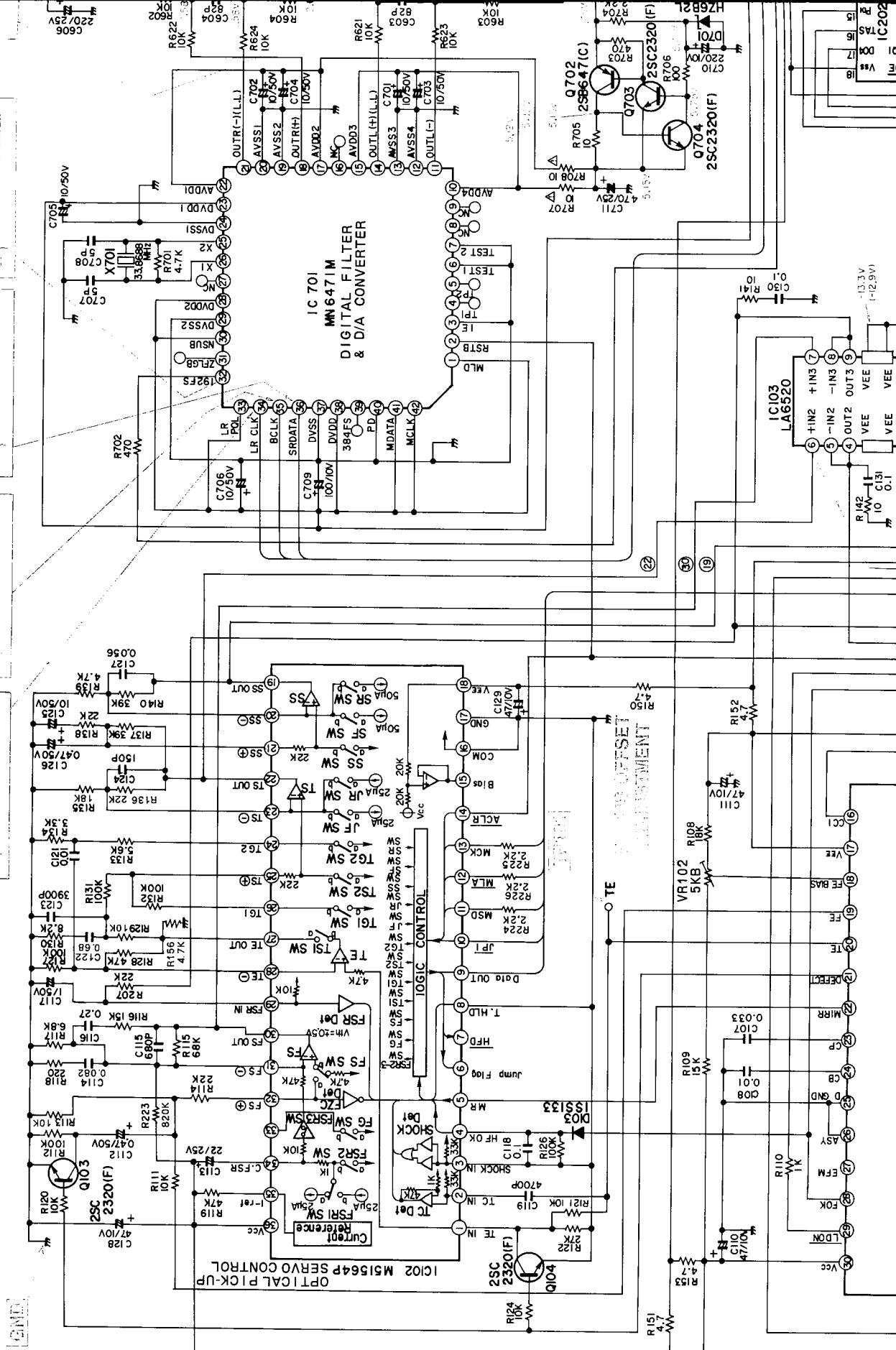
NOTE:

- ALL RESISTANCES VALUES ARE IN Ω .
 $K\Omega=1000\Omega$, $M\Omega=1000K\Omega$.
- ALL CAPACITANCES VALUE ARE IN μF UNLESS OTHERWISE NOTED. $P=\mu\mu F$.
- ... V : DC VOLTAGE AT PLAY MODE.
(. . . V) : DC VOLTAGE AT STOP MODE.
- SAFETY REQUIREMENTS COMPONENTS IN ACCORDANCE WITH PRESENT SAFETY REGULATIONS, THESE COMPONENTS MUST ONLY BE REPLACED BY ORIGINAL PARTS.



SCHEMATIC DIAGRAM

A B C D E F G



N
M
L
K
J
I
H