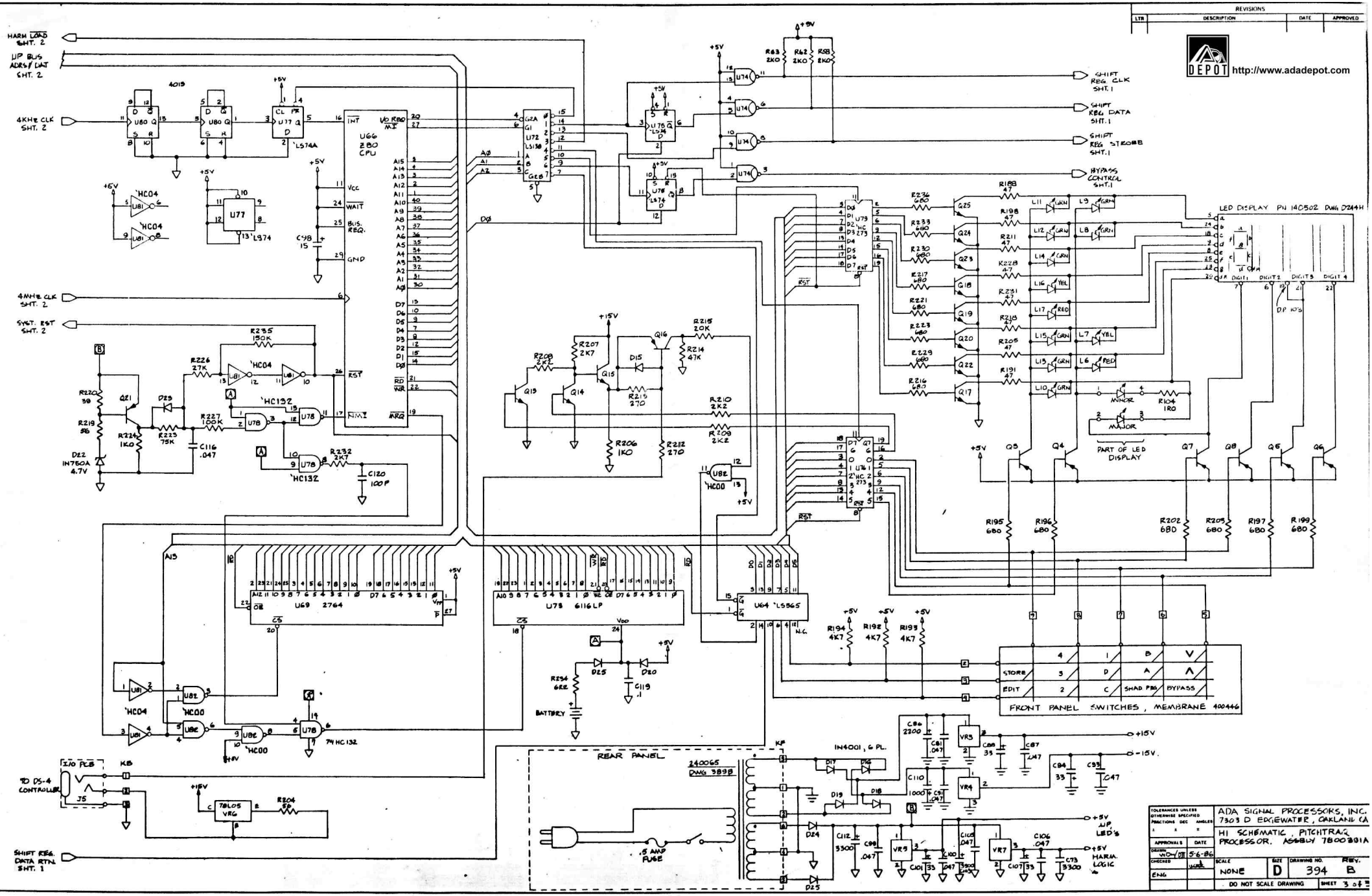
REVISIONS
LTR
DESCRIPTION
DATE
APPROVED
DEPOT
http://www.adadepot.com
HARM LOAD SHT. 2
UP BUS ADRS/DAT SHT. 2
4KHz CLK SHT. 2
4MHz CLK SHT. 2
SYST. RST SHT. 2
SHIFT REG. DATA RTN SHT. 1
SHIFT REG CLK SHT. 1
SHIFT REG DATA SHT. 1
SHIFT REG STROBE SHT. 1
BYPASS CONTROL SHT. 1
4013
U80
U77
U66
Z80 CPU
U72
LS138
U74
U76
LS74
U78
U79
HC 273
U82
'HC00
'HC04
'HC132
'LS74A
'LS74
U81
U77
'LS74
Vcc
WAIT
BUS. REQ.
GND
INT
NMI
RST
RD
WR
MI
IORQ
R235 150K
R226 27K
R220 39
R219 56
R224 1KΩ
R223 75K
R227 100K
C116 .047
D23
DZ2 IN750A 4.7V
R232 2K7
C120 100P
R207 2K7
R208 2K2
Q13
Q14
Q15
Q16
D15
R206 1KΩ
R215 270
R212 270
R214 47K
R215 20K
R210 2K2
R209 2K2
R234 6Ω2
BATTERY
D25
D20
C119 .1
R236 680
R233 680
R230 680
R217 680
R221 680
R223 680
R229 680
R216 680
Q25
Q24
Q23
Q18
Q19
Q20
Q22
Q17
R188 47
R198 47
R211 47
R228 47
R231 47
R218 47
R205 47
R191 47
L11 GRN
L9 GRN
L12 GRN
L8 GRN
L14 GRN
L16 YEL
L17 RED
L15 GRN
L7 YEL
L13 GRN
L6 RED
L10 GRN
MINOR
MAJOR
R104 1RΩ
PART OF LED DISPLAY
LED DISPLAY PN 140502 DWG 0244H
DIGIT1
DIGIT2
DIGIT3
DIGIT4
DP 10's
Q5
Q4
Q7
Q8
Q6
R195 680
R196 680
R202 680
R205 680
R197 680
R199 680
U69 2764
U78 6116LP
U64 'LS365
R194 4K7
R192 4K7
R193 4K7
STORE
EDIT
SHAD PRG
BYPASS
FRONT PANEL SWITCHES, MEMBRANE 400446
74 HC 132
I/O PCB
KB
J5
TO DS-4 CONTROLLER
78L05 VRG
R204 50
REAR PANEL
240065
DWG 3898
.5 AMP FUSE
IN4001, 6 PL.
D17
D16
D19
D18
D24
D25
C112 3300
C110 1000
C86 2200
C81 .047
C87 .047
C84 33
C93 .047
C88 33
C91 .047
C98 .047
C100 .047
C101 .047
C105 .047
C107 .047
C106 .047
C73 3300
VR3
VR4
VR9
VR7
+15V
-15V
+5V UP LED's
+5V HARM. LOGIC
ADA SIGNAL PROCESSORS, INC.
7303 D EDGEWATER, OAKLAND CA
HI SCHEMATIC, PITCHTRAX PROCESSOR. ASSBLY 78003B01A
SCALE NONE
SIZE D
DRAWING NO. 394
REV. B
DO NOT SCALE DRAWING
SHEET 3 of 3