THE 1537 VCA

Our Stereo Image Coordinator made use of a useful device. The 1537A offers some very impressive specs. Keith Brindley explains.

THERE IS always a great deal of excitement generated in electronics on the arrival or introduction of a new circuit, concept or chip, particularly if the system is potentially a field leader. The 1537A chip is just that! The specifications which the device can offer in situ are well above those of any similar preceding systems. Table 1 gives a listing of specifications, which can be obtained in the correct applications.

Parameter	Specification
Bandwidth	DC-200kHz
T.H.D., 20Hz-20kHz	0.004%
I.M.D. (SMPTE TEST)	0.03%
Noise	-90dBv, ± 1dB (worst case, unity gain)
Overshoot and Ringing	None
Slew Rate	> 10v/usec, symmetrical & constant
Input Impedance	20ΚΩ
Maximum Input Level	+20dBv
Gain	0dB (Unity)
Maximum Attenuation	>94dB
Control Voltage	0 to +10V
DC shift vs. Attenuation	≤ 5mV
Power Requirements	Regulated ± 15V at +25, -33mA

Table 1. The maximum possible specifications available from a 1537A system.

With harmonic distortion of 0.004% and a signal / noise ratio of over 90 dB the system is of course well suited to studio applications, although use in this environment is by no means its only area of involvement. The IC itself seems at first glance, somewhat highly priced at around \$22, but nevertheless, it requires few extra components to produce a VCA system of the superb quality (suggested in the specifications of Table 1) and overall represents good value for money to the amatuer and professional engineer alike.

Amplifier Or Attenuator

The term VCA is normally used as an abbreviation of the phrase Voltage Controlled Amplifier, but in its simpler modes the 1537A is, strictly speaking, a voltage controlled attenuator ie with a maximum gain of unity. The inventors do, however, stress that connection of the 1537A into the feedback loop of an amplifier (such as an op amp) produces a voltage controlled amplifier. The applications section of this article show how this can be achieved.

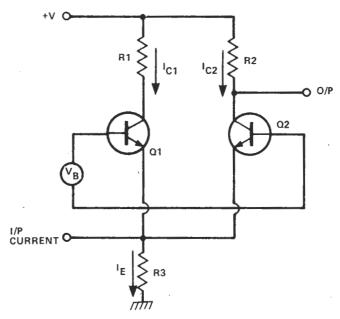


Fig. 1. A differential pair of transistors - the basis of a VCA.

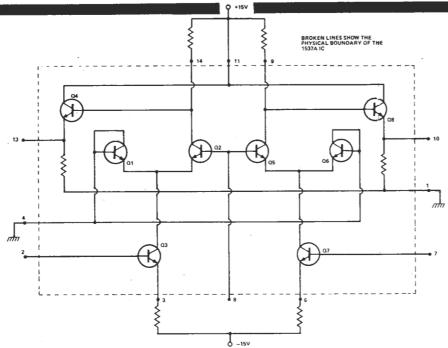
The operation of the 1537A VCA depends upon the gain control function of a differential pair of transistors as in Fig. 1. The transistors in Fig. 1 are connected at their emitters. The current through R3 ($I_{\rm E}$) is, therefore, approximately equal to the sum of their two collector currents $I_{\rm C1}$ and $I_{\rm C2}$ through R1 and R2 respectively. The relative bias voltage, $V_{\rm B}$, between the two bases determines the relative collector currents. If we now apply an input signal current to the joined emitters we obtain output signal currents through R1 and R2, the sizes of which are determined by the bias voltages. In other words, by altering this bias voltage we alter the size of the output signals.

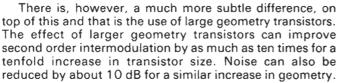
Figure 2 shows a simplified internal circuit of the 1537A chip giving pin numbers and external load and emitter resistors necessary for operation. There are two basic gain control circuits within the chip, similar to that in Fig. 1 (built around Q1, 2 and Q5,6) except for three main differences:

— the diode connection of the transistor pair not used for signal output ie Q1 and Q6, which reduces the

- distortion due to transistor gain differences.

 the addition of buffers around Q4 and Q8 to reduce loading of the output collectors of the gain transistors, in turn allowing idealised characteristics over the full gain range.
- the use of transistors Q3 and Q7 as voltage to current converters enabling the input to be applied as a voltage rather than as a current.





This leads us now to the simplest mode of operation of the 1537A using each gain control circuit individually, although the control voltage affects the gain of each circuit simultaneously (Fig. 3).

The ratio of R9 and R10 is calculated to allow a control voltage range of 10 volts (ie 0 to minus 10 V), altering the gain of the system from 0 dB to about —90 dB. The input impedance of the circuit to applied signal is low and ideally buffers should be placed before this circuit. Although this circuit does not give studio quality specifications it will, however, still produce results in the 'high fidelity' range, providing impedance matches are considered.

Figure 4 shows a circuit application which gives a higher impedance input. Also included is an inverting stage in the control voltage link which allows a voltage of 0 to \pm 10 volts to be used for controlling attenuation.

Although any operational amplifier could be used for ICs 1, 2 and 3 in the previous circuit, it should be fairly apparent that the noise, distortion and bandwidth specs of the circuit are limited to those of the op amps used.

Either of the two circuits of Figs. 1 and 2 can be adopted as the voltage controlled gain heart of a stereo system. Their outputs are about 10 dB down on the inputs so necessary amplification should be given before or after the attenuator.

Coming Up To Scratch

Now, three more developments to the circuitry can be undertaken to improve the specifications to those of Table 1. Figure 5 shows the circuit of the ideal system capable of these high specs.

Firstly, actively linearised voltage to current sources (op amp 3 and 4 in Fig 5) improve distortion figures when using a wide range of input signal voltages.

Secondly, parallelling of the two individual gain control circuits (ie the same input signal is fed to both devices at

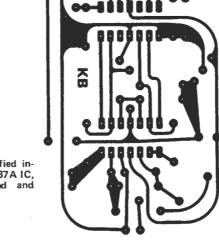


Fig. 2. A much simplified internal circuit of the 1537A IC, showing external load and emitter resistors.

their inputs and mixed at their outputs) gives a 3 dB improvement in S/N ratio.

Finally, a technique is utilised which is complementary to the previous development of parallel devices, whereby the same input is applied to both gain control devices but 180 degrees out of phase. The two outputs are combined in a differential amplifier to give a single ended output. The differential amp is formed around op amp 6. This technique has the effect of reducing DC shift caused by bias and control voltages and with careful adjustment of RV1, the minimal DC shift now left at the output can be reduced even.

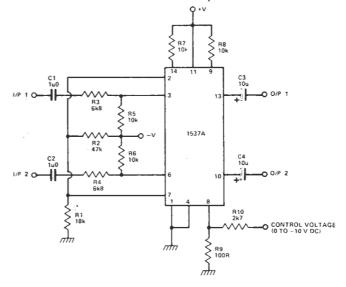
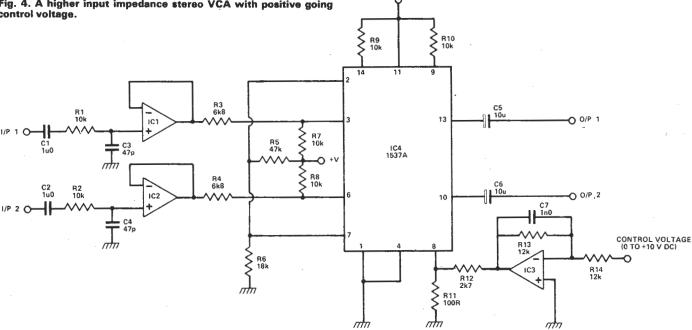


Fig. 3. The simplest mode of operation of the 1537A — a low input impedance stereo VCA with a negative going control voltage.

Fig. 4. A higher input impedance stereo VCA with positive going control voltage.



further to near (if not actually) zero. The prototype circuit shown, upon testing, actually gave no DC shift at all (or at least none measureable on our test equipment).

The complete circuit can be used as an exceptionally high quality VCA whose signal input can be anything from a few millivolts through to about 20 volts pk to pk without distortion. The lack of DC blocking capacitors at the input and output means that the system can be used to control a DC voltage applied to the input. AC signals up to well over 200 kHz are easily catered for, due to the system's wide bandwidth.

The overlay in figure 6 shows the component layout on printed circuit board of the circuit. As far as we know this article is the first of its kind to present a circuit in a form where "experimenters" can benefit easily and directly from the written text while simultaneously using the device in a tried and tested form.

Construction

If the circuit board layout is followed then there should be no problems. IC holders are advisable though by no means necessary. RV1 should be a good quality type (cermet), to assist in setting up the output offset shift to zero, cheaper quality presets can sometimes be tricky to adjust in low voltage DC applications of this nature. Op amps 1 to 4 in the circuit are combined in IC1 and can be of a wide range of types from a quad 741 type (3403) upwards. Obviously, if you wish to obtain the best specs the quality of the op amps are critical. LF 347 or TL 074 will give the best results.

Similarly op amps 5 and 6 are included in IC3 and LF 353 or TL 072 are of optimal quality.

Setting Up

The system should work without any adjustment for an AC signal and varying the control voltage from 0 to 10 volts should give total control over the output amplitude. Some setting up will be required if the input is to be DC, though.

This is best achieved by earthing the input. Measure the output voltage using a high impedance voltmeter (it should only be the order of a few millivolts). Adjust RV1 until a complete sweep of control voltage ie from 0 to 10 volts produces only minimal change in DC output voltage. The circuit is now completely set up to accept an input signal in the frequency range DC to 200 kHz. At minimum attentuation the system operates as a unity-gain wide range, high quality buffer, with a reasonably high input impedance and low output impedance. Variation of the DC control voltage over the range 0 to 10 volts will produce over 90 dB of attenuation of the output signal.

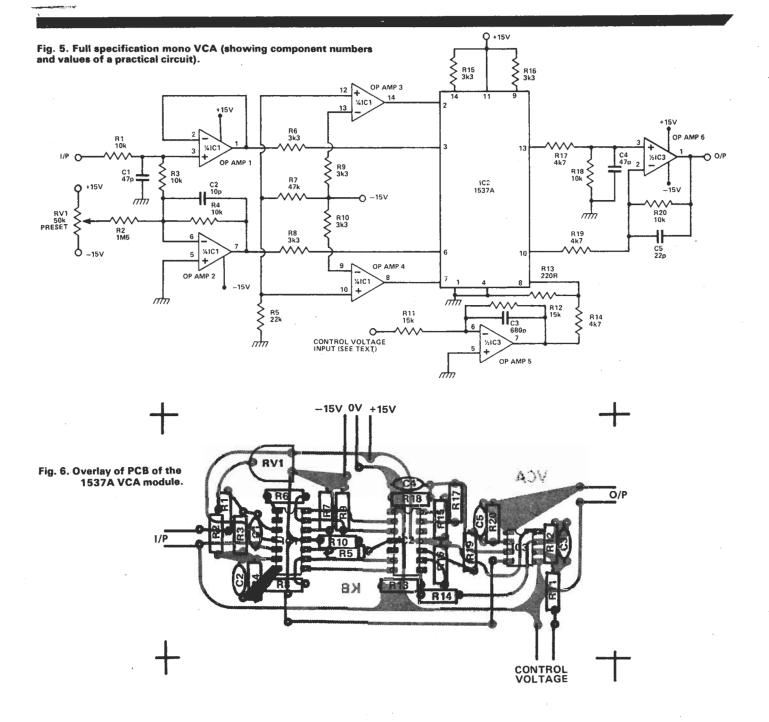
If an overall gain is required in the circuit, resistors R18 and R20 can be changed as in Table 2.

GAIN	R18 & R20
0dB	10k
6dB	22k
10dB	33k
15dB	5 6 k

Table 2. The values of R18 and R20 to give the required overall gain in the VCA system of Fig. 5.

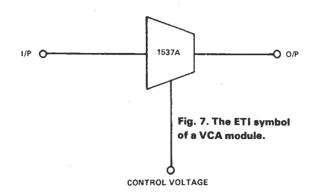
The control voltage range of 10 volts can be altered as required simply by changing the ratio of resistors R13 and R14 to suit.

To our knowledge, there is no officially recognised standard symbol for a VCA and rather than redraw the whole circuit of figure 5 upon every reference to the circuit we thought it better to invent a symbol for the purposes of this article. A horizontal trapezoid shape appeared to be the ideal symbol, as shown in Fig. 7. It symbolizes the system as a modular buffer amplifier, whose output (symbolized by the top line), decreases as the control voltage (the bottom line), increases. We shall use the modular sumbol of a VCA whenever reference is made to the circuit of Fig. 5, although any VCA module of another design should function in the applications which we give.



Use of the 1537A system module as a DC controlled analogue gate can produce many effects. Amplitude modulation of the signal occurs and the usual associated effects are observed. For instance, in Fig. 8 we can see a simple but high quality tremelo unit. Transistors Q1 and Q2 are connected as a phase shift oscillator and buffer, with speed and depth controls whose varying DC output is connected directly to the control port of the 1537A module. The frequency range of the oscillator is approximately 2 to 5 Hz. Altering the values of all three capacitors will change the main frequency, though that stated will give the best results.

The control voltage in the last application was varied as a sine wave of course, but there is no reason why other waveforms eg square, could not be used for control purposes. Figure 9 shows a 555 operating in the astable



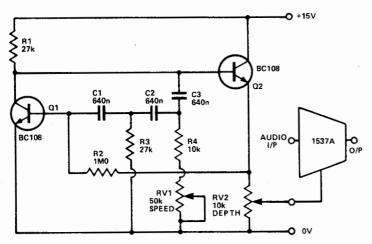


Fig. 8. A simple tremolo circuit.

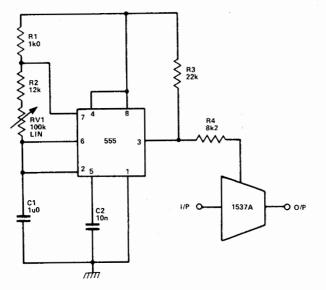


Fig. 9. Ring modulator.

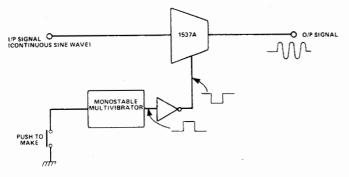


Fig. 10. A simple system enabling the construction of a tone burst generator.

mode with a frequency range of approximately 5-50 Hz. The output signal will be modulated with the square wave and the overall product is a computerized type sound if a vocal signal is applied to the 1537A module.

This square wave control can be taken one stage further if the control voltage is the output from a monostable as in Fig. 10. A tone burst generator can be very easily constructed with this mode of operation. In a tone burst,

generator, a rectangular envelope 50-500 uS long is formed around a single sine wave frequency of normally 1 kHz. Tone burst generators are useful for testing the transient response of speakers, A push to make switch is used to provide the trigger to fire the multivibrator, producing the correct length pulse which in turn is inverted to form the control voltage pulse, applied to the control port of the 1537A.

The previous applications have all used automatic waveform control of the applied signal to produce the required attenuation characteristics, but this is not a necessary trait. The control voltage can be simply tapped off a variable resistor having the maximum control voltage range (ie 10 volts) across it. In this way, altering the position of the wiper alters the attenuation of the applied signal. The pot acts quite simply as a volume or level control. Ordinary non-DC volume controls can suffer from pick-up problems because the signal itself is being rotated through the pot. As only DC is applied to the pot in this application no pick-up can occur and the control can be remotely mounted from the module with no screened cable being necessary. Figure 11 shows such a volume control.

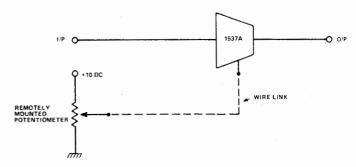


Fig. 11. Remotely (wire-linked) controlled volume control.

This remote control facility can be utilised in an audio mixer which includes remote faders for each channel. Figure 12 shows the general idea of such a circuit, An op amp is used as a summing amplifier into which the output of each channel's VCA is fed and mixed. The mix is relative to the control voltage applied from the remote faders to each VCA. The circuit allows for up to N inputs, where N to practical limits will probably be a maximum of about 12, but with careful layout techniques, there is no reason why this cannot be increased further.

Figure 13, shows an interesting outline to enable digital control of the VCA, say from a computer link. In order that the computer can operate in real-time, ie control of the VCA is not just its only job, it is necessary for the interface to provide a latch for the digital word. The output of this latch is changed to a linear DC voltage by the D/A (digital to analogue) convertor whose output is taken to the control port of the VCA.

The digital latch, once set by a strobe pulse, provides the facility that after the volume required has been found, the computer is free to perform other tasks, When the volume is to be altered, the latch is reset to the new digital input.

The last six applications of the 1537A VCA system have simply shown methods of providing a control voltage (automatically, manually or digitally) to control the module in its function as an analogue gate. The following section begins with the assumption that the control voltage is already present, perhaps by one of the previous methods.

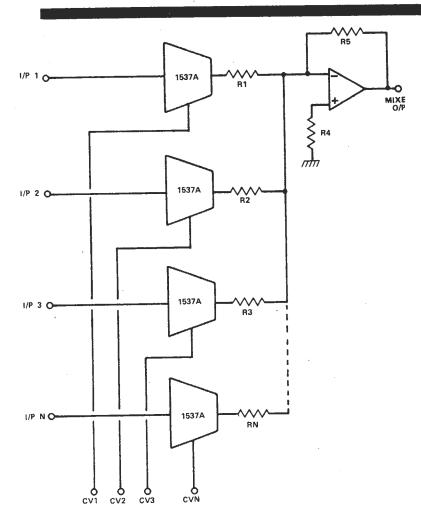


Fig. 12. High quality, remote fader controlled mixer.

Applications

Consequently the next few circuits show the system in a much more versatile role — not just as an analogue gate, but one in where the system itself becomes part of a larger system. Figures 14 and 15 give details of circuit in which the 1537A module is used in the feedback loop of conventional operational amplifiers to allow voltage controlled amplifiers to be constructed. The resistance values used give gains of approximately 1 to 100 over the VCA control voltage range and an inverting VCAmp and a non-inverting VCAmp can be easily built as shown.

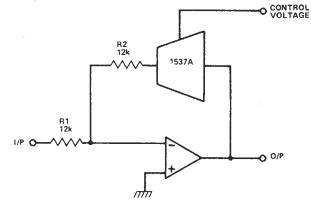


Fig. 14. A non-inverting controlled attenuator.

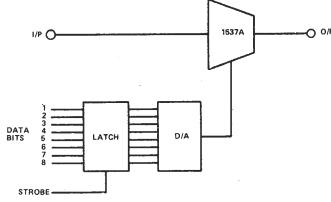
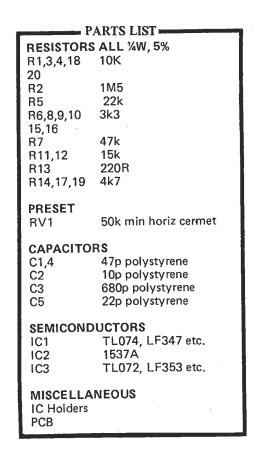


Fig. 13. Main components of a digitally controlled attenuator.



BUYLINES

The Aphex 1537A is available only from Octopus Audio, Suite 315, 69 Sherbourne St, Toronto, Ontario M5A 3X7. Cost is \$22.00 each postpaid (Ontario residents add 7% P.S.T.).

Note the Motorola MC1537 and its second source variants will not work. The Motorola device is a dual 709 op amp.

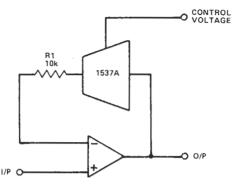


Fig. 15. An inverting voltage controlled amplifier.

A voltage controlled resistor is shown in the application of figure 16. The apparent resistance, R1, is given approximately by the formula

$$\frac{R1}{1-A}$$

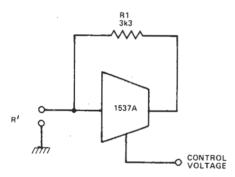


Fig. 16. A voltage controlled variable resistor.

where A is the gain of the VCA module (remembering that it has a maximum gain of unity). The value of R1 shown gives an apparent voltage controlled resistance of 7 k to 100 k over the ten volt control voltage range.

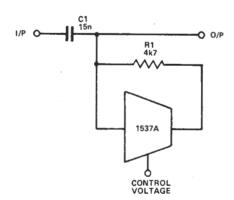


Fig. 17. A voltage controlled High Pass Filter.

The effect of a VCR (voltage controlled resistor) is used in the final two applications as the control element in filter circuits. Figure 17 shows a simple voltage controlled high pass filter. The component values shown filter out all frequencies below the variable limit of 1-2 kHz. Adjustment of the control voltage alters the lower cutoff point.

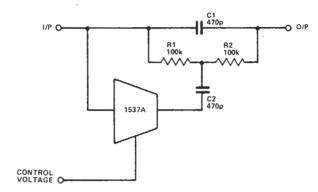


Fig. 18. A voltage controlled Band Reject (Notch) Filter.

Figure 18 consists of the circuit of a voltage controlled band reject or notch filter whose depth of notch is adjusted by the control voltage. The component values shown set the frequency at about 300 Hz and depth of notch is variable from 0 dB to about —15 dB.

Conclusions

The applications given in this article show the 1537A chip to be a very versatile device. It is remarkably easy to work with, a fact which is borne out by the quality (in technical terms) of the circuitry in the breadboarded fashion of our experimental design work, let alone in the modular fashion allowed by the use of our PCB layout.



THIS IS A VERY GOOD LOGIC STATE ANALYSER AND IT ALSO SOLVES CROSSWORD PUZZLES.