## Digitally programmable instrumentation amplifier offers autozeroing

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The current trend in advanced instrumentation amps is to use no external resistors. In these amplifiers, a gain-control word, comprising

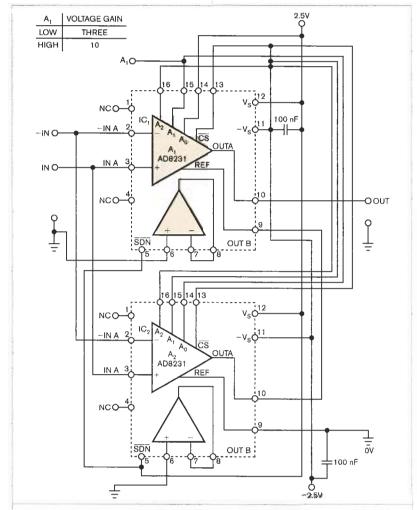


Figure 1 The autozeroed instrumentation amp, digitally programmable for voltage gains of three and 10, can help you to overcome the current inavailability of monolithic ICs for this task.

a binary-coded one, sets the voltage gain. Several integer gains within one to 1000 are currently available; however, this range does not yet include a gain of three. Although external-resistor-free amplifiers with a gain of three are available, they are neither instrumentation amps nor autozeroed devices (Reference 1). These features are essential in applications requiring accurate processing of low-level voltages. You can use the circuit in Figure 1 for applications requiring instrumentation amps having voltage gains of three or 10 and the ability to process voltages as low as 1 mV.

This design achieves a voltage gain of three by using the "algorithm" of 3=2+1. The circuit comprises two units of the Analog Devices (www. analog.com) digitally gain-programmable, autozeroed AD8231 instrumentation amp. These ICs have voltage gains that are programmable as powers of two—that is, one, two, four ... 128 (Reference 2). Amplifier A<sub>1</sub> in IC<sub>1</sub> is preset to provide a gain of two, and auxiliary amp A, in IC, is preset to a gain of one. The noninverting and inverting inputs of A, and A, connect together. The output of A<sub>2</sub> connects with reference input REF, and reference input REF, serves as a freely usable reference. You can thus calculate the output voltage  $\begin{array}{l} \text{as V}_{\text{OUT}} \! = \! \text{V}_{\text{OUT1}} \! + \! \text{V}_{\text{REF1}} \! = \! \text{V}_{\text{OUT1}} \! + \! \text{V}_{\text{OUT2}} \\ = \! 2\Delta \text{V}_{\text{IN}} \! + \! \Delta \text{V}_{\text{IN}} \! = \! 3\Delta \text{V}_{\text{IN}}, \text{ where } \Delta \text{V}_{\text{IN}} \text{ is} \end{array}$ the input-difference voltage.

Similarly, you can achieve a voltage gain of 10 according to a symbolic formula of 10=8+2. This time, A, has a voltage gain of eight, and A, has a gain of two. Using Reference 2, you can derive that, for gains of both three and 10,  $A_{1A1} = A_{0A2}$ . Therefore, the gain-control pins connect and remain low for a gain of three, and the high at

## designideas

these pins sets the gain to 10. Note that three approaches the square root of 10, or approximately 3.16. You can therefore consider it as roughly the geometric center of a decade. EDN

## REFERENCES

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