

Voltage Feedback Is From Venus

Current Feedback Is From Mars

1-38

Op Amp Architectures

Voltage Feedback



- Both Inputs are High Impedance
- High Forward Voltage Gain A, (V_o/V_d)
- Low Output Impedance
- · Error Signal is Vd
- Constant Gain x Bandwidth Product





- Positive Input is High Impedance
- Negative Input is Low Impedance
- High Forward Transimpedance Gain Z_T, (V_o/I_o)
- Low Output Impedance

Current Feedback

- Error Signal is In
- · Bandwidth Independent of Gain (almost)

The Art of Analog 39

Here is a brief summary of the differences between voltage and current feedback op-amps. Voltage feedback op-amps employ a differential pair configuration such that the impedance at each input is high. The inputs of a current feedback (CFB) op amp are unbalanced as seen in the schematic on the right. The non-inverting input exhibits the high impedance normally associated with the voltage feedback (VFB) op amp. However, the inverting input of the CFB present a low impedance to the outside. This is because there is a unity gain buffer between the negative and positive inputs to the CFB. We shall see in the following foils that the error signal employed is also different. The error signal for the VFB is the delta voltage between the inverting and non-inverting input. This characteristic accounts for the name, current feedback op-amp. A performance characteristic associated with this topology is the situation that the bandwidth of the amplifier is independent of the gain of the device. This contrasts with the VFB amplifier whose gain-bandwidth product is constant. This familiar curve slopes downward to the right for the VFB, as will be seen. Although the execution in internal circuitry is different, from the user viewpoint either may be used in the vast majority of applications.

VFB Amplifier – Feedback Activity



This foil reviews the feedback action of the VFB op-amp. Seen in the input stage circuitry are, from top to bottom, a current source of magnitude 2Ie, a differential pair Q1 and Q2, and a current mirror formed by Q3 and Q4 such the the current Ic1 is mirrored to Q4 causing both Ic1 current and Ic2 current to appear at the collector of Q4. The polarities are such that the delta current (Ic1-Ic2) appears as the input current to the next (2nd) stage of the amplifier. This is the classic VFB input stage topology.

Under the assumption of zero input voltage between Vin- and Vin+, Ic1 and Ic2 are equal and no delta current flows to the ouptut, hence no signal is transferred. A non-zero input signal on either input causes a delta current to flow to stage two. This signal is amplified in the interstage and the output stage of the amplifier. Feedback from the output to the inverting input closes the loop.

CFB Amplifier – Feedback Activity



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In a CFB op-amp, the rebalancing action caused by loop closure happens at the node Vn. Upon addition of feedback from the output to Vin-, current is added or subtracted from the In current, that is the current at the output of the input buffer between Vin+ and Vin-. In other words, the external network causes an inbalance between the currents of the push-pull pair, Q1 and Q2. The feedback current signal then functions to re-balance the current at Vn causing the system to faithfully reproduce at the output, the input signal with or without gain as the case may be. This current action in restoring equilibrium produced the designation current-feedback op-amp. To reiterate: in the voltage feedback op-amp, circuit equilibrium is restored based upon the difference voltage, Vin+ and Vin-. In the current feedback op-amp, circuit equilibrium is restored based upon the current condition at the Vin- node. Also note that the buffer currents i1 and i2, are mirrored at an increased amplitude by current mirrors Q5, Q6 and Q7, Q8. This will be important in the slew rate discussion that follows.

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Slew Rate Analysis

Voltage Feedback



All the current for charging and discharging C comes from current source I. This is the slew rate limitation in VFA.

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Current Feedback



The current needed for slewing the node at Z comes from current mirrors G*I1 and G*I2. Larger step inputs causes greater I1-I2 and produce larger output slew rates.

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One of the two important differences between VFB's and CFB's is the capability of the CFB to produce higher slew rates than the VFB for the same quiescent current and process conditions. The other difference is the relative independence of gain and bandwidth in the CFB amplifier. The VFB op-amp is limited in slew rate by the ability of that circuit configuration to charge and discharge the interstage capacitor C in the voltage feedback schematic on the left. The current source I which supplies the balancing currents to the input differential pair, is the sole source of current available to charge and discharge C when the circuit is under large signal, slew rate conditions. Since a current source supplies the same current under all conditions, charging and discharging are limited to I, regardless of the magnitude of the input signal applied to the differential pair.

In the case of the CFB amplifier Ceq, the gain node capacitance is charged by the balanced current sources CM which produce current Iz. This becomes the charging and discharging current for Ceq. However, this current is not limited by the differential pair current source current as in the case of the VFB. Rather, Ceq is charged and discharged by the delta current Iz supplied by the current mirror. These matched current mirrors, CM, CM, are responding to the magnitude of In which is driven by the input signal. Thus, the charging current for Ceq, which becomes the slew rate limitation in a CFB op amp, is proportional to the input signal and would be theoretically limited only by the size of the current mirror transistors.